



# i.MX 7ULP Family, Ultra-Low-Power with Graphics

## i.MX7ULP

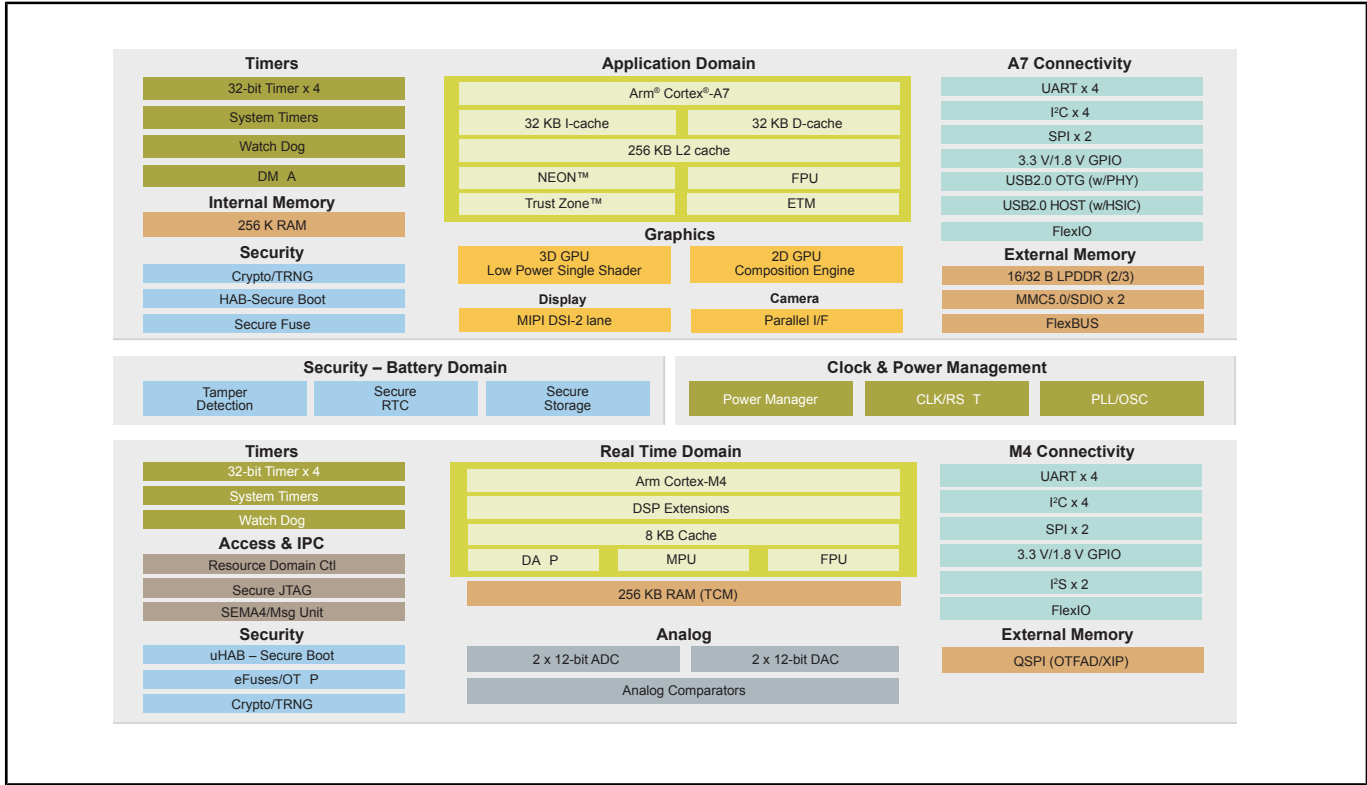
Last Updated: Jan 16, 2023

The i.MX 7ULP family of processors represents NXP's latest achievement in Ultra-Low-Power processing for use cases demanding long battery life. Targeted toward the growing market of portable devices, the i.MX 7ULP family of processors features NXP's advanced implementation of the Arm® Cortex®-A7 core, the Arm Cortex-M4 core, as well as a 3D and 2D Graphics Processing Units (GPUs). The i.MX 7ULP family provides up to 32-bit LPDDR2/LPDDR3 memory interface and a number of other interfaces for connecting peripherals, such as WLAN, Bluetooth®, GPS, displays and camera sensors.

i.MX 7 applications processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

For additional information and sample availability, contact [NXP Support](#).

# i.MX 7ULP Applications Processors Block Diagram



View additional information for [i.MX 7ULP Family, Ultra-Low-Power with Graphics](#).

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.