

Errata to MCIMX31 and MCIMX31L Applications Processors Reference Manual, Rev. 2.4

This document describes corrections to the *MCIMX31 and MCIMX31L Applications Processors Reference Manual*, Revision 2.4. For convenience, the section number and page number of the errata item in the reference manual are provided.

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19.3.3.1, 19-19

Insert the following note after the Table footnote of Table 19-10, “PWDT Bit Field Encoding”.

NOTE

When PWDT is enabled, and **lpmd** or **dvfs** requests are required by the system, PWDT must be disabled before the requests are granted. PWDT can only be re-enabled after these modes are exited.

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