ERRATA SHEET

Date: **March 4, 2002, 2001**
Document Release: **Version 1.0**
Devices Affected: **P87LPC767**

This errata sheet describes both the functional deviations and any deviations from the electrical specifications known at the release date of this document.

Each deviation is assigned a number and its history is tracked in a table at the end of the document.

2002 Mar 04

Philips Semiconductors

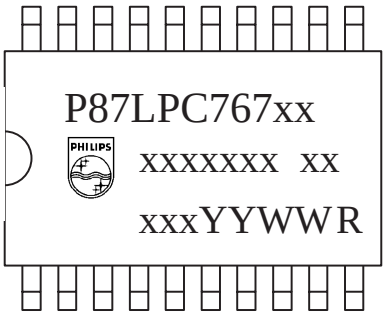
PHILIPS

Low pincount 8-bit microcontroller Errata Sheet

P87LPC767

IDENTIFICATION:

The typical P87LPC767 devices have the following top-side marking (SO 20 package shown):



The last letter in the third line (field 'R') will identify the device revision. This Errata Sheet covers the following revisions of the P87LPC767:

Revision Identifier (R)	Comment
no revision letter or dash ('–')	_

Field 'YY' states the year the device was manufactured. Field 'WW' states the week the device was manufactured during that year.

FUNCTIONAL DEVIATIONS OF P87LPC767

IO.1: **Certain Port 0 pins stuck to low level if ports are configured to be low after reset in UCFG1**

Introduction: Bit 5 of User Configuration Byte 1 (UCFG1.5; bit PRHI) is used to select whether the port pins of the P87LPC767 will be in a low state or a high state after reset.

Problem: If the port pins are configured to be low during and after reset (bit PRHI = 0), the following port pins will stay low even when they are configured to output high level by software after the device reset sequence. The port pins affected are:
- P0.0
- P0.1
- P1.6
- P1.7

Workaround: To avoid these pins to be stuck to 0 after reset, configure bit PRHI in register UCFG1 to 1. Please note that all port pins will then be in a high state after reset. After the reset sequence, 0's can be written to the port pins that need to be low.

ELECTRICAL AND TIMING SPECIFICATION DEVIATIONS OF P87LPC767

No known deviations at the release of this document.

ERRATA HISTORY - FUNCTIONAL PROBLEMS

Functional Problem	Short Description	problem occurs in device revision
IO.1	Certain P0 pins stuck to low level if ports are configured to be low after reset in UCFG1 (= PWM.3 on LPC768)	no revision letter or dash ('–')

ERRATA HISTORY - AC/DC DEVIATIONS

AC/DC Deviation	Short Description	problem occurs in device revision
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