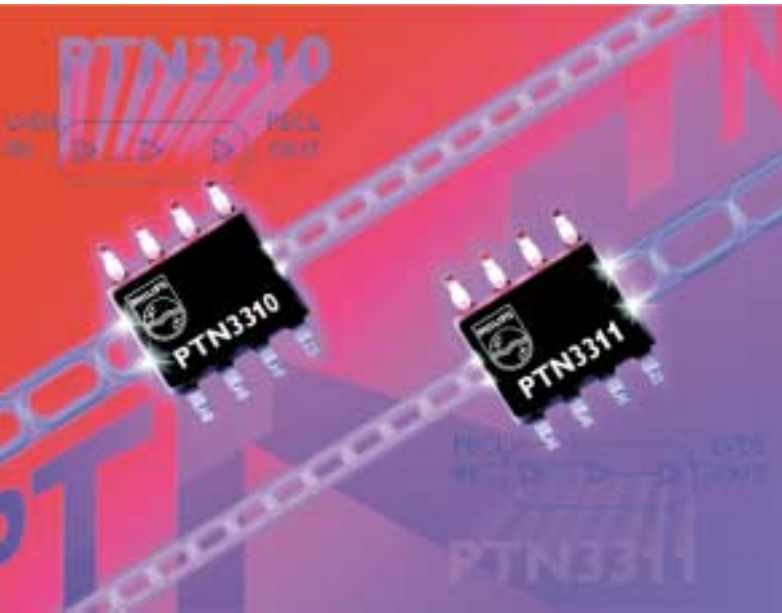


Repeatability and Dependability in Logic Translation

PTN3310/PTN3311

High-Speed Serial Logic Translators



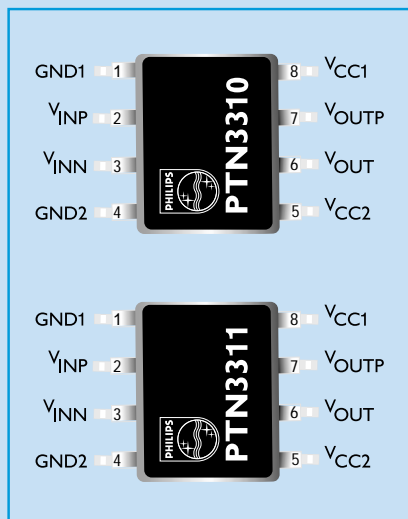
PTN3310 – LVDS to PECL Translator
PTN3311 – PECL to LVDS Translator

PTN3310 LVDS-PECL Logic Translator

- High speed (800 Mbps)
- Single 3.3V supply
- SO-8 package

PTN3311 PECL-LVDS Logic Translator

- Identical package and pin-out



FUNCTIONAL DESCRIPTION

The High-Speed Logic Translator provides a point solution that addresses the various interface logic requirements of Optical Transceiver Modules. The products offer a compact translation between LVDS and PECL high speed serial data lines. This provides the end users a simple way to mix or match optical transceiver modules from various vendors to legacy or new ASIC solutions.

The High-Speed Serial Logic Translator comes in two translation choices to allow mixing of LVDS and PECL input/outputs. The product is offered in a small, convenient, 8-pin package.

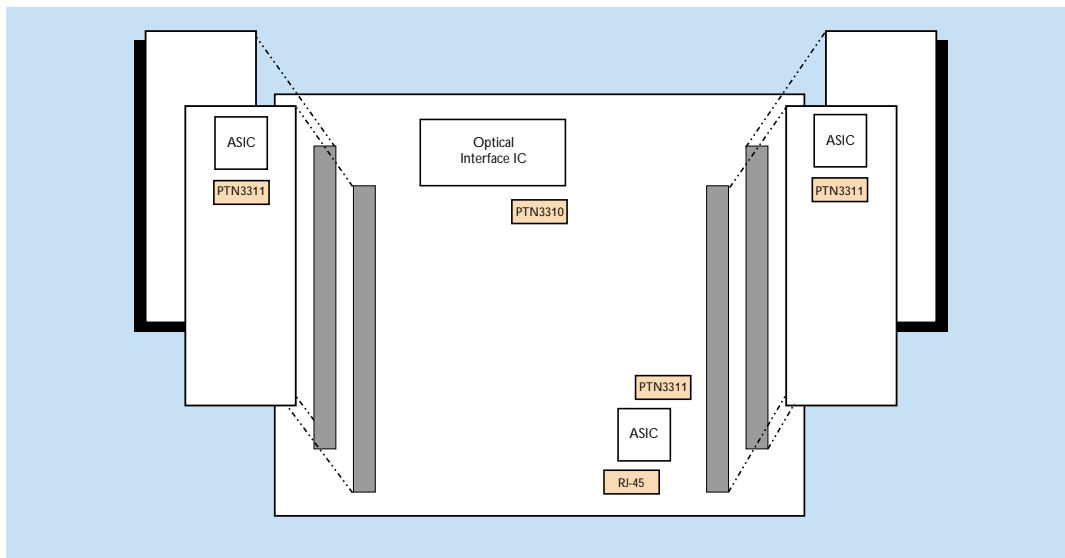
ADVANTAGES

- Ease of design
- Low cost solution
- Small package size (low impact on design layout)
- Signal Integrity advantages (regeneration of signal)
- Line Impedance matching easily achieved
- Repeatable application reduces engineering time
- DC-free data coding not required
- Operating Supply Voltage 3.0 – 3.6V
- Maximum throughput data rate @ 800 Mbps typical
- 2ns propagation delay

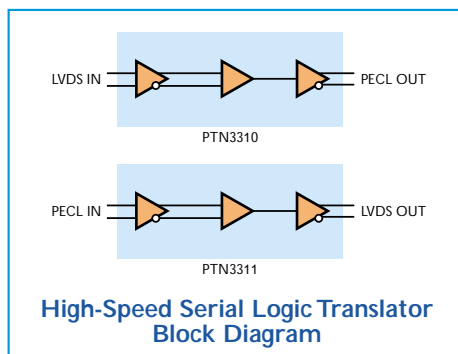
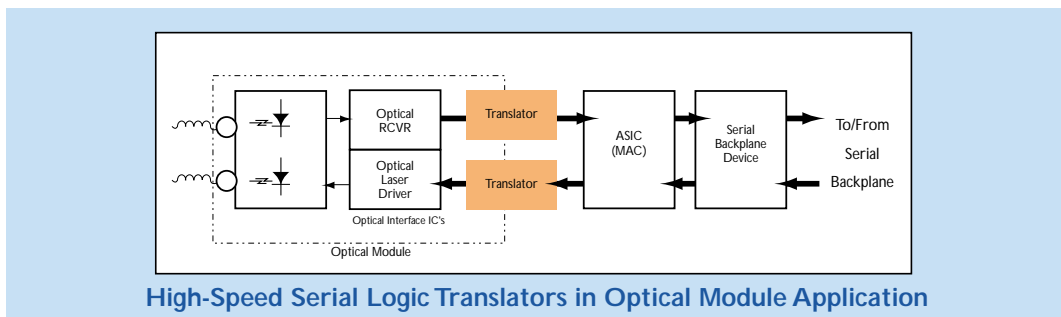
APPLICATIONS

- High-speed Networking and Telecom applications
 - ATM
 - SONET/SDH
 - Switches
 - Routers
 - Add-drop multiplexers

High-Speed Serial Logic Translators



Examples of the application of high-speed, low-voltage LVDS and PECL Translators in the Telecom and Datacom world can be found in Switches, Routers, Hubs, Add/Drop Multiplexers and Network Processors. PTN3310 and PTN3311 may be used to connect point-to-point within a single board, card-to-card across a backplane, or rack-to-rack via a cable connection.



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