

### LPC3000 block diagram

Ext. Memory I/F (NAND, Memory Stick, SD, and DRAM)	E-ICE/RTM Interface Embedded Trace Buffer
64 KB SRAM	Interrupt Controller
DMA	
Vector Floating - Point Coprocessor 32-bit ARM926EJ-S Bus Matrix	
D Cache 32K	I Cache 32K
Power management, RTC, WDT, PLL	
10-bit A/D converter	USB 2.0 full speed/host/OTG
Timers with Capture/Compare	2x PWM
2 x I <sup>2</sup> C	
UART 0-7 IRDA	2x SPI
I/O ports (45)	Keyscan

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