



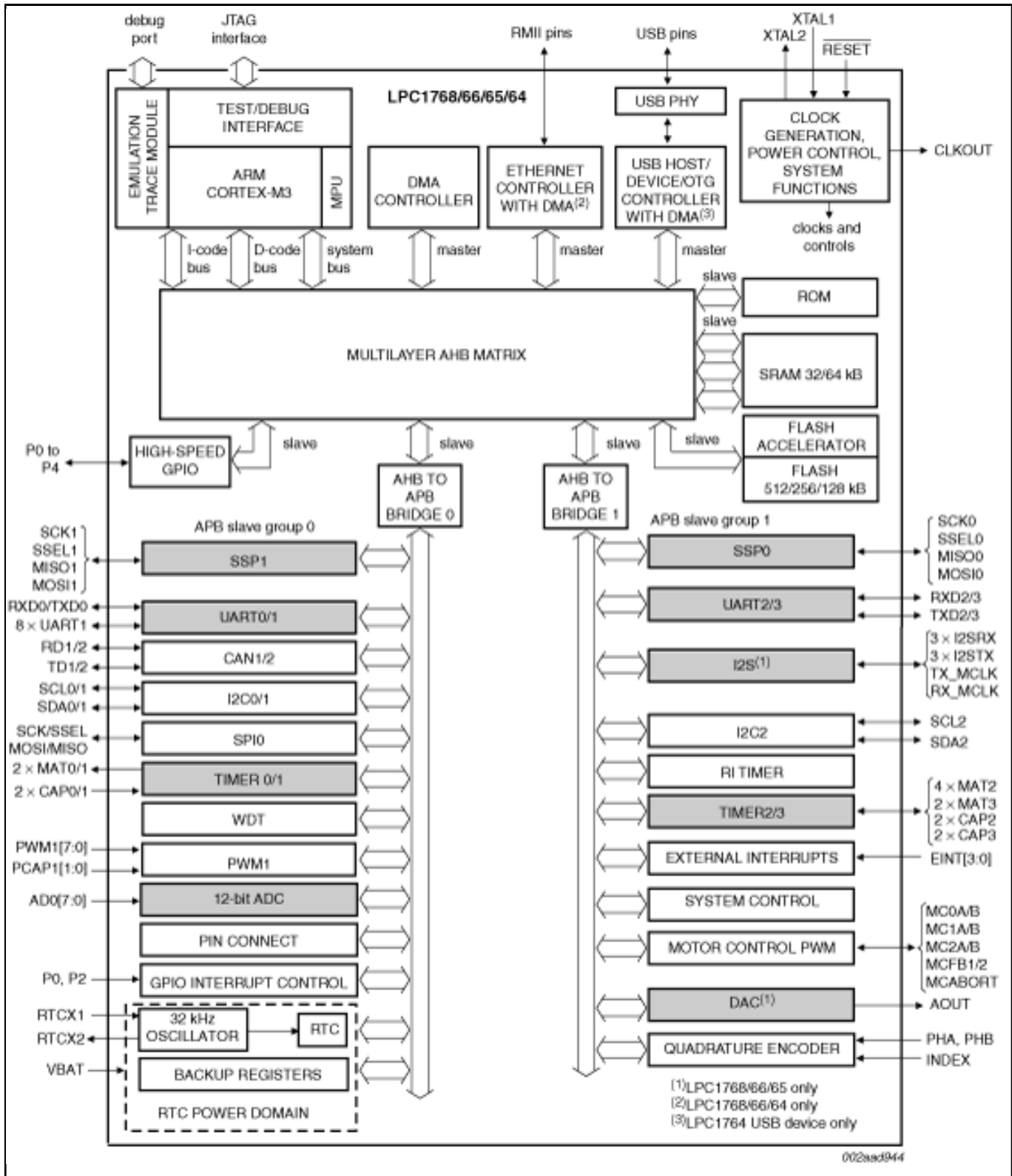
128kB闪存、32kB SRAM、以太网、USB、LQFP100封装

LPC1764FBD100

Last Updated: Apr 8, 2022

LPC1764是一种适合嵌入式应用的Cortex-M3微控制器，具有较高的整合度和低功耗，运行频率为100 MHz。功能包括128 kB Flash存储器、32 kB数据存储器、以太网MAC、USB设备、8通道DMA控制器、4个UART、2个CAN通道、3个SSP/SPI、3个I2C、8通道12位ADC、电机控制PWM、正交编码器接口、4个通用定时器、6输出通用PWM、带独立电池供电的超低功耗实时时钟以及最多70个通用I/O引脚。LPC1764与100引脚的LPC2368 ARM7 MCU引脚兼容

Block diagram: LPC1764FBD100, LPC1765FBD100, LPC1766FBD100, LPC1768FBD100
Block Diagram



View additional information for [128kB 闪存](#)、[32kB SRAM](#)、[以太网](#)、[USB](#)、[LQFP100封装](#)。

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