

4-Bit GTL-to-GTL Buffer

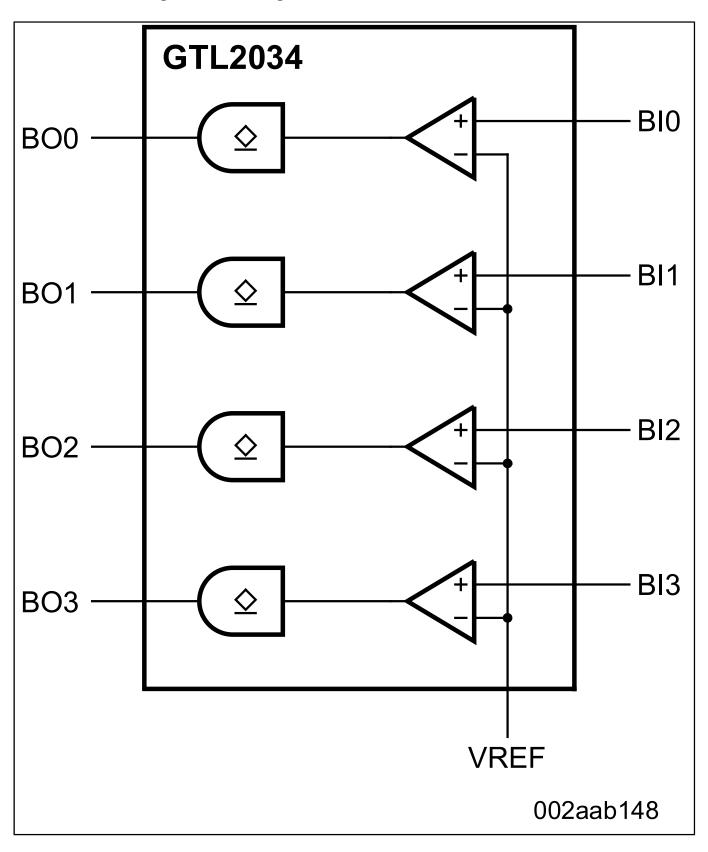
GTL2034PW

Last Updated: Mar 15, 2024

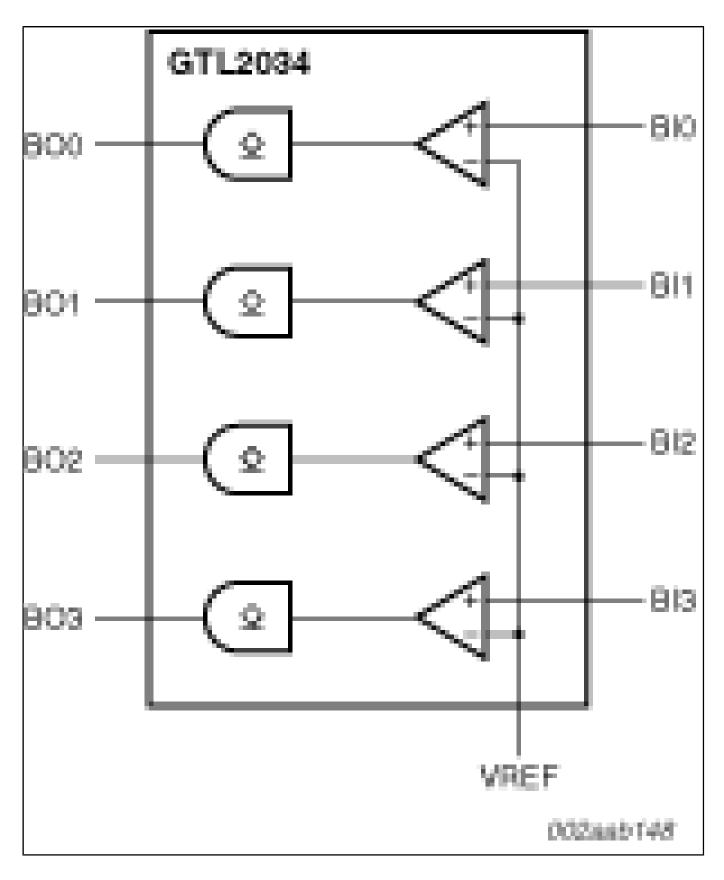
The GTL2034 is a 4-bit GTL-/GTL/GTL+ bus buffer.

The GTL2034 GTL inputs and outputs operate up to 3.6 V, allowing the device to be used in higher voltage open-drain output applications.

GTL2034 Block Diagram Block Diagram



Block diagram: GTL2034PW Block Diagram



View additional information for 4-Bit GTL-to-GTL Buffer.

Note: The information on this document is subject to change without notice.

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