



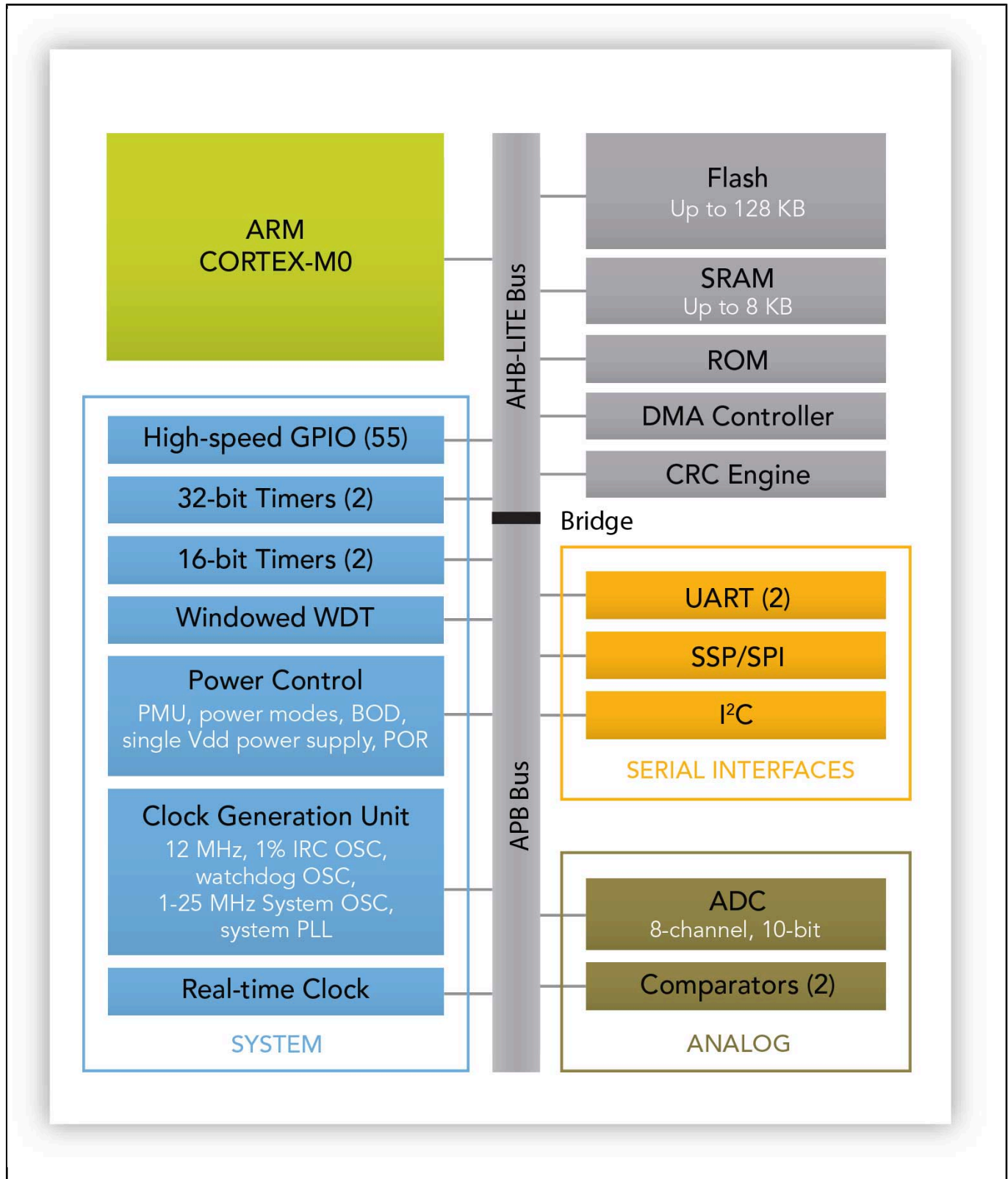
96 KB Flash, 8 KB SRAM, LQFP48 Package

LPC1226FBD64

Last Updated: Dec 2, 2022

The LPC1226FBD64 is an Arm® Cortex®-M0 based microcontroller for embedded applications featuring a high level of integration and low power consumption. The LPC1226FBD64 operates at CPU frequencies of up to 30 MHz and includes 96 kB of Flash memory and 8 kB of data memory. The peripheral complement of the LPC1226FBD64 includes a DMA controller, a CRC engine, one Fast-mode Plus I2C interface, one RTC, one SSP/SPI interface, two UARTs, four general purpose timers, a 10-bit ADC, two comparators, and up to 55 General Purpose I/O (GPIO) pins. The LPC1226FBD64 is available in LQFP64 package.

LPC122x Block Diagram Block Diagram



View additional information for [96 KB Flash](#), [8 KB SRAM](#), [LQFP48 Package](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.