



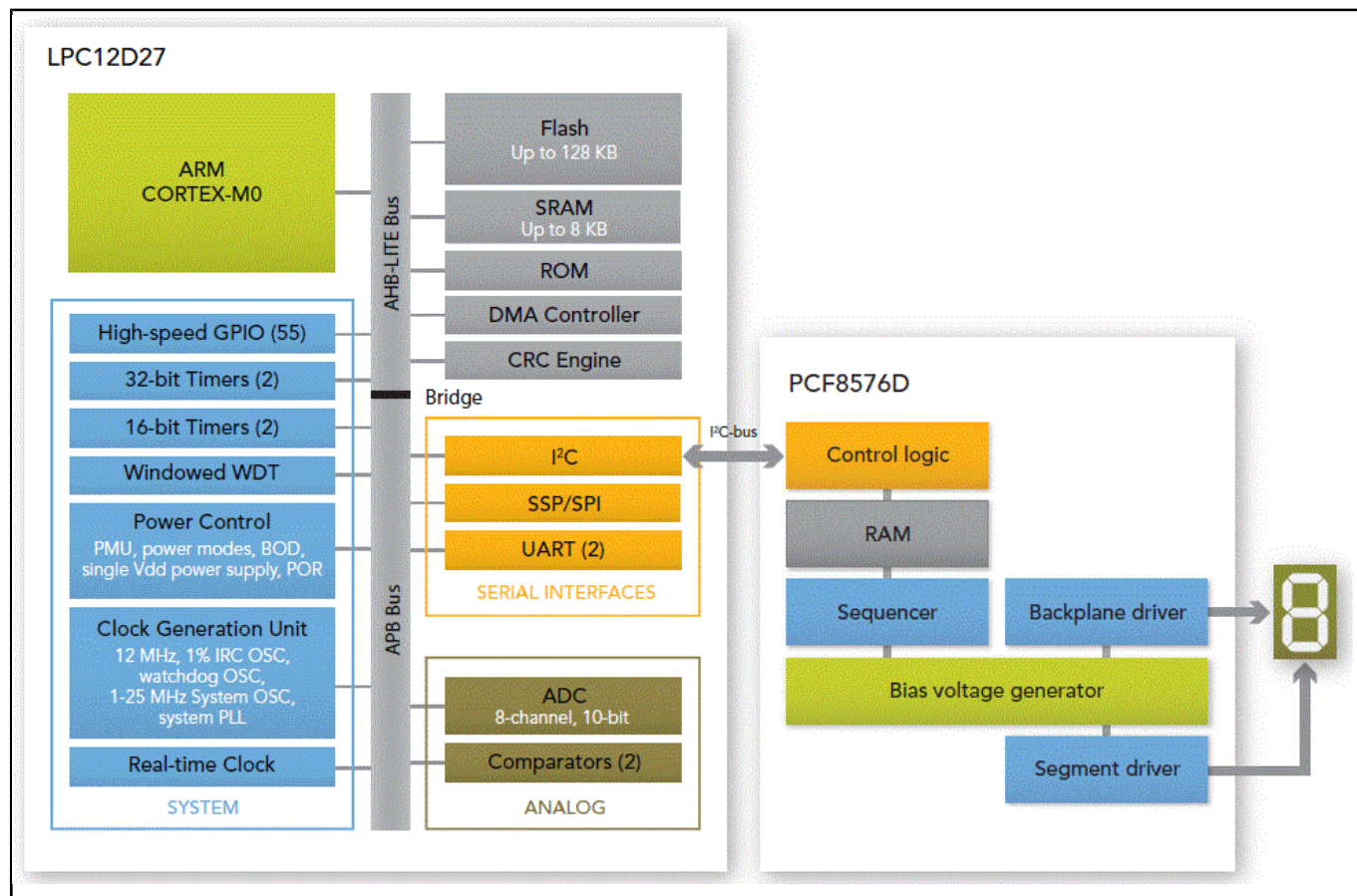
Robust and Reliable 32-bit Microcontroller (MCU) based on Arm® Cortex®-M0 Core

LPC12D27FBD100

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The LPC12D27FBD100 is an Arm Cortex-M0 based microcontroller for embedded applications featuring a high level of integration and low power consumption. The Arm Cortex-M0 is a next generation core that offers system enhancements such as enhanced debug features and a higher level of support block integration. The LPC12D27FBD100 is a dual-chip module consisting of a LPC1227 single-chip microcontroller combined with a PCF8576D Universal LCD driver in a low-cost 100-pin package. The LCD driver provides 40 segments and supports from one to four backplanes. Display overhead is minimized by an on-chip display RAM with autoincrement addressing.

LPC12D27 Block Diagram Block Diagram



View additional information for [Robust and Reliable 32-bit Microcontroller \(MCU\) based on Arm® Cortex®-M0 Core](#).

Note: The information on this document is subject to change without notice.

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