



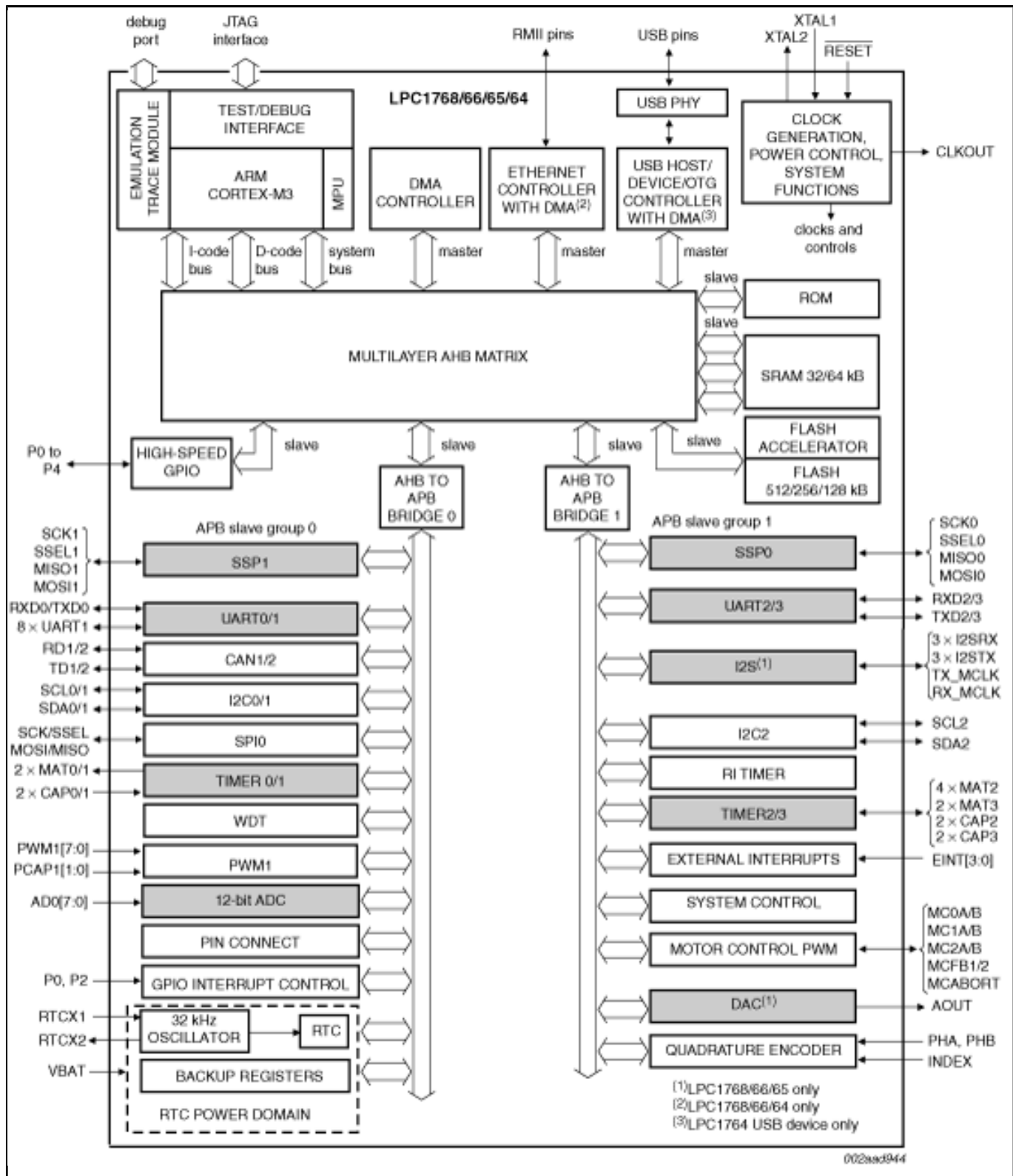
256 KB Flash, 64 KB SRAM, USB, LQFP100 Package

LPC1765FBD100

Last Updated: Jul 31, 2023

The LPC1765 is a Cortex-M3 microcontroller for embedded applications featuring a high level of integration and low power consumption at frequencies of 100 MHz. Features include 256 kB of flash memory, 64 kB of data memory, USB Device/Host/OTG, 8-channel DMA controller, 4 UARTs, 2 CAN channels, 3 SSP/SPI, 3 I2C, I2S, 8-channel 12-bit ADC, 10-bit DAC, motor control PWM, Quadrature Encoder interface, 4 general purpose timers, 6-output general purpose PWM, ultra-low power Real-Time Clock with separate battery supply, and up to 70 general purpose I/O pins. The LPC1765 is pin-compatible to the 100-pin LPC2368 Arm7™ MCU

Block diagram: LPC1764FBD100, LPC1765FBD100, LPC1766FBD100, LPC1768FBD100 Block Diagram



Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.