

LIN SBC with 2 x 60 mA High Side Drivers

MC33910

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The NXP® MC33910 is a SPI-controlled System Basis Chip (SBC) that combines many frequently used functions in an MCU-based system, plus a LIN transceiver.

- 5.0 V, 50 mA low dropout regulator with full protection and reporting features
- Full SPI-readable diagnostic and a selectable timing watchdog for detecting errant operation
- The LIN Protocol Specification, version 2.0 and 2.1 (G5AC) compliant LIN transceiver has waveshaping circuitry that can be disabled for higher data rates
- Two 60 mA high side switches with optional pulse-width modulation (PWM) are implemented to drive small loads

RST IRQ VS2 VS1 VDD INTERRUPT CONTROL BUS AGND MODULE INTERNAL VOLTAGE REGULATOR LVI, HVI, ALL OT (VDD, HS, LIN, SD) PGND RESET CONTROL MODULE LVR, WD, EXT µC 5.0 V OUTPUT HVDD MODULE WINDOW VS2 WATCHDOG MODULE HIGH SIDE PWMIN CONTROL HS1 VS2 MODULE MISO [HS2 MOSI SPI & CONTROL SCLK ANALOG MULTIPLEXER VBAT SENSE MODULE VSENSE cs CHIP TEMPERATURE SENSE MODULE ADOUT0 L1 ANALOG INPUT WAKE-UP MODULE MODULE DIGITAL INPUT MODULE RXD [LIN PHYSICAL LAYER TXD LIN LGND WDCONF

MC33910 Network Transceivers Block Diagram Block Diagram

View additional information for LIN SBC with 2 x 60 mA High Side Drivers.

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