<table>
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<th>Rev</th>
<th>Description</th>
<th>Date</th>
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<tr>
<td>A</td>
<td>Initial version</td>
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| AX1 | 1. As per the new FRDM-KL46 pin assignment xls connections changed.  
     2. Port name and Arduino functions name text added to the nets.  
     3. Arduino connector Ref des changed as per LIB requirement.  
     4. INT1_MAG net removed  
     5. Test point added in PTE26 | 07-MAR-13  | Luis Puebla Palma |
| AX2 | 1. As per the new FRDM-KL46 pin assignment xls connections changed.  
     2. Port name and Arduino functions name text changed to the nets as per new pin assignment.  
     3. Arduino connector Ref des changed as per LIB requirement.  
     4. INT1_MAG net removed  
     5. Test point added in PTE26 | 14-MAR-13  | Luis Puebla Palma |
| AX3 | 1. Magneto meter circuit added with i2C Bus (SCL/SDA)  
     2. Magneto meter interrupt is connected to accelerometer interrupt 2 through a 0 ohms resistor.  
     3. Spare buffer USB input is pulled low using a 0 ohms res. | 25-MAR-13  | Luis Puebla Palma |
| AX4 | 1. FXOS8700CQ replaced with MMA4451Q.  
     Pins wired to make compatible with CST, RST,  
     5V_v2/5V_v12 in FXOS8700CQ  
     2. DNP 6 ohms resistor added between inertial sensor 3rd pin and GND.  
     3. DNP 6 ohms resistor added b/w inertial sensor 16th pin and PTE26 | 26-MAR-13  | Luis Puebla Palma |
| B   | 1. DNP Updated  
     2. RST_K20D50 renamed to RST_K20D50_B  
     3. A085 Release | 28-MAR-13  | Luis Puebla Palma |
| B1  | 1. DNP - DNP  
     2. J7 - MOUNT  
     3. Schematic title changed to FRDM-KL46Z  
     4. A085 Release | 03-MAY-13  | Luis Puebla Palma |
| C   | Changing C17 from 1uF to 0.1uF to fix yield issue | 13-JUN-13  | Luis Puebla Palma |
1. Unless Otherwise Specified:
   - All resistors are in ohms, 5%, 1/8 Watt
   - All capacitors are in uF, 20%, 50V
   - All voltages are DC
   - All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:
   - B Denotes - Active-Low Signal
   - <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.
KL46Z Decoupling Caps

- PTC6 - I2S_RX_BCLK
- PTA1 - D0/RX/int PTC16
- PTA4 - D4/int PTC9 - D7/CMP/int
- PTD2 - D9/PWM/int
- PTD6 - D11/MOSI/int
- PTC7 - SOF_OUT
- PTC10 - I2S_RX_FS
- PTC11 - I2S_RXD

- PTA6 PTA14 PTA15 PTA16 PTA17 PTC13

- KL46Z Decoupling Caps
  - C9 1.0UF
  - C19 1.0UF
  - C22 0.1UF
  - R47 1K

- Push Buttons

- TSI Capacitive/Touch Interface

- SWD CONNECTOR

- USB

- 7 Segment LCD

- LEDs