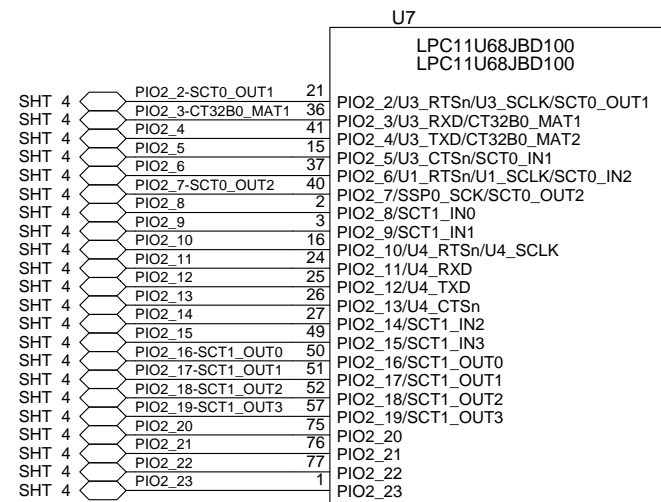
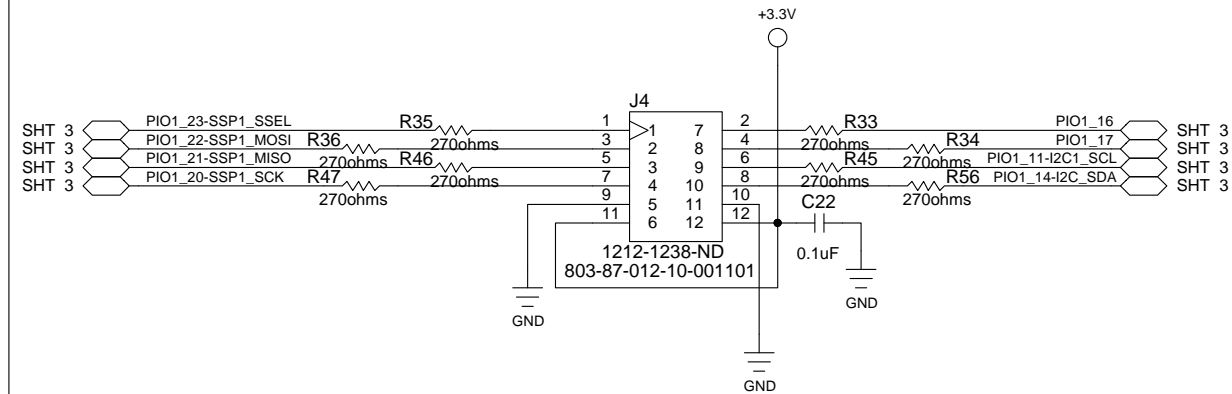


ISP Boot Modes		
Mode	PIO0_1	PIO0_3
No ISP (Flash)	high	X
USB	low	high
UART0	low	low



CONTRACT NO.		Target LPC1549			
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/			
DRAWN d.consiglio	9/25/2013				
CHECKED					
ISSUED	9/10/13	SIZE	FSCM NO.	DWG. NO.	REV
		D		Xpresso-LPC11U68	0
		SCALE		SHEET	3 OF 04

PMOD compatible header



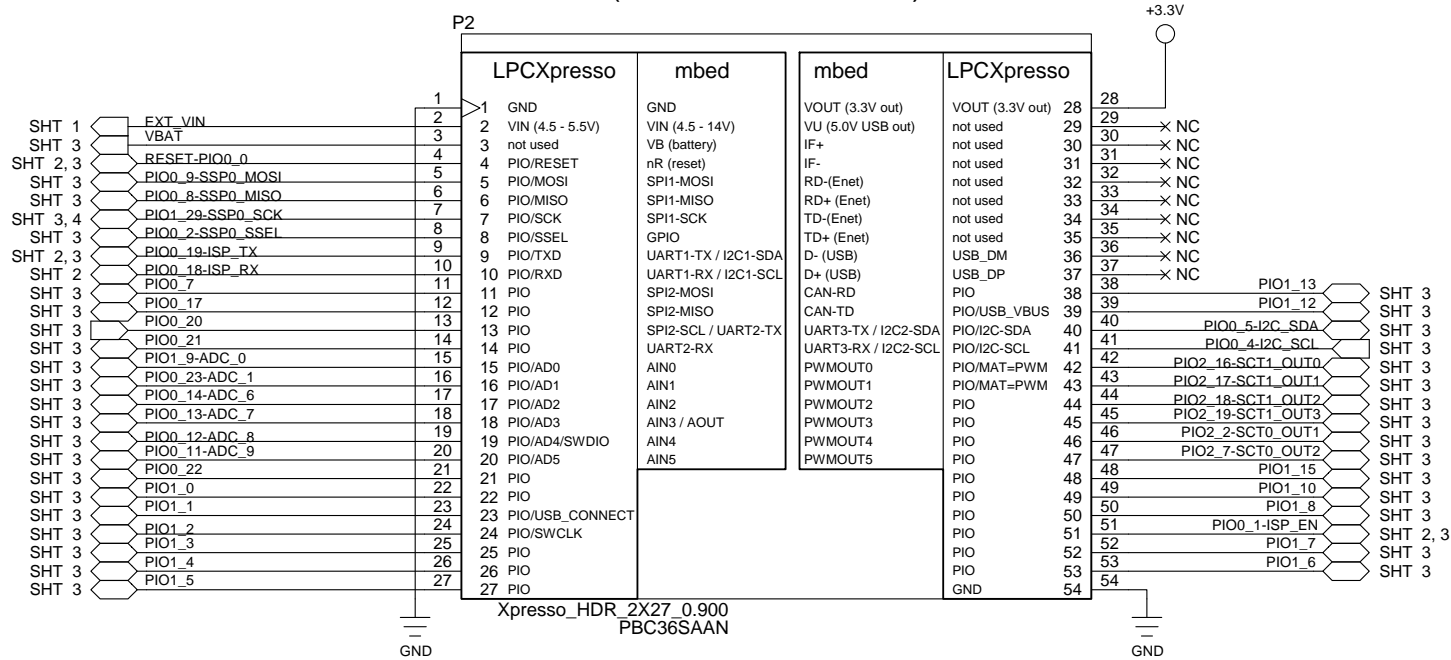
PMOD pin function

Pin 1: GPIO/SPI-SSEL(out)/UART-CTS(in)
Pin 2: GPIO/SPI-MOSI(out)/UART-TXD(out)
Pin 3: GPIO/SPI-MISO(in)/UART-RXD(in)
Pin 4: GPIO/SPI-SCK(out)/UART-RTS(out)
Pin 5: GND
Pin 6: VCC(3.3V)
Pin 7: GPIO/INT(in)
Pin 8: GPIO/RESET(out)
Pin 9: GPIO
Pin 10:GPIO
Pin 11:GND
Pin 12:VCC(3.3V)

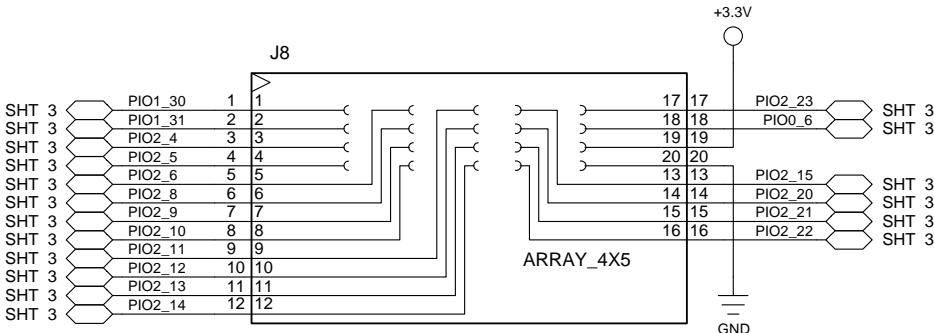
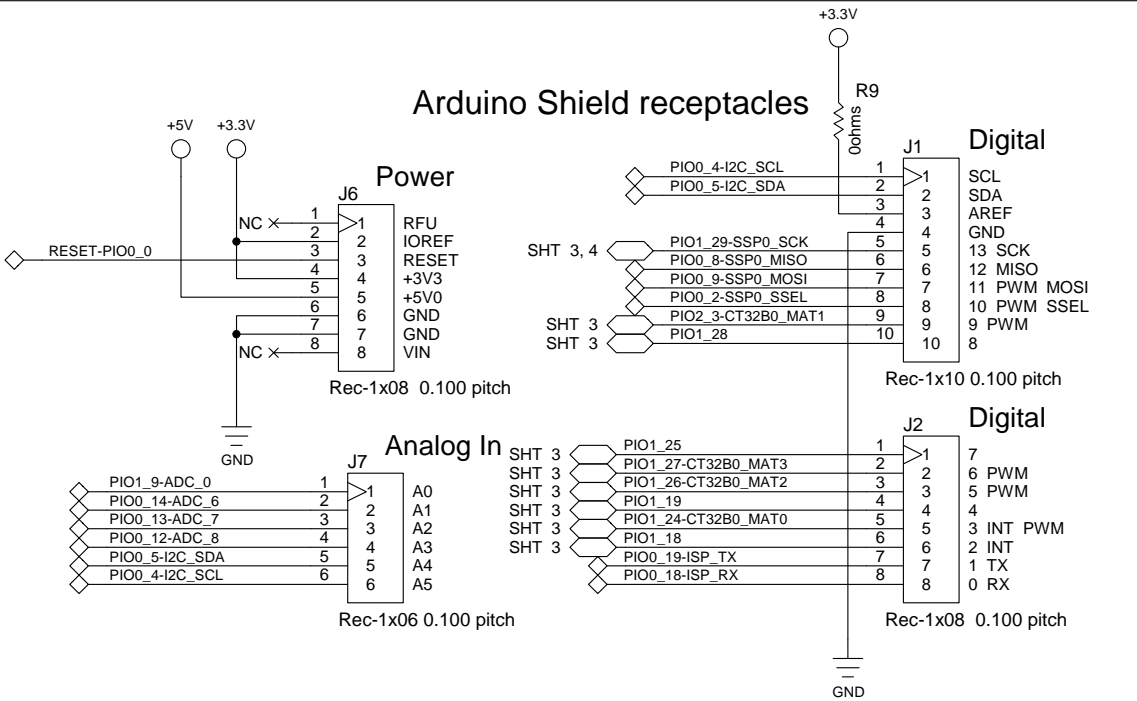
LPC11U68 supported function

GPIO/SPI-SSEL(out)
GPIO/SPI-MOSI(out)
GPIO/SPI-MISO(in)
GPIO/SPI-SCK(out)
GND
VCC(3.3V)
GPIO/INT(in)
GPIO/RESET(out)
GPIO
GPIO
GND
VCC(3.3V)

LPCXpresso brd header
(Bottom side of PCB)



Arduino Shield receptacles



CONTRACT NO.		Xpresso MC/Arduino/PMOD headers		
APPROVALS		DATE	NXP Semiconductors	
DRAWN		9/25/2013	411 E. Plumeria Dr	
CHECKED			San Jose, CA 95134	
ISSUED		9/10/13	www.standardics.nxp.com/microcontrollers/	
		SIZE	FSCM NO.	DWG. NO.
		D		Xpresso-LPC11U68
		SCALE		REV 0
				SHEET 4 OF 04