AN11578

Energy Harvesting with the NTAG I²C and NTAG I²C plus

Rev. 1.0 — 1 February 2016 301910 Application note COMPANY PUBLIC

Document information

Info	Content
Keywords	NTAG I ² C, NTAG I ² C plus, Energy Harvesting
Abstract	Show influencing factors and optimization for energy harvesting of the NTAG I ² C and NTAG I ² C <i>plus</i>



Revision history

Rev	Date	Description
1.0	20160201	Initial version

Contact information

For more information, please visit: <u>http://www.nxp.com</u>

For sales office addresses, please send an email to: salesaddresses@nxp.com

AN11578

All information provided in this document is subject to legal disclaimers.

Application note

1. Introduction

This application note is valid for both, NTAG I²C and NTAG I²C *plus.* To reduce complexity, in this document NTAG I²C is used to address both connected tags.

The NTAG I²C provides the capability to harvest energy from the RF field. This feature can be used to supply external circuits like microcontrollers with enough energy to operate.

This documents focuses on showing how much energy the NTAG I²C can delivery under which conditions and show the influencing factors for energy harvesting

1.1 Influencing Factors on Energy Harvesting

The factors which influence the power the NTAG I²C is able to harvest are mainly the following:

Antenna Size

Larger antennas usually provide more energy to the tag. To show a typical energy harvesting case, Class 5 antenna was chosen in this application note. With larger antennas it is possible to get more power out of the field.

• Antenna Turn Count

An antenna with less turns but with added capacitance to provide the same resonance frequency changes the interaction of the tag with the reader and provides usually more energy.

- Field Strength More field gives more energy
- Reader detuning especially with highly coupled reader systems (reader and tag antenna size the same, small or zero distance) the reader can be detuned from the tag

In general, under load the V_{out} voltage will drop. If too much current is taken out of the NTAG I²C the IC will not be able to communicate over RF. For stable I²C communication the V_{CC} should not drop below recommended minimum V_{CC} (see Electrical characteristics in [1]).

2. Recommendations

To optimize energy harvesting systems, these points should be considered:

- Minimize the current needed to be harvested. Any energy supplied via energy harvesting needs to be supplied by the reader and thus reduces the read range of the tag
- On MCU systems it is advisable to clock down the MCU and also use the deep sleep modes to minimize the current consumption
- The capacity connected to the energy harvesting pin should only be as large as needed by the external system – as larger the capacity is, as harder it is for the reader to wake up and supply the NTAG I²C

3. Example Energy Harvesting Data

For measuring the data a standard ISO-Setup according to ISO/IEC 10373-6 was used as field source. For measuring the energy harvesting current a Class 5 antenna was used and the small ISO tower (Test PCD assembly 2) was used.

The ISO-setup which is built according to ISO/IEC 10373-6 can be used to provide reliable test conditions and field strengths. With this setup exact field strengths can be produced.

3.1 Influence of Field strength variation

The influence of the field strength on the harvested energy is shown in the figure below. A 220 nF cap was used between GND and V_{out}, V_{out} and V_{CC} is connected. Class 5 antenna was used for this measurements. The graphs shows the minimum voltage measured on V_{out} during modulation. A current source is used as load to sink constant current.



For a given current load, minimum field strength required for stable operation with least voltage drop on V_{OUT} during RF communication is shown in the below given table:

Table 1. Hmin and Vout min on Vout for given load current					
Current Load [mA]	H _{min} [A/m]	Vout_min [V]			
1	1.2	2.7			
2	1.9	2.5			
3	2.7	2.4			
4	3.5	2.2			
5	4.3	2.0			
6	5	1.9			
7	5.7	1.7			

4. References

[1] NT3H2111/NT3H2211, NTAG I2C *plus*, NFC Forum Type 2 Tag compliant IC with I2C interface

http://www.nxp.com/documents/data_sheet/NT3H2111_2211.pdf

Application note

AN11578

5. Legal information

5.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

5.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the

customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

5.3 Licenses

Purchase of NXP ICs with NFC technology

Purchase of an NXP Semiconductors IC that complies with one of the Near Field Communication (NFC) standards ISO/IEC 18092 and ISO/IEC 21481 does not convey an implied license under any patent right infringed by implementation of any of those standards. Purchase of NXP Semiconductors IC does not include a license to any NXP patent (or other IP right) covering combinations of those products with other products, whether hardware or software.

5.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.

MIFARE — is a trademark of NXP B.V.

I²C-bus — logo is a trademark of NXP B.V

Application note

6. Contents

1.	Introduction	3
1.1	Influencing Factors on Energy Harvesting	3
2.	Recommendations	3
3.	Example Energy Harvesting Data	4
3.1	Influence of Field strength variation	4
4.	References	5
5.	Legal information	6
5.1	Definitions	6
5.2	Disclaimers	6
5.3	Licenses	6
5.4	Trademarks	6
6.	Contents	7

Please be aware that important notices concerning this document and the product(s) described herein, have been included in the section 'Legal information'.

© NXP B.V. 2016.

All rights reserved.

For more information, visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 1 February 2016

301910 Document identifier: AN11578