NXP Semiconductors

Application Note

MC33910/11/12 filtering capacitor for VS1

1 Introduction

The MC33910/11/12 family is a Serial Peripheral Interface (SPI) controlled System Basis Chip (SBC), combining many frequently used functions in an MCU based system, plus a Local Interconnect Network (LIN) transceiver. The SMARTMOS devices are low dropout regulators with full protection and reporting features. The device provides full SPI readable diagnostics and a selectable timing watchdog for detecting errant operation. The LIN Protocol Specification 2.0 and 2.1 compliant LIN transceiver has waveshaping circuitry that can be disabled for higher data rates.

This application note explains how to use capacitor filter on the VS1 input pin.

Contents

1	Introduction1
2	Using capacitor filter2
3	Application solution4
4	References
5	Revision history



2 Using capacitor filter

As you are working with the MC33910/11/12 devices, it is important that you understand the filtering mechanism for the product. The device may generate resets when spikes with falling slew rate in the range of -0.9 V/ μ s to -1.4 V/ μ s are applied on VS1 pin. Below -1.4 V/ μ s and above -0.9 V/ μ s, the device functionality is guaranteed.



Figure 1. Falling slew rate on VS1 pin

3 Application solution

If such behavior appears in the application, the ESR adjustment of C1 capacitor should be considered to ensure you have protected your application from the RESET condition. RESET condition is typically not found below -1.4 V/ μ s or above -0.9 V/ μ s.



Figure 2. Filtering capacitor on VS1 pin

4 References

Support Pages	URL
MC33910 Product Summary Page	http://www.nxp.com/webapp/sps/site/prod_summary.jsp?code=MC33910
MC33911 Product Summary Page	http://www.nxp.com/webapp/sps/site/prod_summary.jsp?code=MC33911
MC33912 Product Summary Page	http://www.nxp.com/webapp/sps/site/prod_summary.jsp?code=MC33912
Analog Home Page	http://www.nxp.com/analog

5 Revision history

Revision	Date	Description of Changes
1.0	1/2015	 Initial release AN5063 replaces MC33910ER, MC33911ER, and MC33912ER
	7/2016	Updated to NXP document form and style

How to Reach Us:

Home Page: NXP.com

Web Support: http://www.nxp.com/support Information in this document is provided solely to enable system and software implementers to use NXP products. There are no expressed or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation, consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by the customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address:

http://www.nxp.com/terms-of-use.html.

NXP, the NXP logo, Freescale, the Freescale logo, and SMARTMOS are trademarks of NXP B.V. All other product or service names are the property of their respective owners. All rights reserved. © 2016 NXP B.V.



