



8-bit S08 embedded controllers

MC9S08EL32/16 and MC9S08SL16/8

Embedded slave LIN interface controller (SLIC) and on-chip EEPROM

Target Applications

- Sensors
- · Small motors
- · Control network systems
- · Personal care/hand-held devices
- AC powered consumer goods
- Power tools
- · Fire alarms
- Simple media access controller (SMAC)
- Watchdog coprocessors
- Small appliances
- Security systems

Overview

Freescale Semiconductor's 8-bit EL and SL families of microcontrollers are designed for general market and LIN slave applications. The EL and SL microcontrollers integrate on-chip EEPROM and embedded slave LIN interface controller (SLIC) to increase performance and save cost for general purpose industrial and consumer applications.

MC90S08EL/SL Block Diagram

40 MHz S08 CPU (20MHz Bus)	Up to 32K Flash			Up to 512B EEPROM
Up to 1K RAM	SPI	l ² C	WDT	RTC
	ICS Clock	1 x SCI	LVI + LVW	Up to 6-ch., 16 bit timer
SLIC	Up to two comparators	Up to 16-ch., 10 bit ADC	On-Chip ICE On-Chip BDM	Up to 22 GPIO

Indicates a differentiating feature





Benefits Features 8-Bit HCS08 Central Processing Unit (CPU) Up to 20 MHz internal bus (40 MHz HCS08 Core) Offers strong performance throughout the entire frequency offering 2.7 to 5.5V across temperature range of -40°C to 85°C voltage range **On-Chip Memory** Up to 32K flash read/program/erase over full Allows user to take full advantage of in-application, re-programmability benefits in virtually any environment operating voltage and temperature • Up to 512 Bytes EEPROM Provides board space savings and reduces development time by allowing ability to manipulate diagnostic data at byte level, which provides finer granularity with smaller sector sizes than flash Power-Saving Modes • Two very low-power stop modes Offers power control and flexibility where multiple modules can run, while others are powered down Reduced power wait mode Extend battery life using on-chip functionality Very low-power real-time interrupt for use in run, wait, and stop **Clock Source Options** Oscillator (XOSC) - Loop-control Pierce oscillator; Optimizes power consumption and provides Crystal or ceramic resonator range of 31.25 kHz to user flexibility 38.4 kHz or 1 MHz to 16 MHz Provides accurate on-chip clock source and Internal Clock Source (ICS) — FLL modes; Internal saves cost by eliminating the need for reference clock with trim adjustment external components Peripherals Slave LIN interface controller (SLIC) — Supports LIN 2.0 and SAE J2602 protocols; up to 120 kbps, full LIN message buffering, automatic bit rate and Saves development time by automatically adjusting to LIN speed – no special code changes needed; enables high performance with fewest interrupts on frame synchronization, checksum generation and verification, UART-like byte transfer mode CPU of any known solution; saves cost with use of smaller and simpler driver code Analog to digital converter (ADC)—Up to 16-channel, 10-bit resolution Provides fast and easy conversion of analog inputs 2.5us conversion time, automatic compare function, temperature sensor • Serial communications interface (SCI) - with LIN Enables standard or LIN UART-based 2.0 Protocol and SAE J2602 compliance; Master communication. Offers additional support for lower extended break generation; Slave extended break detection; LIN slave supplement on SCI1; Automatic power using Wake from STOP feature baud rate correction; Message time-out detection Improved task scheduling for applications requiring Time of Day calendar functions' frees up timers for RTC-(Real-time counter) 8-bit modulus counter with binary or decimal based prescaler; External clock source for precise time base, time-of-day other activities calendar or task scheduling functions; Free running on-chip low power oscillator (1 kHz) for cyclic wake-up wit Input/Output · 16 interrupt pins with selectable polarity Offers flexibility by interfacing to a large number of pins that are capable of generating interrupts • 22 general purpose input/output (GPIO)s Results in a large number of flexible I/O pins **System Protection** Low-voltage detection with reset or interrupt; · Built in system protection to help secure data and selectable trip points warn of possible voltage loss conditions · Flash block protection Helps provide security by protecting code from unauthorized reading and guards against unintentional write/erase of user-code/data EEPROM block protection **Hardware Development Support** · Single-wire background debug interface This allows the developers to use the same interface for multiple platforms · On-chip in-circuit emulator (ICE) debug with real Grants full access to built-in chip emulation without the added expense of traditional emulator hardware time bus capture Reduces development time as emulation can be done real-time and on-chip

Cost-Effective Development Tools

DEMO9S08EL32 MSRP \$69

Cost-effective demonstration board, provides USB to BDM interface, LIN PHY, RS232 interface, Push buttons, LEDS, LIN and USB cables

CodeWarrior® Development Studio for Microcontrollers 6.x CWX-HXX-SE Complimentary*

CodeWarrior Development Studio for Microcontrollers is an integrated tool suite that supports software development for Freescale's microcontrollers. Designers can further accelerate application development with the help of the Processor Expert™ tool, which is an award-winning rapid application development tool in the CodeWarrior tool suite.

* Subject to license agreement

Part Number	Temp Ranges	Package
MC9S08EL32CTL	-40°C to +85°C	28 TSSOP
MC9S08EL32CTJ	-40°C to +85°C	20 TSSOP
MC9S08EL16CTL	-40°C to +85°C	28 TSSOP
MC9S08EL16CTJ	-40°C to +85°C	20 TSSOP
MC9S08SL16CTL	-40°C to +85°C	28 TSSOP
MC9S08SL16CTJ	-40°C to +85°C	20 TSSOP
MC9S08SL8CTL	-40°C to +85°C	28 TSSOP
MC9S08SL8CTJ	-40°C to +85°C	20 TSSOP

Learn More:

For more information about the EL/SL family, please visit **www.freescale.com/8bit.**

