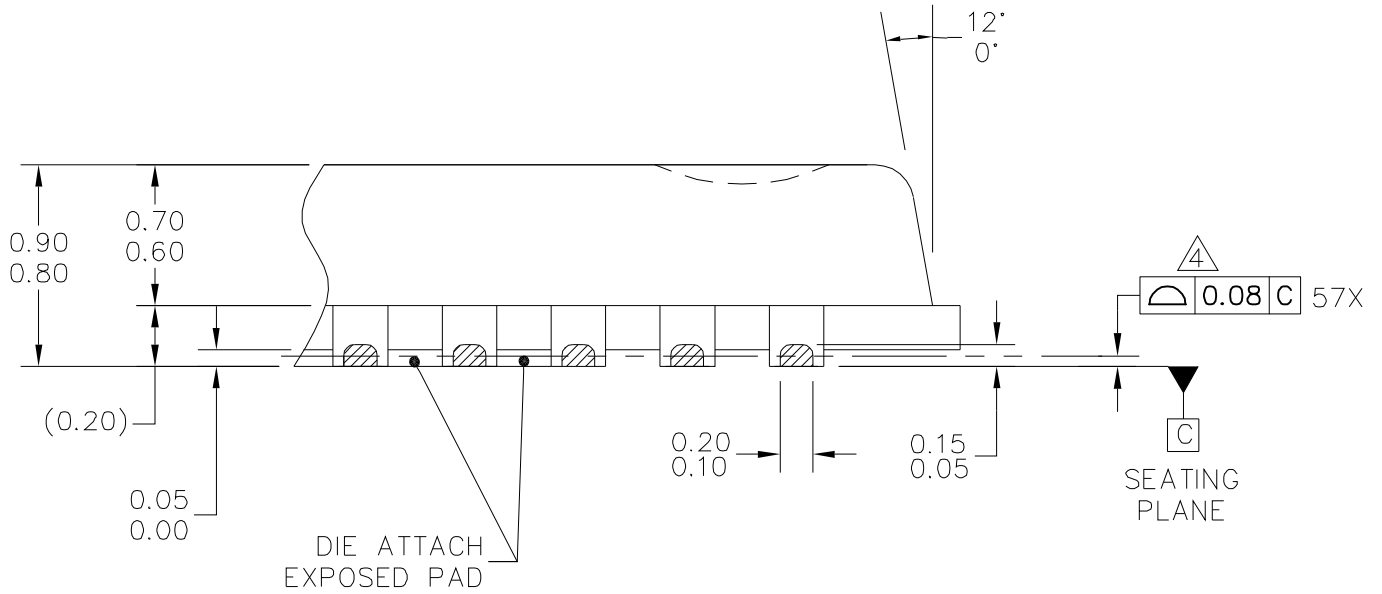
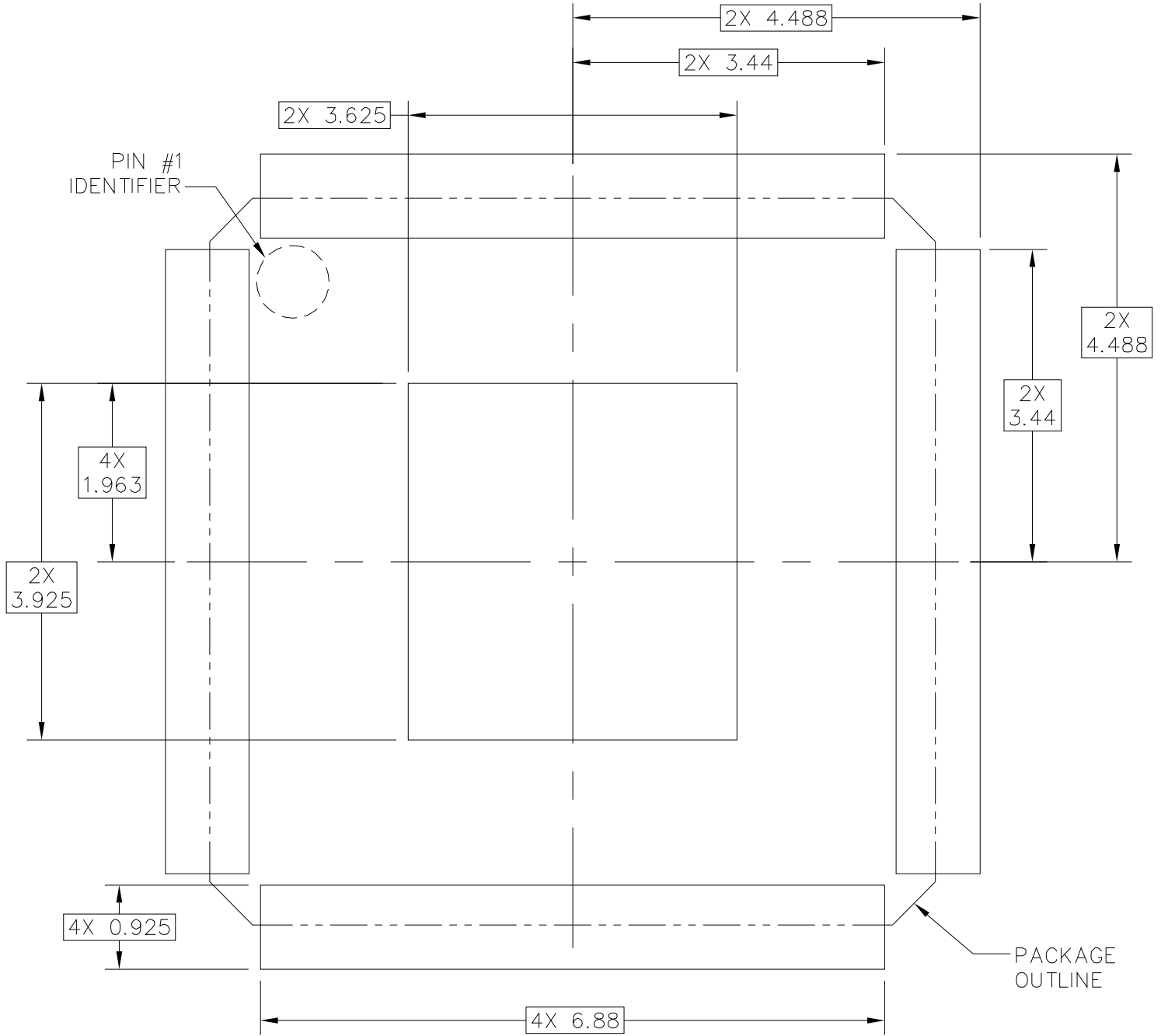


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TITLE: QFN (PUNCH), THERMALLY ENHANCED, WETTABLE FLANKS, 8 X 8 X 0.85, 0.5 PITCH, 56 TERMINAL	DOCUMENT NO: 98ASA00379D	REV: F
	STANDARD: NON-JEDEC	
	SOT684-15	08 JAN 2016



DETAIL G  
VIEW ROTATE 90° CW

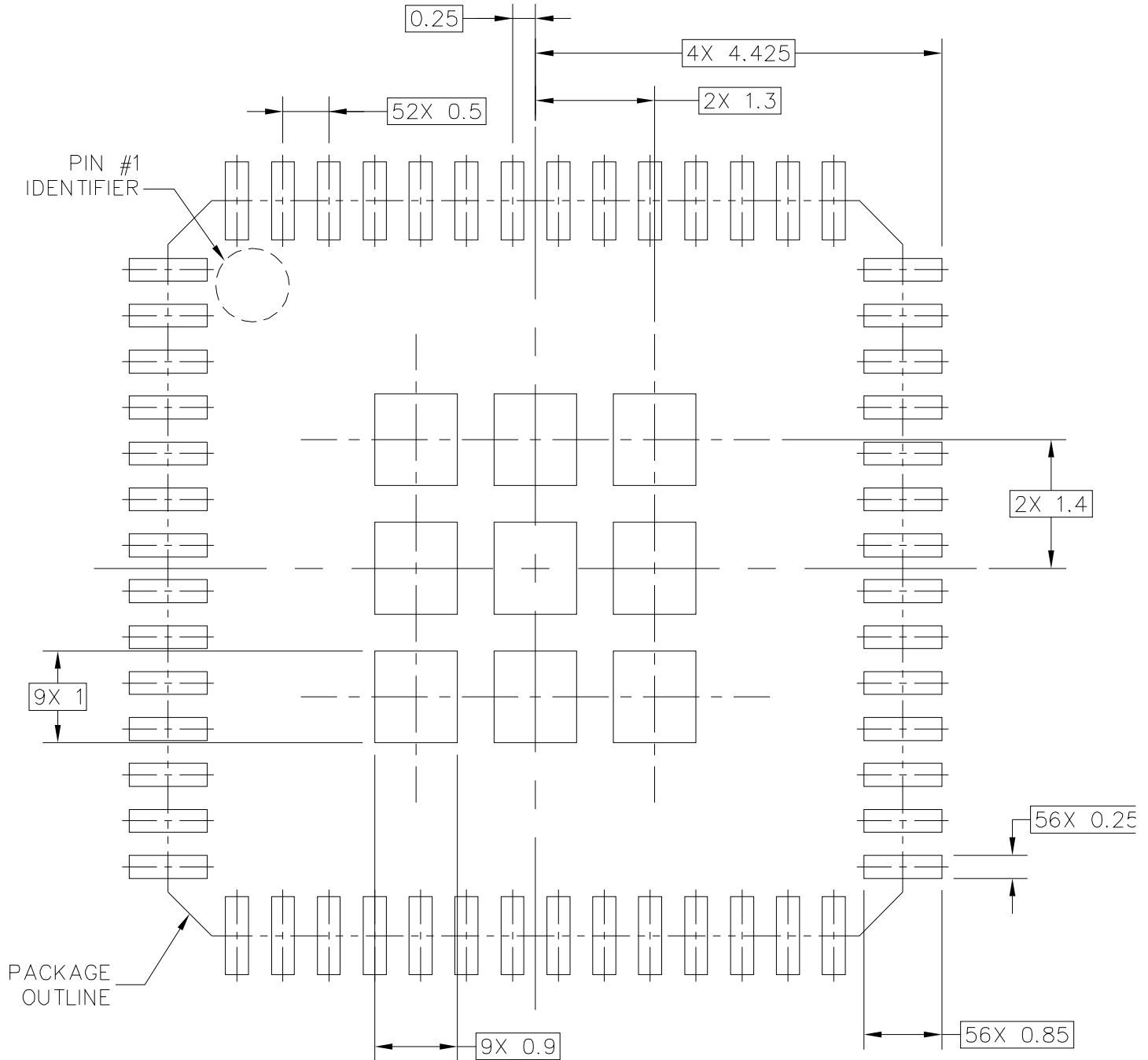
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TITLE: QFN (PUNCH), THERMALLY ENHANCED, WETTABLE FLANKS, 8 X 8 X 0.85, 0.5 PITCH, 56 TERMINAL		DOCUMENT NO: 98ASA00379D	REV: F
		STANDARD: NON-JEDEC	
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## PCB DESIGN GUIDELINES – SOLDER MASK OPENINGS

THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION. DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL / SPECIFIC REQUIREMENTS.

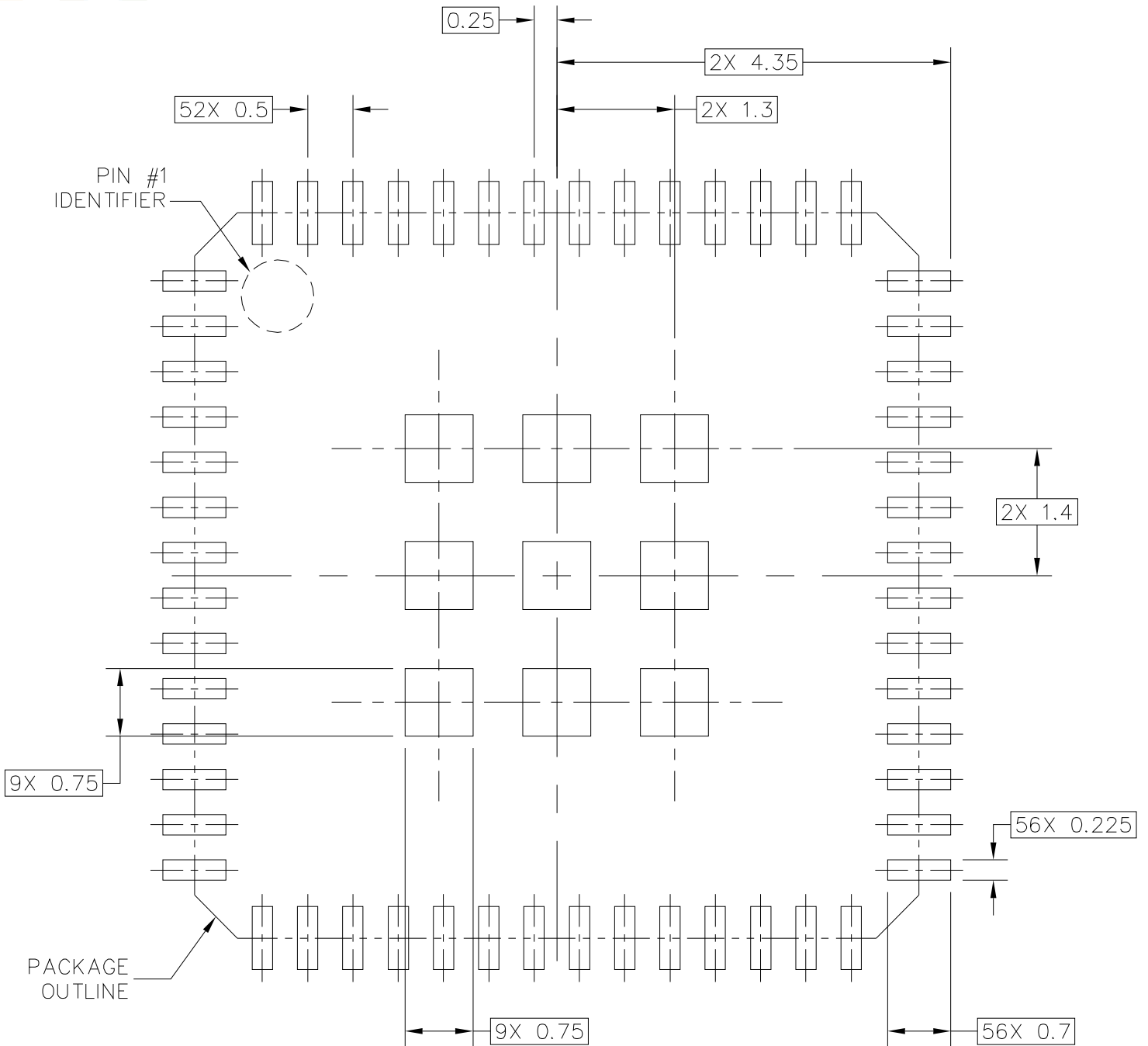
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TITLE: QFN (PUNCH), THERMALLY ENHANCED, WETTABLE FLANKS, 8 X 8 X 0.85, 0.5 PITCH, 56 TERMINAL		DOCUMENT NO: 98ASA00379D	REV: F
		STANDARD: NON-JEDEC	
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## PCB CU GUIDELINES – I/O PADS & SOLDERABLE AREAS

THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION. DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL / SPECIFIC REQUIREMENTS.

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		STANDARD: NON-JEDEC	
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## SOLDER PASTE STENCIL DESIGN GUIDELINES

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		STANDARD: NON-JEDEC	
		SOT684-15	08 JAN 2016



NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. THIS IS A NON-JEDEC REGISTERED PACKAGE.
4.  COPLANARITY APPLIES TO LEADS AND DIE ATTACH FLAG.
5. MIN. METAL GAP SHOULD BE 0.2 MM.

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		STANDARD: NON-JEDEC	
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