

Multiservice Platform ATM/IP/FR Line Card

Overview

One of the greatest challenges of OEMs building multiservice platforms (MSPs) is maximizing their return on investment (ROI). With the massive network build-outs of the past few years and falling bandwidth prices putting pressure on margins, achieving better ROI can be challenging, especially for the access and edge multiservice platform market.

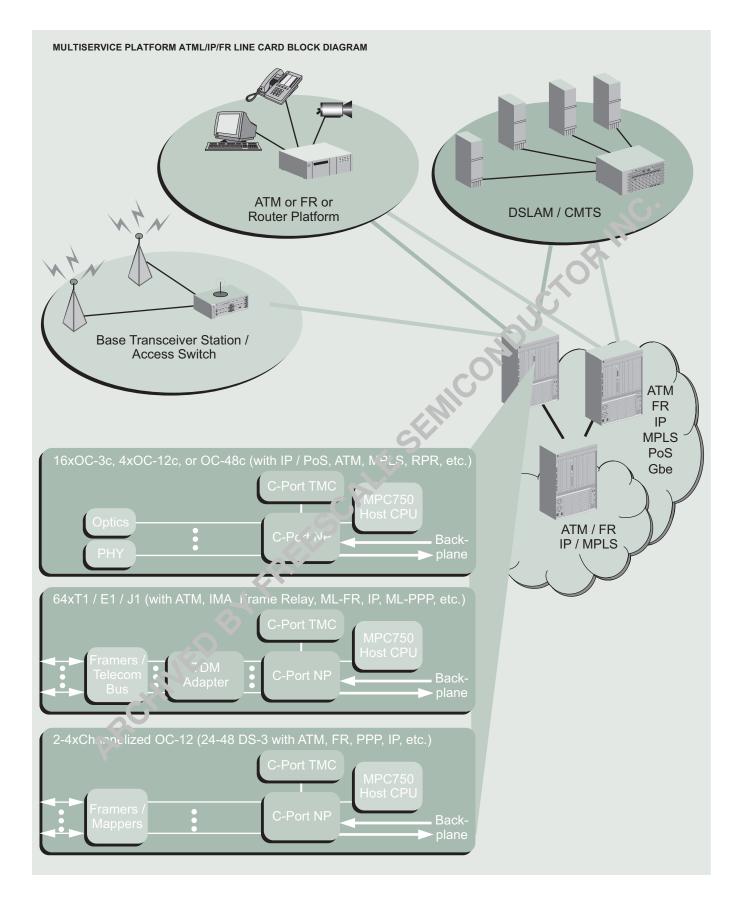
The market is diverse with single MSPs accommodating a mixture of ATM, Frame Relay, IP, MPLS, and PoS, with port speeds ranging from subT1/E1 to OC-3 to full duplex OC-48c/STM-16 links. Emerging applications including packetized voice (VoIP) and 2.5G/3G wireless backbone networks add further to the demands of scalability and adaptability. And with converging applications (data/voice/::29c) come issues of quality of service (QcC).

Key Benefits

- > Realize great efficiencies of design with a single "platform" architecture for virtually any MSF time card
- > S. ft vare upgradable line cards can easily address evolving standards and new services— minimizing OEM and service-provider risk
- > True, fine-grained, multiservice support for a high number of users
- Get to market faster with streamlined development and C-programmable solutions









Freescale Ordering Information				
PCC3E0RX180WB0B	C-3e NP	5.5 W @ 180 MHz freescale.com/networkprocessors		
PCC5E0RX266WB0B	C-5e NP	9.0 W @ 266 MHz freescale.com/networkprocessors		
PCQ500RX000WA0A	Q-5 TMC	5Gbps throughput freescale.com/networkprocessors		
PCM500ZUTA0A	M-5 Channel Adapter	3.0 W @ 104 MHz freescale.com/networkprocessors		
MPC750	PowerPC ISA	freescale.com/PowerPC		
CSTC501W Note 1	C-Ware Software Toolset	Web site download of current CST release (Windo, 's or UNIX)		
CDEV101A Note 2	C-Ware Development System	CDS Base Unit (Chassis, Power Supply, Single Roard Computer)		

Design Challenges

A common architecture of a multiservice system today is a rack or shelf with multiple flavors of line cards with a common backplane bus and control processor card. The backplane is often ATM-based. These line cards can be dedicated to a specific function such as IP, ATM, or Frame Relay or combine functions, such as ATM and Frame Relay on a single line card. Interworking among the cards is provided by routing to other cards within the shelf across the switch fabric.

Typically, these solutions are not easily scalable in terms of increasing bandwidths, implementing new interfaces, or adding new services. Any solution for the access and edge markets needs to offer a range of speeds and physical interfaces for toury's applications and an easy nigration path for future applications - Juding a more IP-centric network. n addition, today's MSPs cannot easily adapt to new and evolving stanca, as. The ability to upgrade software versus forklift upgrades is paramount.

By implementing greater hardware integration and 'soft' implementation of functionality in an MSP product design, vendors will see a significant increase in ROI. For example, to be able to take a previous solution consisting of two cards and replace it by a single line card, vendors will automatically see ROI gains—and increasing port density, er card creates even better financial performance. And within that the line card, vendors need to support both ATM/ frame relay technologies and IP/MPLS services, plus be about o upgrade functionality inso ware. Both OEMs and their customars will benefit from this type of solu cal

Free ale Semiconductor Solution Fi rescale Semiconductor's C-Port™ hetwork processor family enables a new approach to MSP design that delivers a broad solution set within a highly integrated hardware and software platform providing flexibility and adaptability. With C-Port family network processors, traffic management coprocessors, and channel adapters, vendors can create a hybrid line card capable of incorporating multiprotocol capabilities and standards on a costefficient platform, which can be easily integrated into existing and new multiservice switches.

The C-55 ™ network processor (NP) ວວາຮ່ວເຮ of sixteen programmable, full าแplex CPs (channel processors). Each CP can be programmed for a particular service/application (ATM, frame relay, Ethernet, SONET, and so on), and CPs can be aggregated for a wide spectrum of data streams such as Fast Ethernet to OC-12c to OC-48c on a single C-5e NP. To increase your bandwidth and computing power, two C-5e NPs can be used to scale your system up to 10Gbps aggregate bandwidth. Multiple C-5e NPs, used with a switch fabric, can achieve Terabits per second bandwidth.

A single C-5e NP-based interface card design can be built with a variety of different PHY interface types. For example, the C-5e NP architecture can support up to:

- > 16 10/100 Ethernet interfaces
- > 4 Gigabit Ethernet interfaces
- > 16 OC-3c/STM-1 interfaces
- > 4 OC-12c/STM-4 interfaces
- > 1 OC-48c/STM-16 interface (with assistance of the M-5 Channel Adapter)
- > 1 OC-48 interface connecting up to 48 logical interfaces

can be downloaded from freescale.cportcorp.com/support. sales representative or distributor for details and availability of system modules, which you order separately.



Cost efficiencies can be realized through integrated functions on the C-5e NP, including Ethernet MACs and SONET framers, which speed system development, simplify device design, and lower total system costs. With Freescale Semiconductor's Smart Networks Alliance program, industrystandard interfaces enable system designers to gluelessly integrate additional hardware. For example, the C-5e NP has a glueless interface to the IBM[™] Packet Routing Switch Fabric, in addition to conforming to standard CSIX-L1, Utopia 2, and Utopia 3 interfaces.

With the addition of Freescale
Semiconductor's tightly integrated traffic management coprocessors (TMCs), advanced QoS configurations can be implemented such as active queue management, policing and shaping, and scheduling of flows—all programmed using the C-Port family QoS APIs.

Software for ATM, frame relay, MPLS, SARing and SONET can be implemented in the C-5e NP, which handles all the forwarding plane software. Through a tight coupling to a host processor, such as Freescale Semiconductor's MPC750, which runs

control/management plane software, even more advanced services can be achieved. Further adaptability via a stable, standard API ensures that system design investments can be carried forward.

Thus, one basic hardware design, matched with many different PHY interfaces, combined with applications that are fully software programmable yields a vast range of different solutions leveraging a common platform. By using the C-Port family in an MSP line card design, a multi-year phased product delivery can be radically condensed, offering a massive time-to-market competitive advantage.

Development Environment

The C-Port family development environment consists of the followin components:

> C-Ware™ Applications '.irraly (CAL). Comprehensive set of reference applications for building networking systems based on Freescale Semiconductor's C-Port family. The CAL significantly accelerates customer software development by providing extensive servence source code that is

instrumented for and tested with the CST.

- C-Ware Software Toolset (CST).
 Functional and performance accurate simulation environment, standard GNU-based compiler and debugger, GUI performance analysis tool, traffic scripting tools, and comprehensive C-Ware APIs.
- > C-Ware Development System (CDS). Compact PC chassis with Freescale Semiconductor MPC750 Host Application Module, which can also include IP Switch Modules, TMC Daughter Cards, and various Physical Interface Modules (PIMS). Complete had dware reference designs also available.

Vendors may also select Wind River Systems Tornado® for Managed Switches (TMS) to program the host processor/control plane software. Freescale Semiconductor has established an alliance with Wind River Systems to provide integration of the C-Port network processors with TMS 2.0 supporting both Layer 2 and Layer 3 services. In addition, the Host Application Module in the CDS runs VxWorks® on the MPC750.

Development Tool	s		
			Description
Software Kit	C-Ware S जिल्ला Toolset (CST)	Freescale Semiconductor	Provides a comprehensive software development environment for building networking systems based on Freescale Semiconductor's C-Port network processor family. By providing a simple programming model, standard tools, a robust simulation environment, and a host application environment, the CST can significantly accelerate your software development effort.
Reference Application	C-Ware Applications Library (CAL)	Freescale Semiconductor	Offers a comprehensive set of reference applications for building networking systems based on Freescale Semiconductor's C-Port network processor family. The CAL significantly accelerates customer software development by providing extensive reference source code that is instrumented for and tested with the C-Ware Software Toolset.
Development Kit	C-Ware Development System (CDS)	Freescale Semiconductor	Provides an environment for you to prototype an entire product that leverages C-Port network processors. This chassis-based system allows you to develop your unique software and then integrate and test it in an actual hardware environment, well before your target product is available.



Third Party Suppo	ort	
	Description	
Corrent	For High-Performance Security Processing	Tel: 480-648-2300 sales@corrent.com
IBM	For Switch Fabric Solutions	Tel: 33 4 92 11 56 40 Fax: 33 4 92 11 00 23 garciag@fr.ibm.com
IDT	For Advanced Classification	Tel: 613-724-6004 Fax: 613-724-6008
Wind River Systems	For Tornado for Managed Switches and RTOS	Tel: 603-724-6004 Fax: 613-724-6008 Tel: 800-545-9463 Fax: 510-814-2010



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