emWin Graphics Library

Release Notes

- Pre-Compiled Libraries
- Board Support Packages (BSPs)
- Alternative Solutions

Pre-Compiled Libraries

The following libraries are provided for use with any current NXP ARM MCU, including the ARM Cortex M4, Cortex M3, Cortex M0/M0+, ARM7 and ARM9. These object code libraries serve as the starting point to porting emWin to your own HW. Library is compiled for multitasking (GUI_OS=1) with maximum number of tasks equal to four (GUI_MAXTASK = 4).

Segger emWin 5.22 Libraries with Hardware Floating point for NXP M4 MCUs

These emWin library files were built using:

- arm-none-eabi-gcc gcc version 4.6.2 (LPCXPresso 6.1.0 by Code Red).
- IAR ANSI C/C++ Compiler V6.60.1.5140/W32 for ARM
Copyright 1999-2012 IAR Systems AB.
- ARM C/C++ Compiler, 4.72.10.0 MDK-ARM
- Visual C++ 2010 Express edition

There is one set of library files for Cortex M4.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5. Linking with these libraries will cause diagnostic messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.
These libraries contain emWin version 5.22. You can also get emwin version using below function call

GUI_GetVersionString()

Segger emWin 5.22 Libraries for NXP MCUs

These emWin library files were built using:

- arm-none-eabi-gcc gcc version 4.6.2 (LPCXPresso 5.1.0 by Code Red).
- IAR ANSI C/C++ Compiler V6.50.1.4415/W32 for ARM
Copyright 1999-2012 IAR Systems AB.
- ARM C/C++ Compiler, 5.02 [Build 28] [MDK-ARM Professional]
- Visual C++ 2010 Express edition

There is one set of library files for each of the 5 ARM cores and one for the x86 core.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5. Linking with these libraries will cause diagnostic messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.

Segger emWin 5.20 Libraries for NXP MCUs

V1.1 Applied patch by segger to update from V5.20C to V5.20D.

V1.0 first release

These emWin library files were built using:

- arm-none-eabi-gcc gcc version 4.6.2 (LPCXPresso 5.1.0 by Code Red).
- IAR ANSI C/C++ Compiler V6.50.1.4415/W32 for ARM
  Copyright 1999-2012 IAR Systems AB.
- ARM C/C++ Compiler, 5.02 [Build 28] [MDK-ARM Professional]
- Visual C++ 2010 Express edition

There is one set of library files for each of the 5 ARM cores and one for the x86 core.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5. Linking with these libraries will cause diagnostic messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.

Segger emWin 5.18 Libraries for NXP MCUs

Updates since v100 (release v101)
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Support added for PNG image

These emWin library files were built using:

1) arm-none-eabi-gcc gcc version 5.0.10 (Red Suite 2010Q4 by Code Red).

2) IAR ANSI C/C++ Compiler V6.50.1.4446/W32 for ARM
Copyright 1999-2012 IAR Systems AB.

3) ARM C/C++ Compiler, 5.02 [Build 28] [MDK-ARM Standard]

4) Visual Studio 2010 Express edition

There is one set of library files for each of the 5 ARM cores and one for the x86 core.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5.
Linking with these libraries will cause diagnostic messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.

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V100 first release

This version of the library fixes a problem with name mangling that caused some graphics driver functions to be excluded from IAR builds.

These emWin library files were built using:

arm-none-eabi-gcc gcc version 5.0.10 (Red Suite 2010Q4 by Code Red).

IAR ANSI C/C++ Compiler V6.50.1.4446/W32 for ARM
Copyright 1999-2012 IAR Systems AB.

Visual Studio 2010 Express edition

The library files in the uVision 4 folder are copies of the library files in the EmbeddedWorkbench 6 folder. Only the filenames are different.

There is one set of library files for each of the 5 ARM cores and one for the x86 core.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5. Linking with these libraries will cause diagnostic messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.

Segger emWin 5.16 Libraries for NXP MCUs Version 101

This version of the library fixes a problem with name mangling that caused some graphics driver functions to be excluded from IAR builds.

These emWin library files were built using:

arm-none-eabi-gcc gcc version 4.5.1 (Red Suite 2010Q4 by Code Red).

IAR ANSI C/C++ Compiler V6.40.1.53790/W32 for ARM
Copyright 1999-2012 IAR Systems AB.

Visual Studio 2010 Express edition.

The library files in the uVision 4 folder are copies of the library files in the EmbeddedWorkbench 6 folder. Only the filenames are different.

There is one set of library files for each of the 5 ARM cores and one for the x86 core.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5. Linking with these libraries will cause diagnostic messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.

Segger emWin 5.14 Libraries Version 102

This version of the library fixes a problem with name mangling that caused some graphics driver functions to be excluded from IAR builds.

These emWin library files were built using:

arm-none-eabi-gcc (Sourcery G++ Lite 2011.03-42) 4.5.2
Copyright (C) 2010 Free Software Foundation, Inc.

IAR ANSI C/C++ Compiler V6.30.1.53127/W32 for ARM
Copyright 1999-2011 IAR Systems AB.

Visual Studio 2010 Express edition

The library files in the uVision 4 folder are copies of the library files in the EmbeddedWorkbench 6 folder. Only the filenames are different.

There is one set of library files for each of the 5 ARM cores and one for the x86 core.

Library files that include '_d5' are built with GUI_DEBUG_LEVEL=5. Linking with these libraries will cause diagnostic

messages for log entries, warnings, and entries to be produced.
The file GUI_Debug.h contains a description of these debug levels.

Version 1.02

This version does not include libraries for Rowley CrossWorks. These will be included in the next version.

Segger emWin 5.12 Libraries for NXP MCUs

emWin 5.12 libraries for use with any NXP ARM Cortex M0, M3, M4, ARM7 or ARM9 MCU. These libraries can be used in combination with an existing BSP from Segger to use emWin on any supported NXP development board, or to get emWin working on new HW using the current BSPs as a starting point. See the license details for further information on usage restrictions.

Board Support Package

The emWin documentation includes a chapter on porting emWin to a new platform, and you can use one of the Board Support Packages below as a starting point, or reference any up-to-date demo BSP on Segger's website.

emWin 5.22 Board Support Package for EA1788 Board

LPC1788 BSP for Embedded Artists EA1788 Board. You can use this BSP on EA LPC4088 board too by uncommenting following line:
//#define LPC4088 in LCDConf.h file

emWin 5.20 Board Support Package for EA1788 Board

LPC1788 BSP for Embedded Artists EA1788 Board. You can use this BSP on EA LPC4088 board too by uncommenting following line:
//#define LPC4088 in LCDConf.h file

Touch calibrated for EA LCD 4.3"(480x272)

emWin 5.18 Board Support Package for EA1788 Board

LPC1788 BSP for Embedded Artists Board. You can use this BSP on EA LPC4088 board by uncommenting following line
//#define LPC4088 in LCDConf.h file

Touch calibrated for EA LCD 4.3"(480x272)
Fix for IAR and LPCXpresso projects

emWin 5.16 Board Support Package for EA1788 Board

LPC1788 BSP for Embedded Artists Board. This BSP has following changes.

This version of the library fixes a problem with name mangling that caused some graphics driver functions to be excluded from IAR builds.
Port P2.12 carries LCD_VD_3 signal instead of LCD_VD_18. LCD.conf.c modified.

Alternative Solutions

If you are looking for a lighter-weight, basic graphics library, you may also be interested **SWIM**, a free Graphics library produced and provided by NXP.

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