PIFI Library Release Notes

PIFI Library release history and known issues

PIFI Library - Release Notes

The version history and known issue lists on this page are for PIFI Library

Some issues are known at the time of the versioned package release. Issues found after the release can be found on the LPCOpen bug tracker pages.



LPCSPIFI Library v1.03 release (Released: 1/19/2015): Library updates

Changes

- Corrected id for S25FL256S device
- Added 4 Byte address support for large devices
- Reorganized directory structure to flat layout
- Removed --gnu flag from Keil project
- Keil generates aeabi compliant libraries for use with other toolchains (like IAR)

Known issues

- To use with LPCOpen v2.xx examples the libraries must be manually copied to the software/lpcspifilib/lib/xxxx folder
- Option SPIFI_CAP_NOBLOCK is not implemented.
- Will break IAR examples in LPCOpen v2.12 [Libraries in projects must be renamed from lib llpcspifi Mx.a to lib lpcspifi Mx.a]

LPCSPIFI Library v1.01 release (Released: 12/11/2014): Library updates

Changes

- Changed reported device string on S25FL512S from "S25FL512S 256kSec" to "S25FL512S".
- Changelog update: Added support for MX25L1635E, MX25L6435E, MX25L8035E, S25FL016K, S25FL064P, S25FL128S, S25FL256S, S25FL512S, W25Q16DV, and W25Q64FV devices.
- Changelog update: Changed maxRead to 16128 (was 32768).

- Option SPIFI_CAP_NOBLOCK is not implemented.
- Will break IAR examples in LPCOpen v2.12 [Libraries in projects must be renamed from lib_llpcspifi_Mx.a to lib_lpcspifi_Mx.a]

LPCSPIFI Library v1.00 release (Released: 12/05/2014): Library updates

Changes

- Added support for MX25L1635E, MX25L6435E, MX25L8035E, S25FL016K, S25FL064P, S25FL128P, S25FL256S, S25FL512S, W25Q16DV, and W25Q64FV devices
- Added support for device enumeration
- Added support for device base address return
- Improved read/write performance by issuing dword access to spifi controller
- Changed device structure to use function enum to facilitate adding external devices without recompiling library
- Added support for dual read / write.
- Changed guad command to guad io read (improved performance).
- Added ability for device to define read / write configuration word on a per device basis.
- Changed maxRead to 16383 (was 32768)
- Removed API spifiDeInit();
- Renamed spifiDevGetInfo option SPIFI_INFO_MAX_QUADREAD_CLOCK to SPIFI_INFO_MAX_HSREAD_CLOCK
- Renamed spifiDevGetInfo option SPIFI_INFO_MAX_QUADPROG_CLOCK to SPIFI_INFO_MAX_HSPROG_CLOCK

- Option SPIFI_CAP_NOBLOCK is not implemented.
- Will break IAR examples in LPCOpen v2.12 [Libraries in projects must be renamed from lib_llpcspifi_Mx.a to lib_lpcspifi_Mx.a]

LPCSPIFI Library v0.06 (Beta) release (Released: 06/25/2014): Library updates

Changes

- Changed identify to allow dynamic ID bytes per MFG/PART #. Moved to common module.
- Added support for S25FL129P.
- API Changes:
 - ADD spifiDevGetFamilyName(...)
 - DEPRICATED spifiDevGetDeviceName(...)
 - ADD SPIFI_REG_FAMILY_Spansion_2Byte_PStatus()
 - DEPRICATED SPIFI_REG_FAMILY_SpansionS25FLP()
 - ADD SPIFI_REG_FAMILY_Spansion_3Byte_Status()
 - DEPRICATED SPIFI_REG_FAMILY_SpansionS25FL1()
 - ADD SPIFI_REG_FAMILY_Macronix_2Byte_Status()
 - DEPRICATED SPIFI_REG_FAMILY_MacronixMX25L()
- Synchronized variable / function names between families to aid in comparison.
- Removed Chip.h dependency.
- Code/Memory optimizations.
- LPCXpresso XML projects added.
- M0 Library added.
- Sub-block erase enabled for devices with full device addressability.
- IAR library renamed to match Keil library.

Known issues

- Option SPIFI_CAP_NOBLOCK is not implemented. (Carried forward)
- Will break IAR examples in LPCOpen v2.12 [Libraries in projects must be renamed from lib_llpcspifi_Mx.a to lib_lpcspifi_Mx.a]

LPCSPIFI Library v0.05 (Beta) release (Released: 05/15/2014): Initial Source Release

Changes

- Comment cleanup.
- Added support for 40xx series
- Changed spifilnit() api to include spifi control register address.
- Changed spifilnitDevice() to include spifi control register address.

- Option SPIFI_CAP_NOBLOCK is not implemented.
- Option SPIFI_CAP_SUBBLKERASE is not working.
- Device S25FL129P is un-tested.

LPCSPIFI Library v0.04 release (Released: 04/25/2014): Initial Beta package (Pilot)

Changes

Initial pilot (beta) release

- Option SPIFI_CAP_NOBLOCK is not implemented.
- Option SPIFI_CAP_SUBBLKERASE is not working.
- Device S25FL129P is un-tested.

How to Reach Us

Home Page: www.nxp.com

Web Support: www.nxp.com/support

USA/Europe or Locations Not Listed:

NXP Semiconductor

Technical Information Center, EL516

2100 East Elliot Road

Tempe, Arizona 85284

+1-800-521-6274 or +1-480-768-2130

www.nxp.com/support

Europe, Middle East, and Africa:

NXPHalbleiter Deutschland GmbH

Technical Information Center

Schatzbogen 7

81829 Muenchen, Germany

+44 1296 380 456 (English)

+46 8 52200080 (English)

+49 89 92103 559 (German)

+33 1 69 35 48 48 (French)

www.nxp.com/support

Japan:

NXP Semiconductor

ARCO Tower 15F

1-8-1, Shimo-Meguro, Meguro-ku,

Tokyo 153-0064, Japan

0120 191014 or +81 3 5437 9125

support.japan@nxp.com

Asia/Pacific:

NXP Semiconductor Hong Kong Ltd.

Technical Information Center

2 Dai King Street

Tai Po Industrial Estate

Tai Po, N.T., Hong Kong

+800 2666 8080

support.asia@nxp.com

Contributor/Author

Name

Title

www.nxp.com