

Valis X2 (MPMC745x) Configuration Guide

	3	2	1	0	PCI	BUS
↑	↓	↑	↑	↑	33	33
↓	↓	↑	↑	↓	33	50
↓	↓	↑	↓	↑	33	66
↑	↑	↑	↓	↓	33	83
↑	↑	↑	↑	↓	33	100
↑	↑	↑	↑	↓	33	133

	2	1	0	EX	X	66	83	100	133
↑	↓	↓	↑	↑	3.5	233	292	350	350
↓	↓	↓	↑	↑	4	266	333	400	400
↓	↓	↓	↑	↑	7	466	584	700	700
↓	↓	↓	↑	↑	8	533	666	800	800
↓	↓	↓	↓	↓	10	660	830	1000	1000
↓	↓	↓	↓	↓	14	933	1166	1400	1866
↓	↓	↓	↓	↓	4.5	300	375	450	600
↓	↓	↓	↓	↓	5	333	416	500	666
↓	↓	↓	↓	↓	5.5	366	458	550	733
↓	↓	↓	↓	↓	6	400	500	600	800
↓	↓	↓	↓	↓	7	466	584	700	933
↓	↓	↓	↓	↓	7.5	500	625	750	1000
↓	↓	↓	↓	↓	9	600	750	900	1200
↓	↓	↓	↓	↓	11	733	913	1100	1466
↓	↓	↓	↓	↓	12	800	1000	1200	1600
↓	↓	↓	↓	↓	13	1080	1083	1300	1733

BOARD OPTIONS

NAME	SET	PCI
SYSRST	←=0	COP resets entire system
	→=1	COP resets MPMC only
M66EN	←=0	33 MHz PCI only
	→=1	66 MHz PCI allowed
ROMSEL	←=0	Alternate Flash selected
	→=1	Standard Flash selected
PROGSEL	←=0	Swap local flash to program
	→=1	Local flash is bootable
AGENT	←=0	Standard PCI target
	→=1	Host or Free agent
PMCTYPE	←=0	Mot MPMC (MPMC PCI arb)
	→=1	VITA PrPMC (SP PCI arb)
MAPSEL	←=0	Map "A" / PRrP
	→=1	Map "B" / CHRrP
ROMLOC	←=0	Boot from Sandpoint PCI flash
	→=1	Boot from MPMC Local flash

NOTE: Above table assumes PLL_CFG(3)=0; to use PLL_CFG(3)=1, remove R94.