ERRATA SHEET

Date: 2009 August 11
Document Release: Version 1.1
Device Affected: LPC2888/01

This errata sheet describes both the functional problems and any deviations from the electrical specifications known at the release date of this document.

Each deviation is assigned a number and its history is tracked in a table at the end of the document.

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NXP Semiconductors

founded by Philips

Document revision history

Rev	Date	Description
1.1	August 11, 2009	Added ESD.1
1.0	May 23, 2008	First version

Identification

The typical LPC2888/01 devices have the following top-side marking:

LPC2888xxx

/01

xxxxxxx

xxYYWW R

The last letter in the third line (field 'R') will identify the device revision. This Errata Sheet covers the following revisions of the LPC2888/01:

Revision Identifier (R)	Comment
'(blank)'	Initial device revision

Field 'YY' states the year the device was manufactured. Field 'WW' states the week the device was manufactured during that year.

Errata Overview - Functional Problems

Functional Problem	Short Description	Device Revision the problem occurs in
Core.1	Thumb code execution is not possible from on-chip 1MB Flash	‘(blank)’

Errata Overview - AC/DC Deviations

AC/DC Deviation	Short Description	Device Revision the deviation occurs in
ESD.1	ESD weakness on the $V_{DD(DADC1V8)}$, DCDC_$V_{DDO(1V8)}$, $V_{DD1(FLASH1V8)}$, $VDD2_{(USB1V8)}$, SCL, D0/P[0], DP and LD1/ P4[5] pins.	‘(blank)’

Errata Notes

Note	Short Description
NA	NA

Functional Problems of LPC2888/01

Core.1: Thumb code execution is not possible from on-chip 1MB Flash

Introduction: The LPC2888/01 has an ARMTDMI core and it supports the Thumb Instruction set.

Problem: Thumb code cannot execute from on-chip Flash and if attempted, there will be a data abort exception.

Workaround: No known workaround. Only ARM code execution is possible from the on-chip Flash.

AC/DC Deviations of the LPC2888/01

ESD.1: ESD weakness on the $V_{DD(DADC1V8)}$, DCDC_$V_{DDO(1V8)}$, $V_{DD1(FLASH1V8)}$, $VDD2_{(USB1V8)}$, SCL, D0 P[0], DP and LD1/P4[5] pins.

Introduction: The LPC2888/01 is rated for 2kV ESD HBM specification.

Problem: The LPC2888/01 does not meet the 2kV ESD HBM requirements on the $V_{DD(DADC1V8)}$, DCDC_$V_{DDO(1V8)}$, $V_{DD1(FLASH1V8)}$, $VDD2_{(USB1V8)}$, SCL, D0/P[0], DP and LD1/P4[5] pins.

Workarounds: Observe proper ESD handling precautions on these pins.