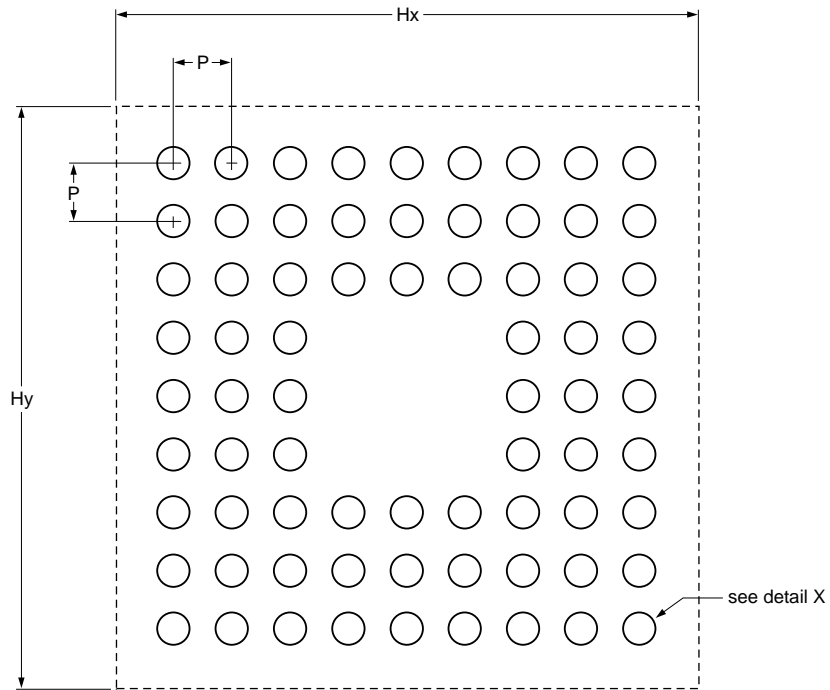





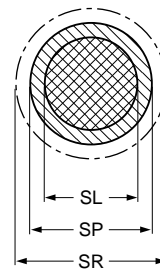
Footprint information for reflow soldering of VFPGA56 package

SOT702-1



Generic footprint pattern
Refer to the package outline drawing for actual layout

-  solder land
-  solder paste deposit
-  solder land plus solder paste
- occupied area
- solder resist



detail X

DIMENSIONS in mm

P	SL	SP	SR	Hx	Hy
0.65	0.325	0.400	0.475	7.550	5.050