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Revisions			
Rev	Description	Date	Approved
X1	Initial Design Release	22-Jul-14	Antonio Quiroz
X2	Level Translators, Arduino or FRDM support	28-Jul-14	Antonio Quiroz
X3	U1 Pin name update BQM Optimization U2	4-Aug-14	Antonio Quiroz
A	Production Release	1-Sep-14	Antonio Quiroz
B	External MCU control RGB	18-Nov-14	Antonio Quiroz

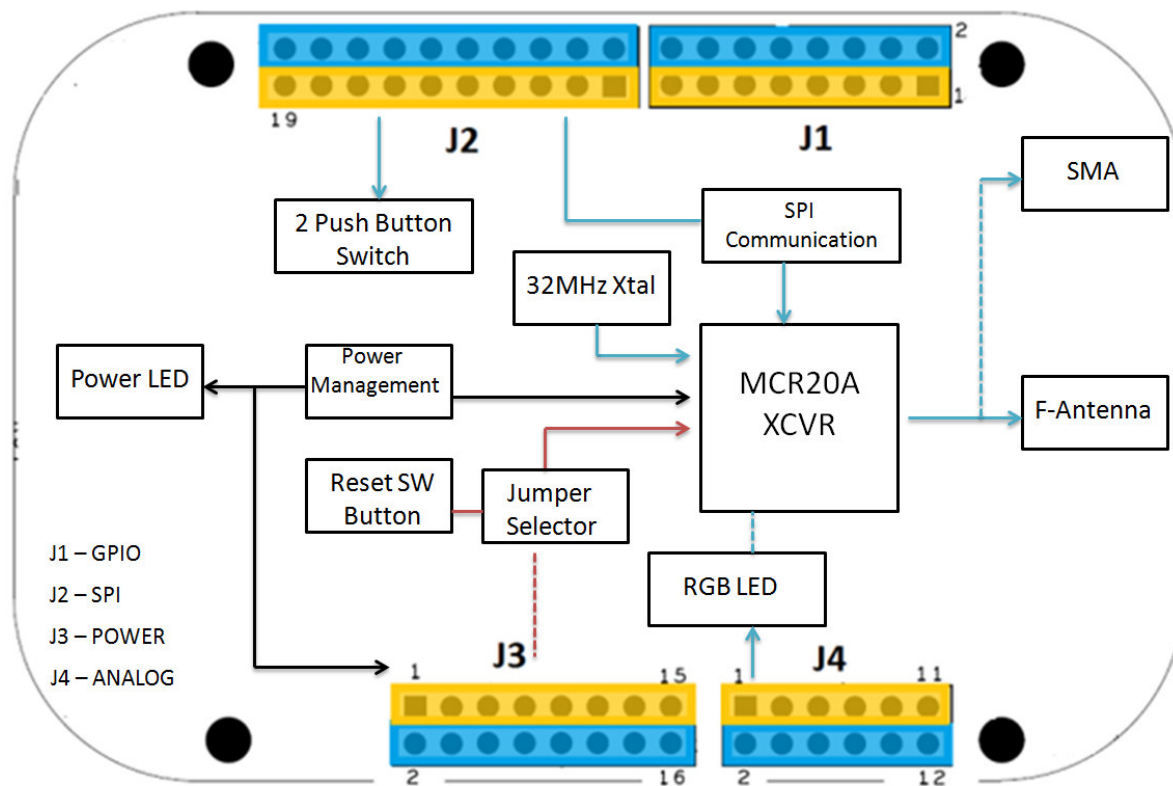
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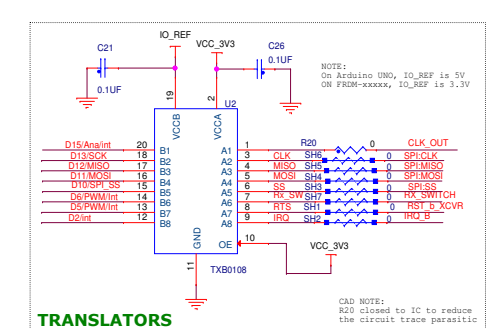
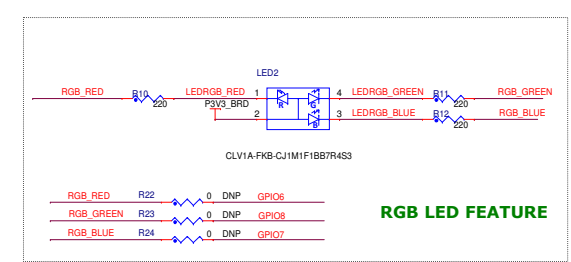
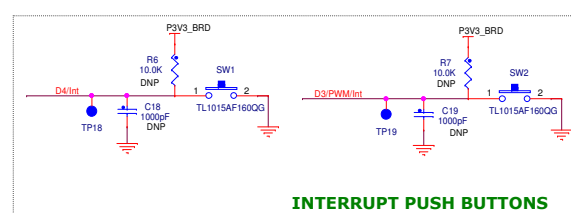
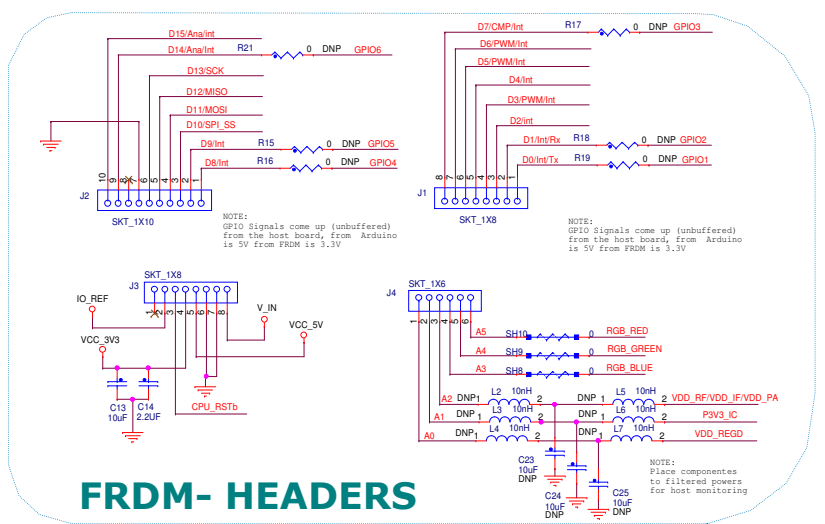
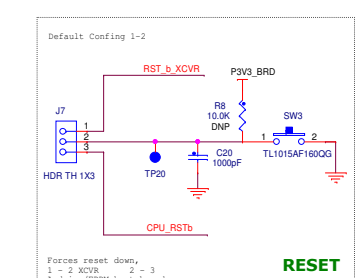
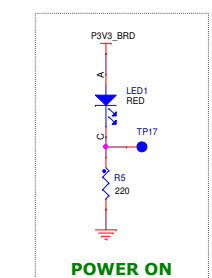
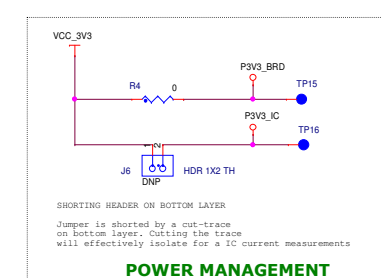
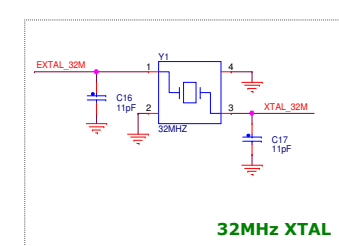
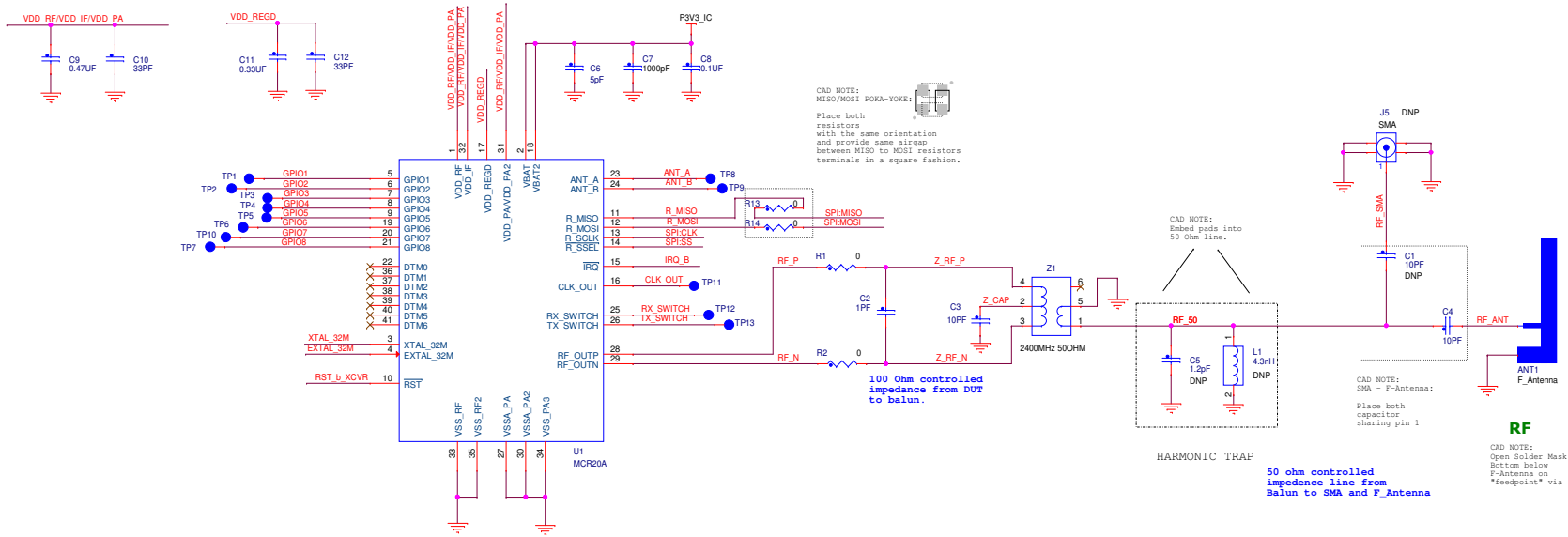
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Approved: Frank Latona		Size C	Document Number SCH-28535 PDF: SPF28535
Date: Tuesday, November 18, 2014		Sheet 1	of 3
		Rev B	

1. Interrupted lines coded with the same letter or letter combinations are electrically connected.

2. Special signal usage:
 _B Denotes - Active-Low Signal
 <> or [] Denotes - Vectored Signals

3. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.





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