

Table of Contents

2	Notes/ Block Diagram
3	MCU MCR20A

Revisions

Rev	Description	Date	Approved
X1	Initial Design Release	22-Jul-14	Antonio Quiroz
X2	Level Translators, Arduino or FRDM support	28-Jul-14	Antonio Quiroz
X3	U1 Pin name update BOM Optimization U2	4-Aug-14	Antonio Quiroz
A	Production Release	1-Sep-14	Antonio Quiroz
B	External MCU control RGB	18-Nov-14	Antonio Quiroz

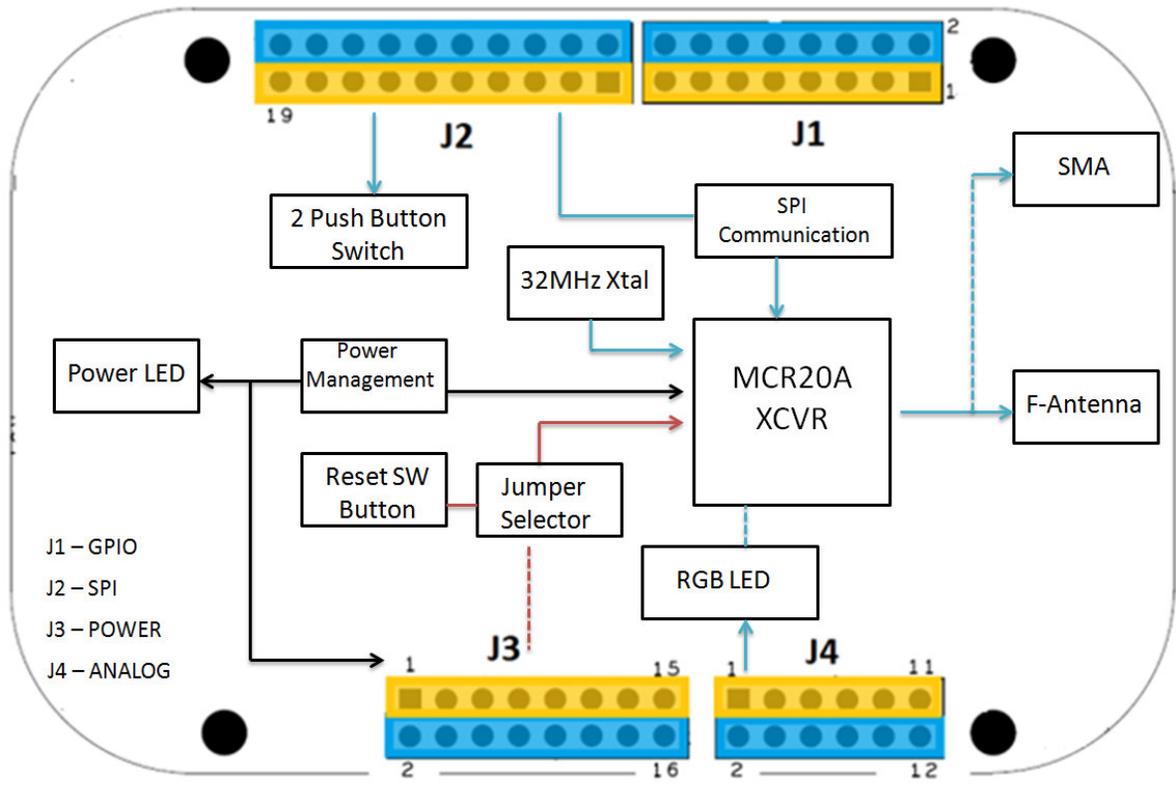
FRDM-MCR20A

		Microcontroller Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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ICAP Classification: FCP: FIUQ: X PUBI:			
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Drawn by: Antonio Quiroz	Page Title: TITLE PAGE		
Approved: Frank Latona	Size C	Document Number SCH-28535 PDF: SPF28535	Rev B
Date: Tuesday, November 18, 2014		Sheet 1 of 3	

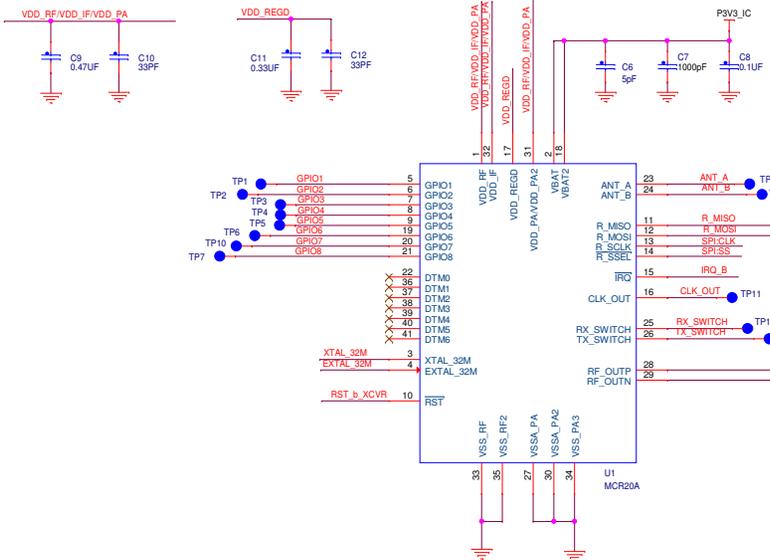
1. Interrupted lines coded with the same letter or letter combinations are electrically connected.

2. Special signal usage:
 _B Denotes - Active-Low Signal
 <> or [] Denotes - Vectored Signals

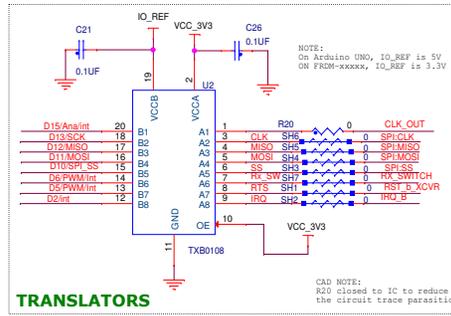
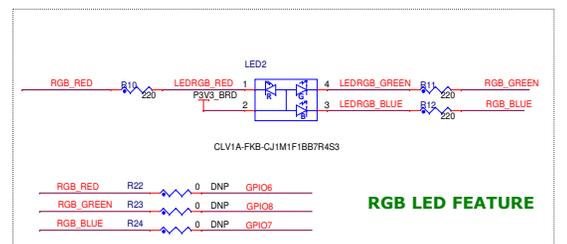
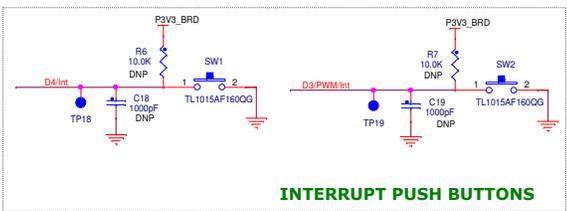
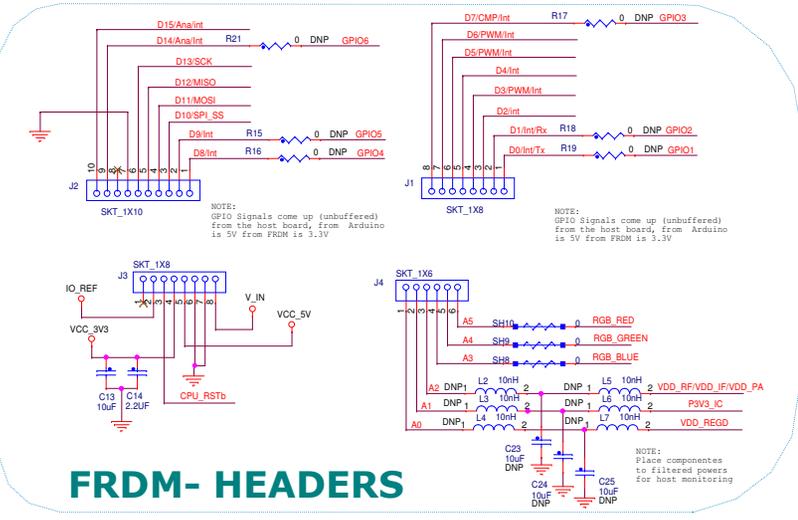
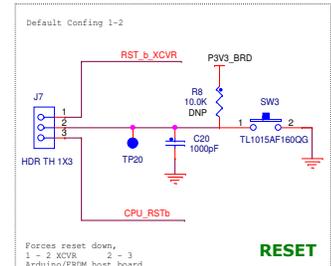
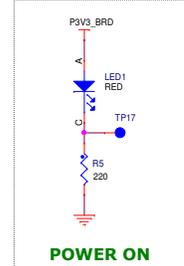
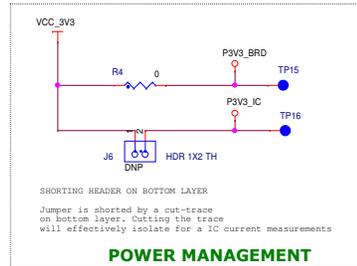
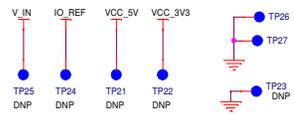
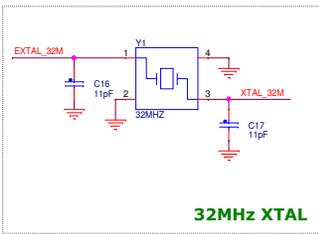
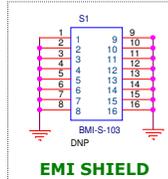
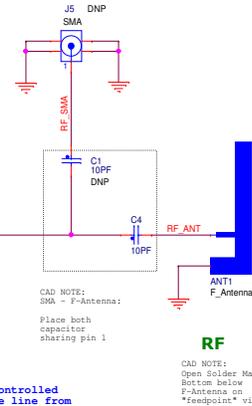
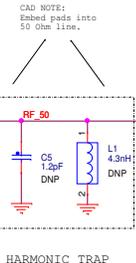
3. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



J1 – GPIO
 J2 – SPI
 J3 – POWER
 J4 – ANALOG



CAD NOTE:
MISO/MOSI POKA-YOKE:
Place both resistors with the same orientation and provide same airgap between MISO to MOSI resistors terminals in a square fashion.



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Size C Document Number SCH-28535 PDF: SPF28535 Rev B
Date: Wednesday, November 19, 2014 Sheet 3 of 3