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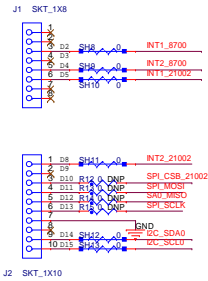
FRDM-STBC-AGM01

		RF, Analog & Sensor Group 6001 William Cannon Drive West Austin, TX 78735-6508	
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<small>ICAP Classification:</small>		<small>FCP:</small>	<small>FRQ:</small> X <small>PUB:</small>
<small>Designer:</small> Krahn S	<small>Drawing Title:</small> FRDM-STBC-AGM01		
<small>Drawn by:</small> Krahn S	<small>Page Title:</small> CONTENT		
<small>Approved:</small> Team	<small>Size</small> C	<small>Document Number</small> SCH-28566 PDF: SPF-28566	<small>Rev</small> C
<small>Date:</small> Saturday, March 28, 2015		<small>Sheet</small> 1 <small>of</small> 1	

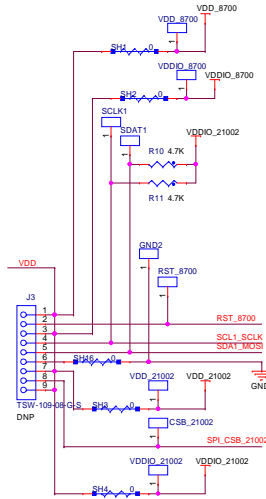
1. Unless Otherwise Specified:
All resistors are in ohms, 5%, 1/8 Watt
All capacitors are in uF, 20%, 50V
All voltages are DC
All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

		
ICAP Classification: FCP: _____ FIG: X PURL: _____		
Drawing Title: FRDM-STBC-AGM01		
Page Title: NOTES		
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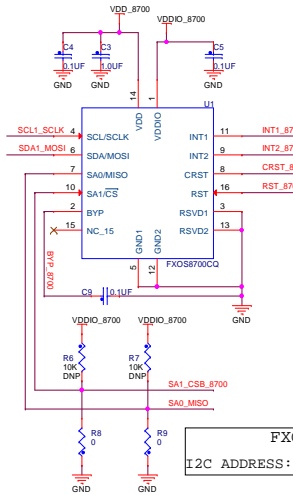
SHIELD BOARD ARDUINO HEADER



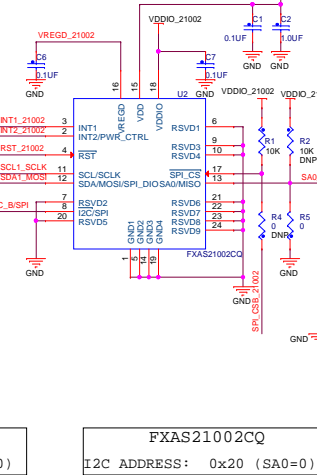
EDGE OF BRKOUT BOARD



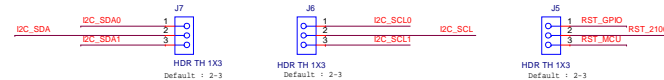
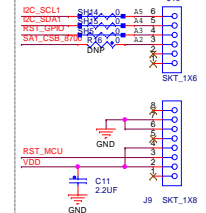
FXOS8700CQ COMBO SENSOR



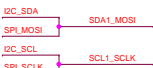
FXAS21002CQ GYROSCOPE



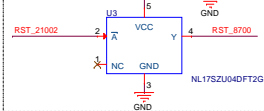
SHIELD BOARD ARDUINO HEADER



I2C/SPI SHARED PINS



INVERTER FOR RESET



ICAP Classification:	FCP:	FIG: X	PUBL:
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