
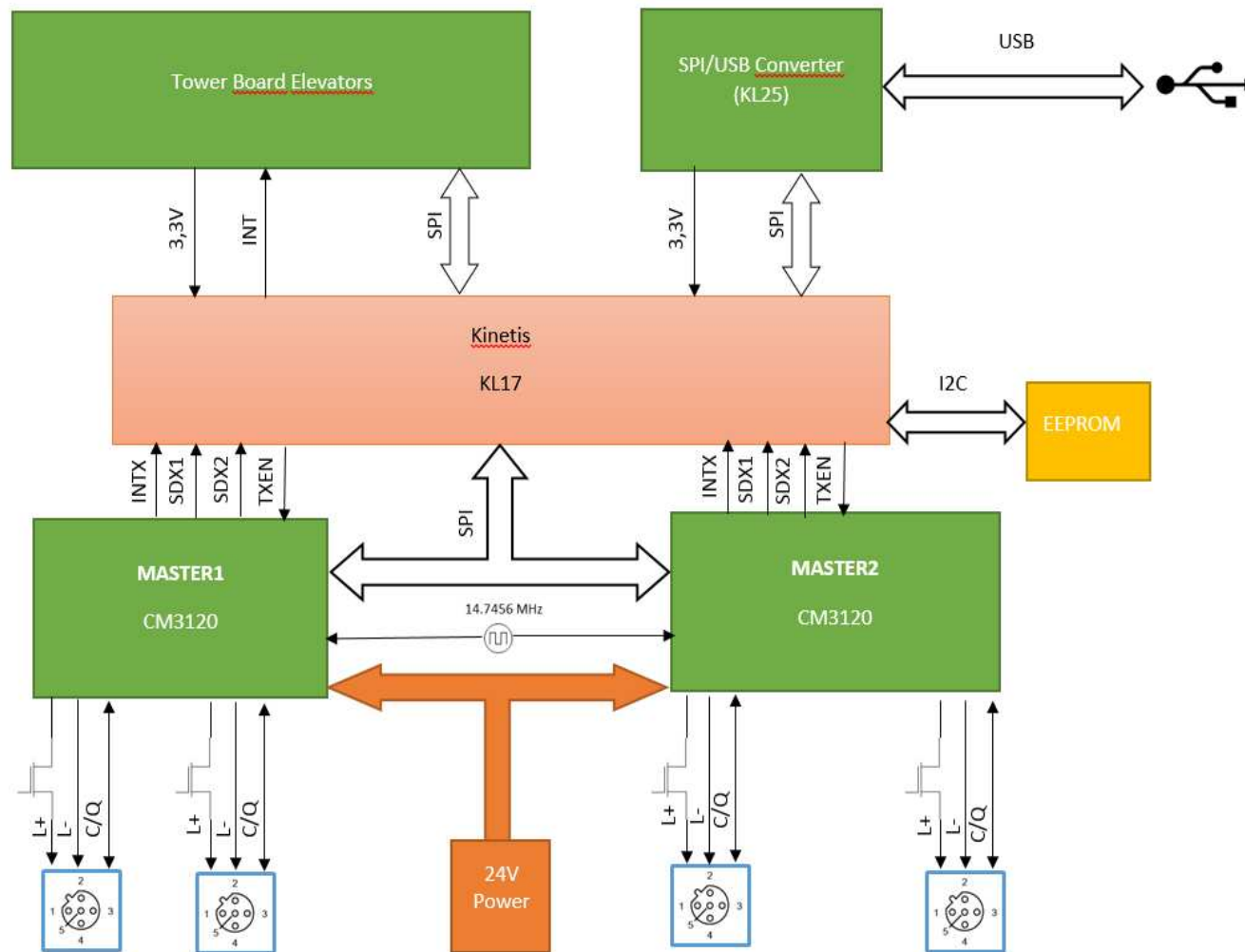


Table of Contents	
1	TITLE, TOC & REV
2	NOTES
3	IO link - Master
4	MCU_KL17
5	MCU_KL28
6	Elevator Connectors

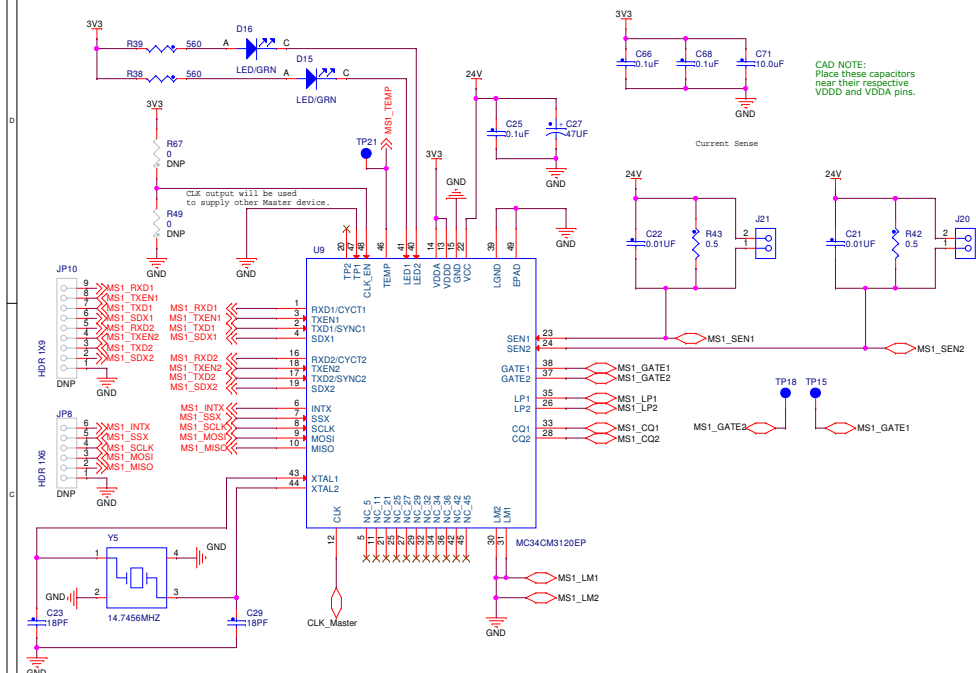
Revisions			
Rev	Description	Date	Approved
X1	Original Release	14-JUN-15	Tomas Marek
A	Prototype Manufacturing Release	23-SEP-15	Tomas Marek
B	BOM CHANGES	12-MAY-16	Tomas Marek

TWR-CM3120-EVM

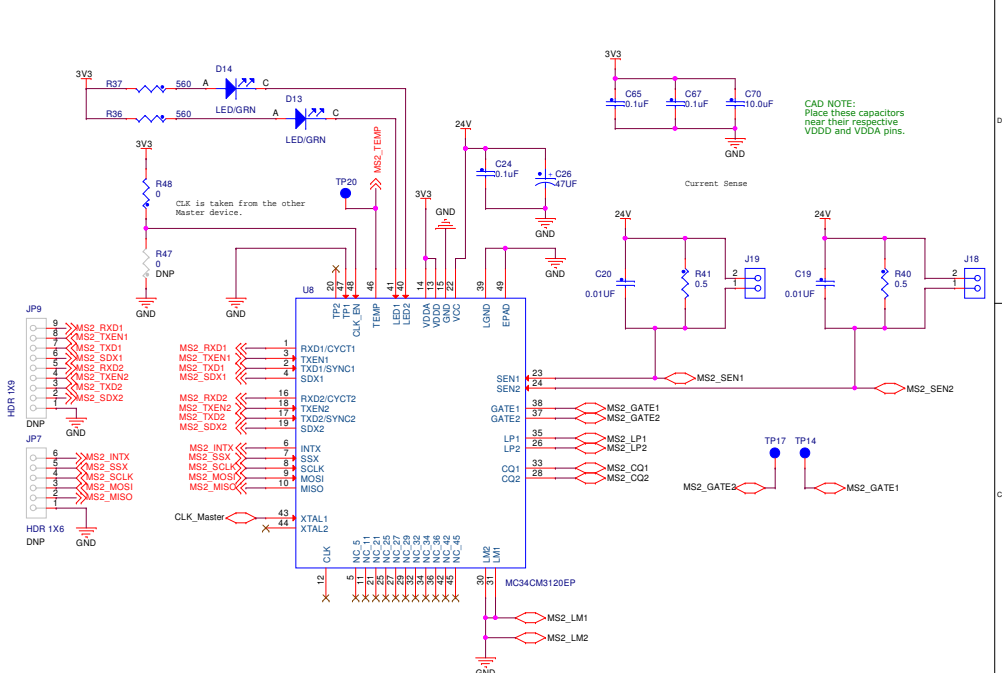
		Analog Sensor Product Group 6501 William Cannon Drive West Austin, TX 78735-5058	
<small>This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.</small>			
<small>ICAP Classification:</small> CP:		<small>IUC:</small> X	<small>PUB:</small>
<small>Designer:</small> Tomas Marek	<small>Drawing Title:</small> TWR-CM3120-EVM		
<small>Drawn by:</small> Tomas Marek	<small>Page Title:</small> TITLE, TOC & REV		
<small>Approved:</small> Tomas Marek	<small>Size</small> C	<small>Document Number</small> SCH-29083 PDF: SPF-29083	<small>Rev</small> B
<small>Date:</small> Wednesday, June 15, 2016		<small>Sheet</small> 1 of 6	



IO-Link MASTER 1

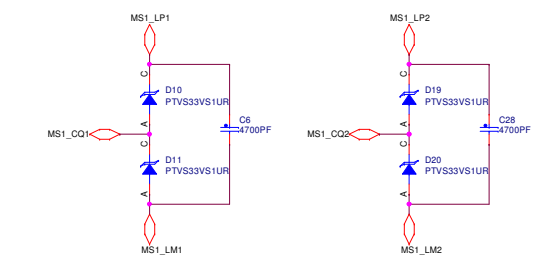


IO-Link MASTER 2

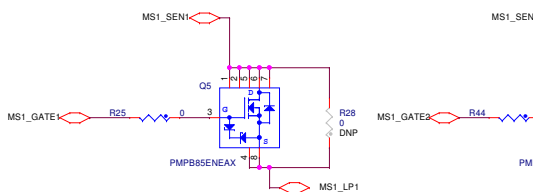


IO-Link Channel 1 and 2

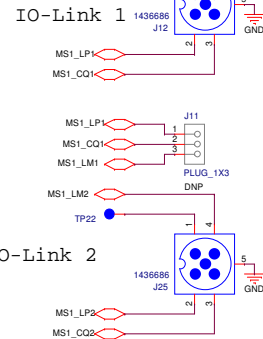
Channel Protection



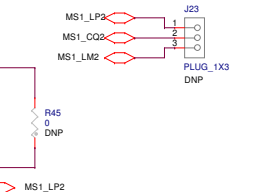
MOSFETs



M12 Connectors

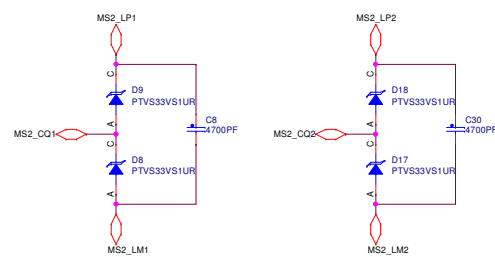


IO-Link 2

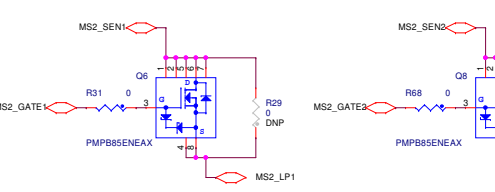


IO-Link Channel 2 and 3

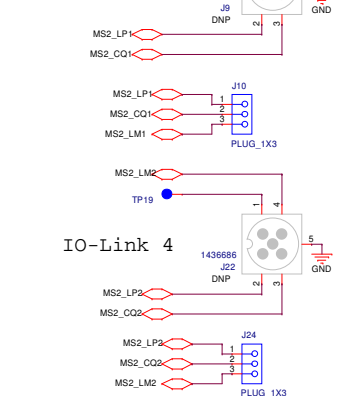
Channel Protection



MOSFETs

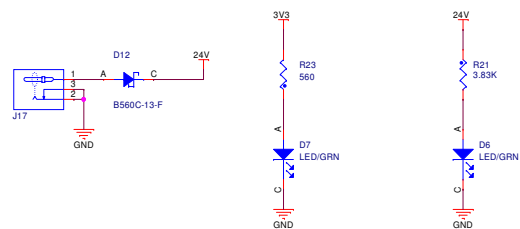


M12 Connectors

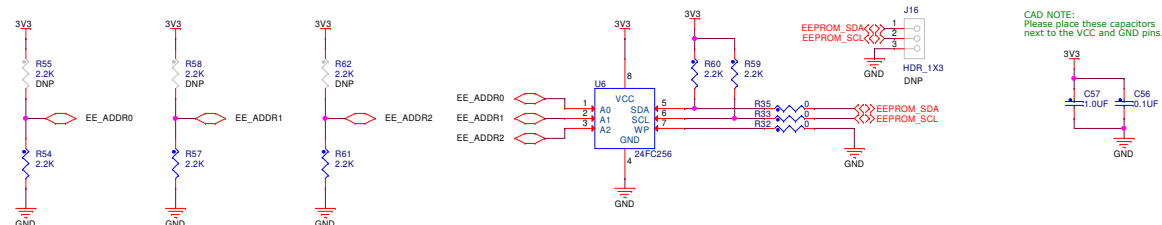


ICAP Classification: CP: IUC: X PUB:			
Drawing Title: TWR-CM3120-EVM			
Page Title: IO link - Master			
Size C	Document Number	SCH-29083 PDF: SPF-29083	Rev B
Date: Wednesday, June 15, 2016	Sheet	3	of 6

Power/Power Indicators

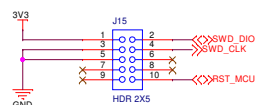


EEPROM

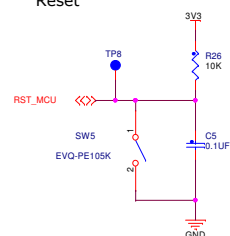


MCU Circuitry

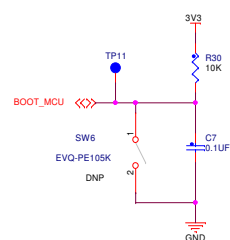
SWD Connector



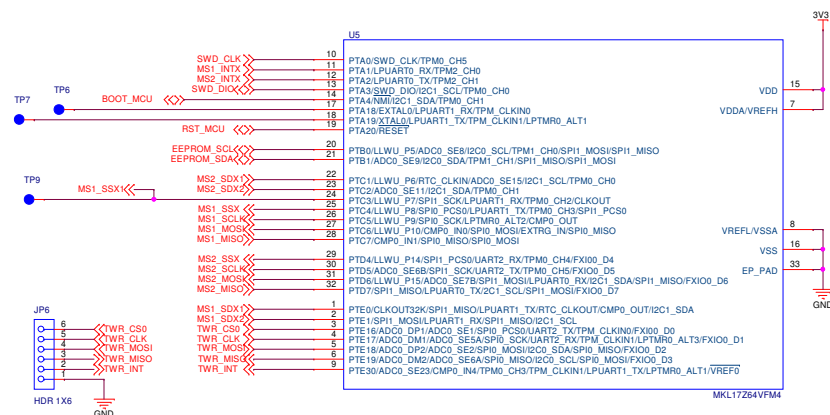
Reset



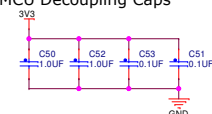
ROM Bootloader Activation



MCU KL17



MCU Decoupling Caps

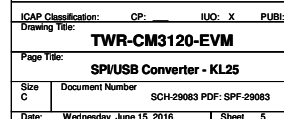
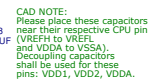
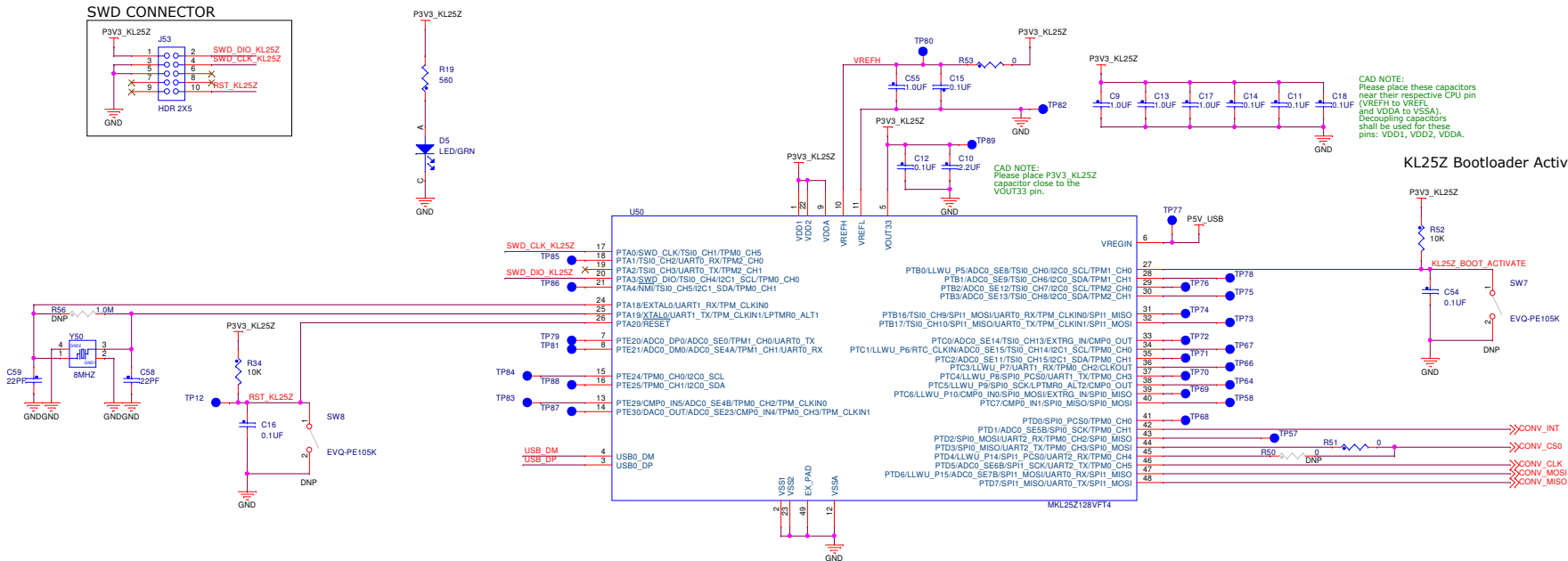
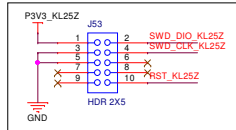
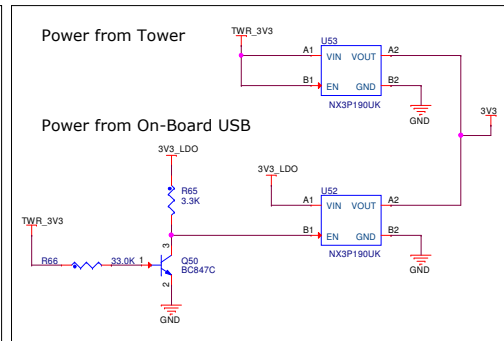
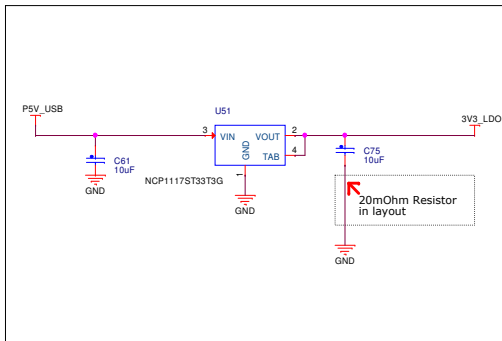
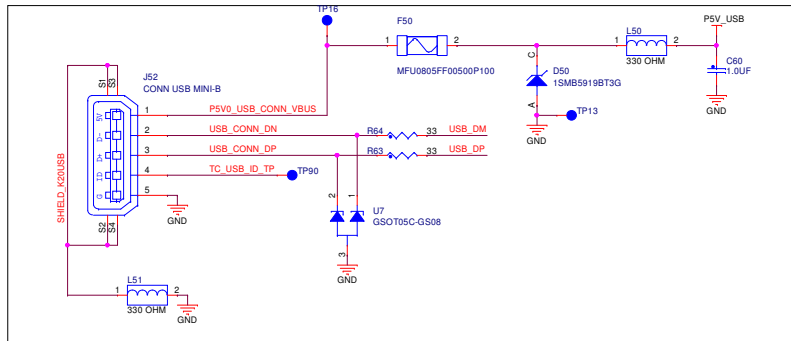


CAD NOTE:
Place these capacitors
near their respective CPU pin
(VDDA to VSSA)

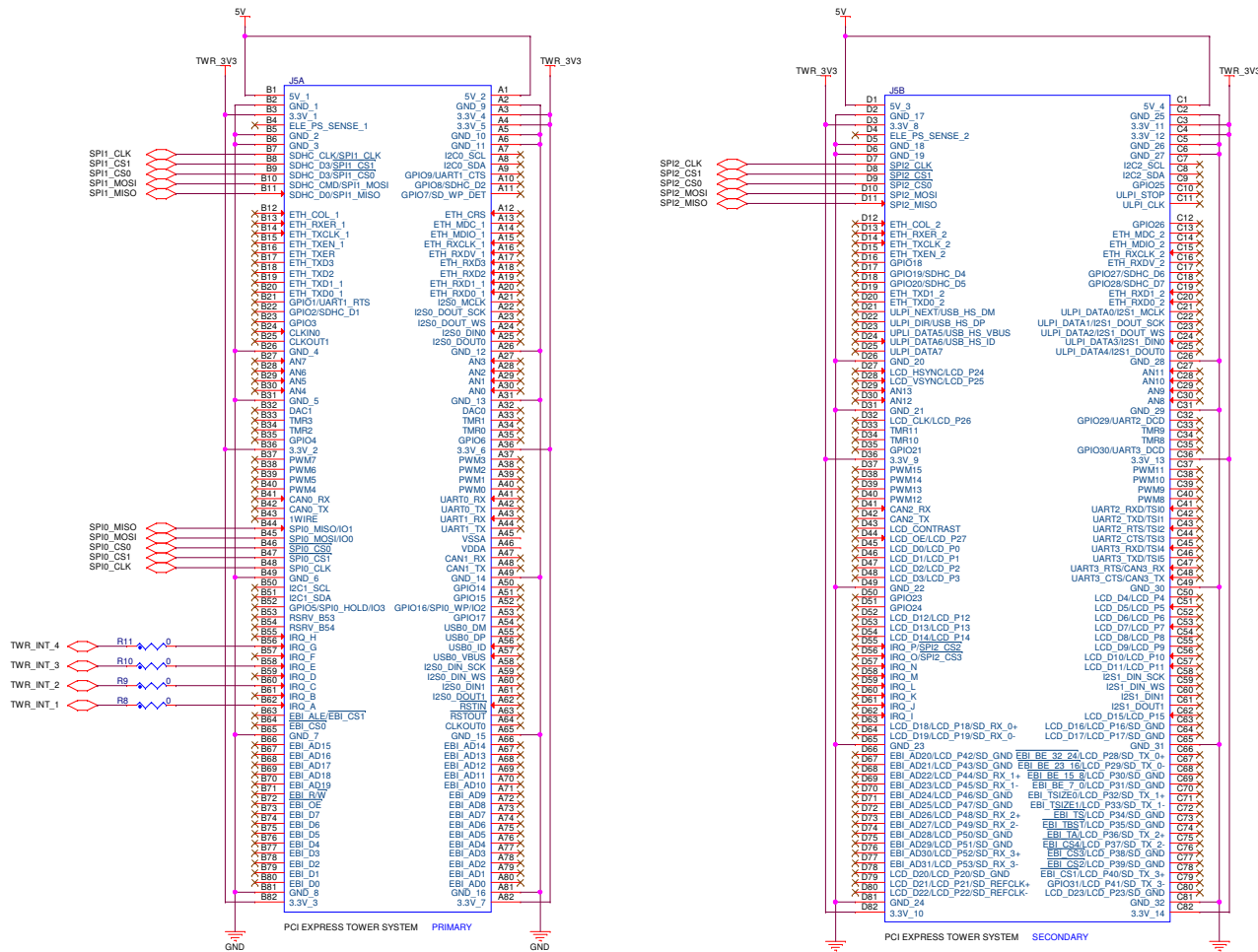


ICAP Classification:		CP:	IUO: X	PUBI:
Drawing Title:				
TWR-CM3120-EVM				
Page Title:				
MCU_KL17				
Size C	Document Number			Rev B
	SCH-29083 PDF: SPF-29083			
Date:	Wednesday, June 15, 2016	Sheet	4 of 6	

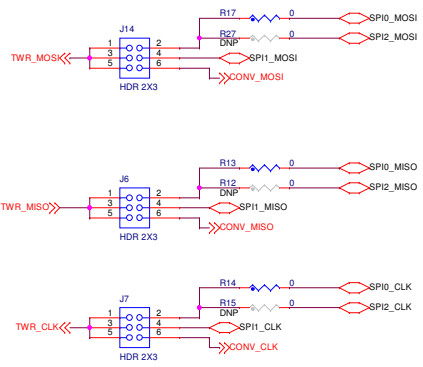
1



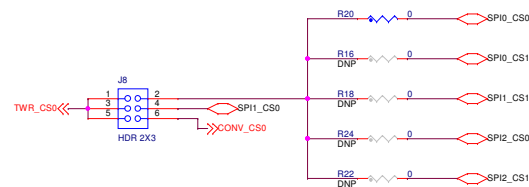
Test Points/Connectors



SPI Switch



Interrupt Switch



ICAP Classification:		CP:	IUC: X PUB:
Drawing Title:		TWR-CM3120-EVM	
Page Title:		Elevator Connectors	
Size C	Document Number	SCH-29083 PDF: SPF-29083	Rev B
Date:	Wednesday, June 15, 2016	Sheet	6 of 6