


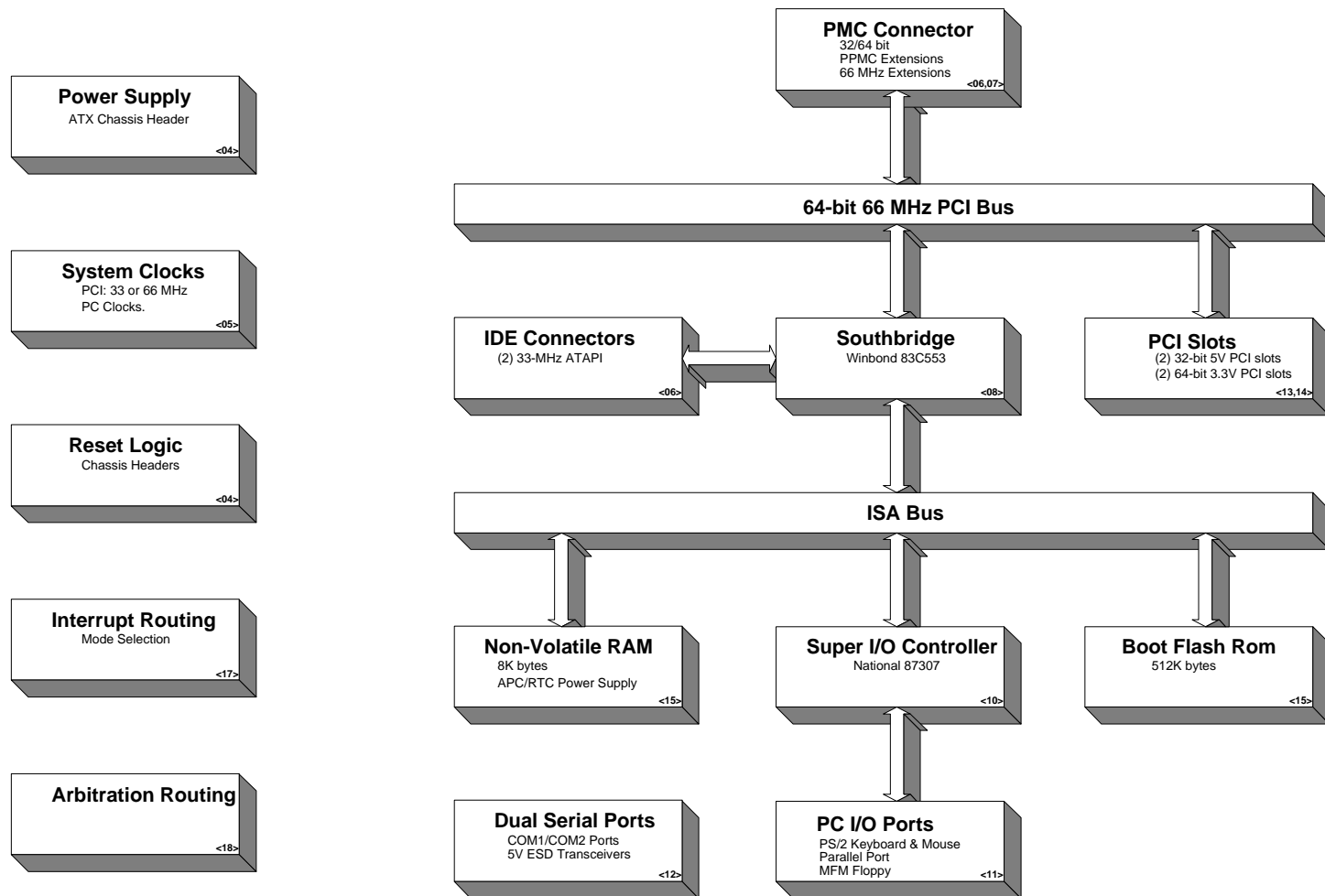
S A N D P O I N T
SiO₂*

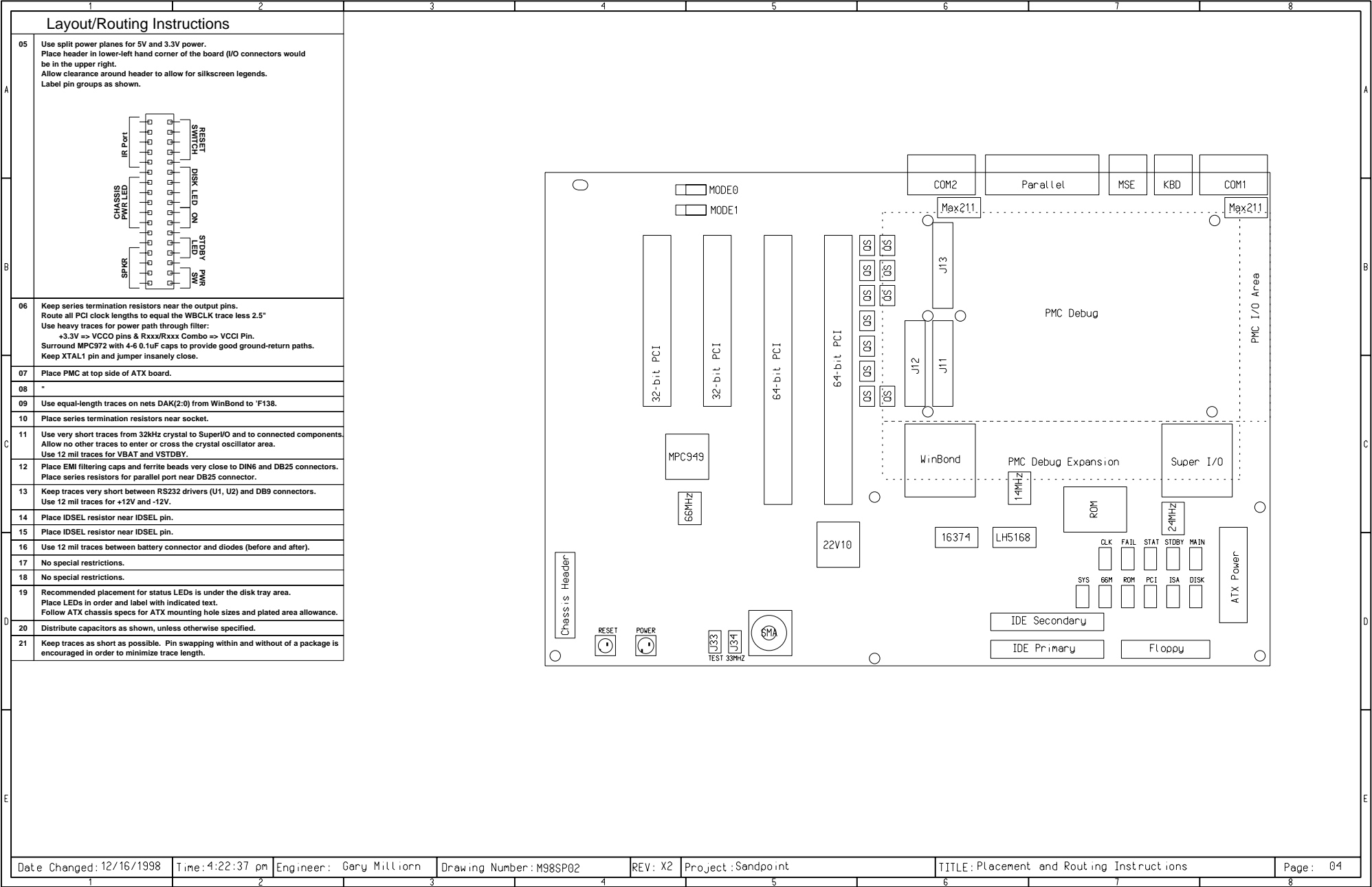
INITIAL RELEASE V1.0

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accuracy of the information contained herein. Contact
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***Digital DNA**
from Motorola

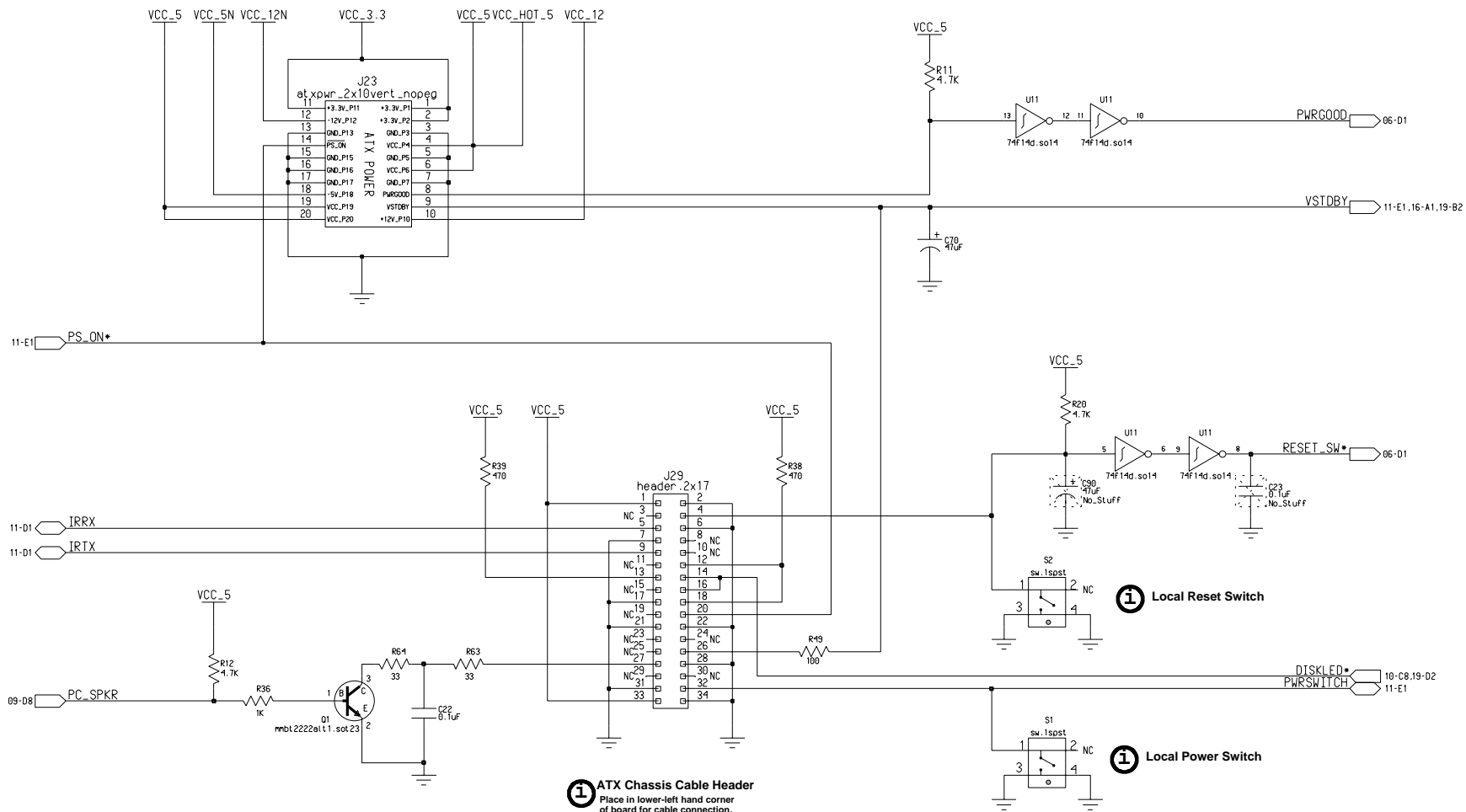
1		2		3		4		5		6		7		8																									
Schematic Notes		<div>SANDPOINT</div> <div>SiO₂*</div>												Page	Contents																								
A	1.													Unless otherwise specified: All resistors are SMD0805, in ohms, 0.1W, +/-5% All capacitors are SMD0603, in microfarads (uF), +/-20%. All inductances are in microhenries (uH). All ferrites are Z=50 ohms at 100 MHz. All fuses are self-resetting polyswitch (PTC) devices. Board impedance is 50-60 ohms.	01	Cover Page																							
	2.													Integrated circuits have default connections to power and ground unless explicitly shown otherwise. Global power connections are: GND VCC_3.3V VCC_5V	02	General Information																							
	3.													Part numbers used are for reference only; compatible parts may be used; refer to the bill of materials.	03	Block Diagram																							
	4.													Motorola and the Motorola logo are registered trademarks of Motorola. PowerPC is a trademark of IBM. Other trademarks are the respective property of their respective copyright holders. Diane, I am holding in my hand a box of small, chocolate bunnies. All rights reserved.	04	Routing and Layout Information																							
	5.													The sheet-to-sheet cross reference format is: Sheet "-" VertZoneLetter HorizZoneNumber	05	Power Supply																							
	6.													Components surrounded by a dashed/crossed-out box are not to be installed by default; they are for test or manufacturing purposes only: <div></div>	06	Processor PMC Socket																							
B	7.	All buses follow big-endian bit numbering order (bit 0 is the most-significant bit), except where industry standards apply (i.e. PCI). Little-endian numbering is noted at the source component.	07	64-bit PCI PMC Connector																																			
			08	PCI/ISA Bridge																																			
			09	IDE Connectors																																			
			10	Super I/O Controller																																			
			11	Floppy, PC I/O Connectors																																			
			12	Serial Ports																																			
C			13	PCI Slots #1, #2																																			
			14	PCI Slots #3, #4																																			
			15	NVRAM, Battery power																																			
			16	Interrupt Configuration																																			
			17	Arbitration Configuration																																			
			18	Spares; Status LEDs; Chassis Grounds																																			
D			19	Bypass capacitors																																			
			20	Global pullups/pulldowns																																			
E	Team Sandpoint												REV		DATE	CHANGES																							
	Cindy Black												X1	98MAR23	Original																								
	Ivan Erickson												X2	(*DEC11	Connector orientation; crosspoint tweaks.																								
		Gary Million		PCB CAD												Date Changed: 12/11/1998			Time: 5:02:02 pm			Engineer: Gary Million			Drawing Number: M98SP02			REV: X2			Project: Sandpoint			TITLE: Information, please.			Page: 02		





1 ATX Chassis Power

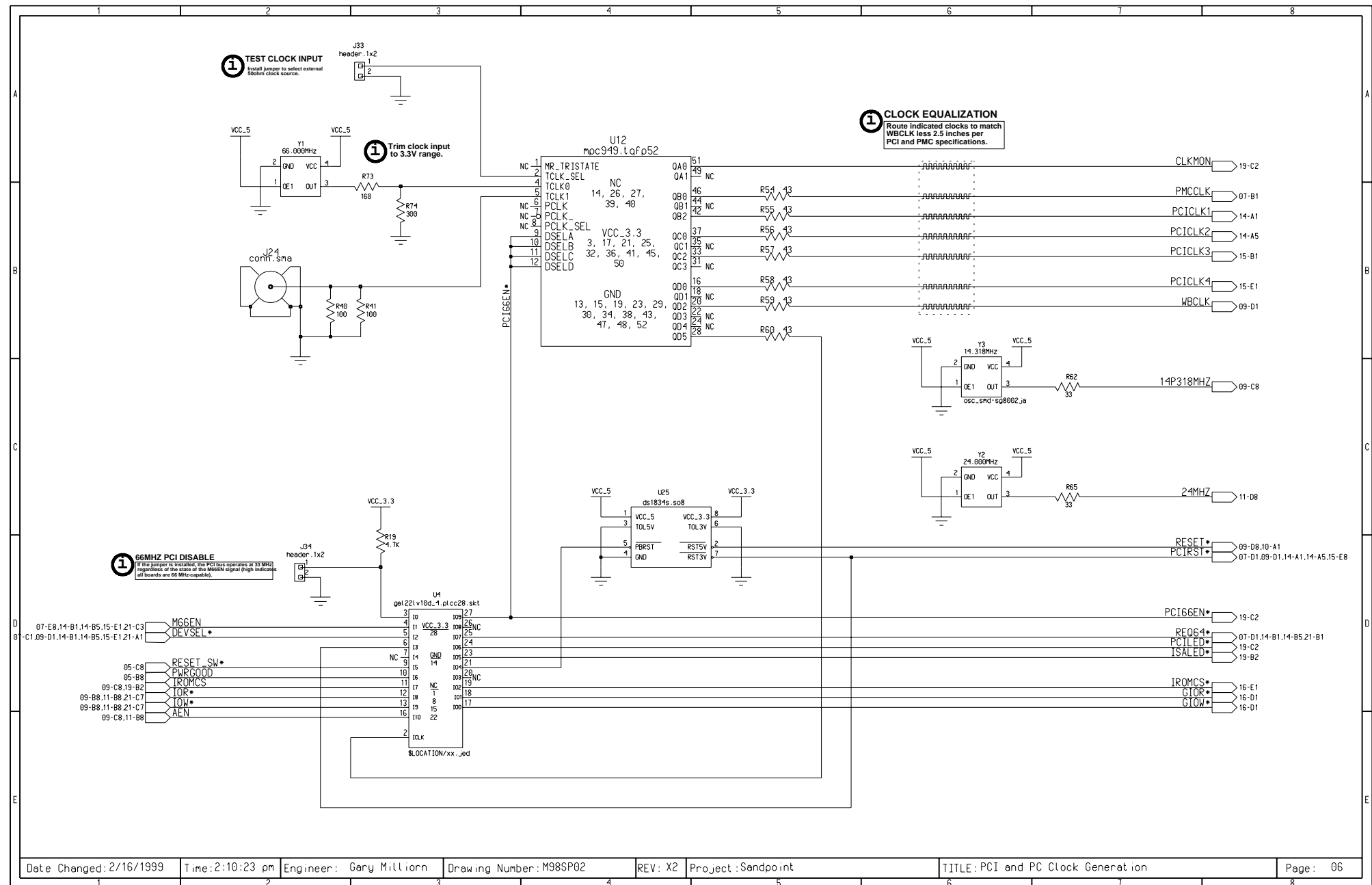
Route VCC and POS3.3V on separate power planes.



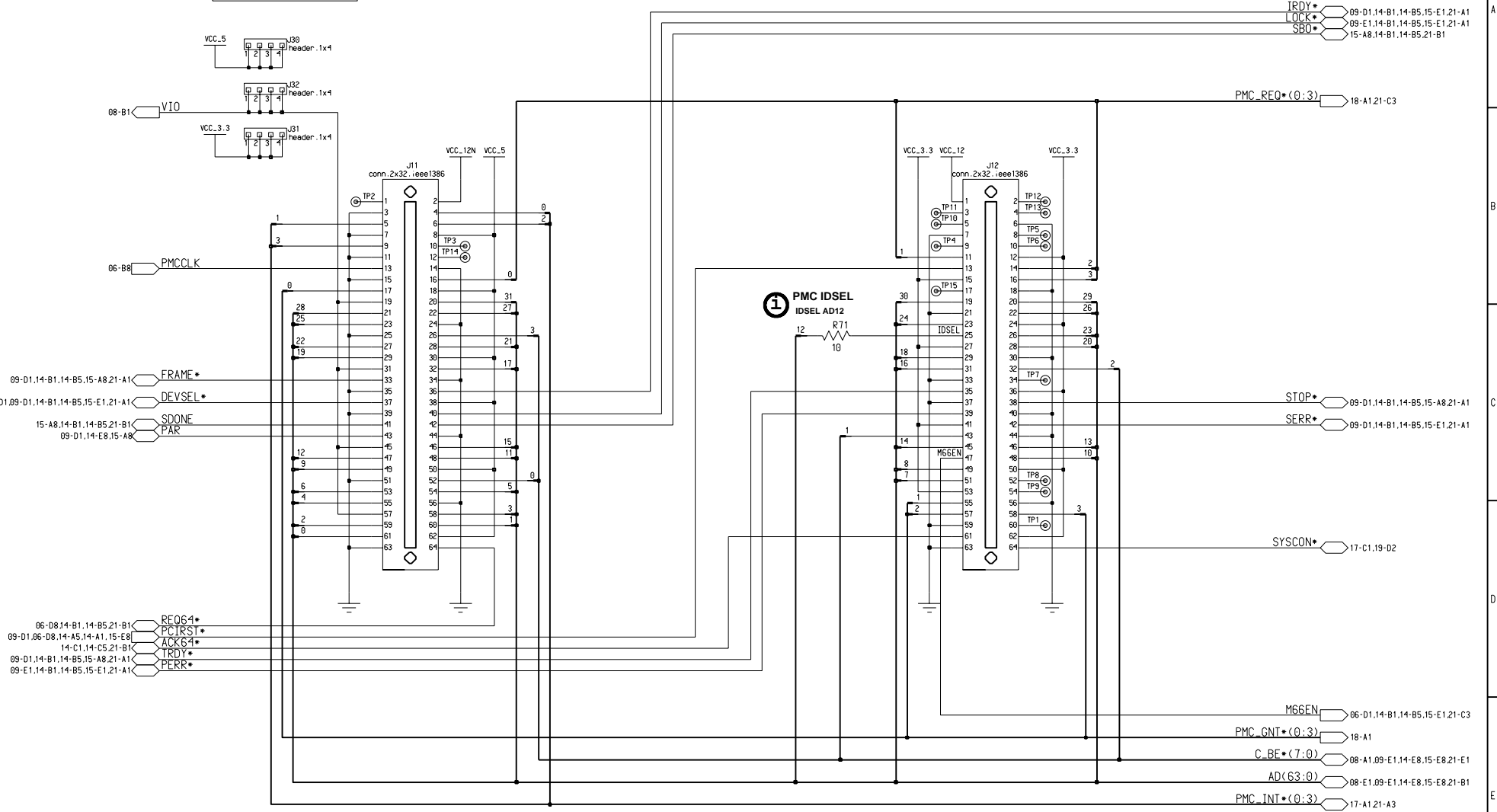
1 ATX Chassis Cable Header
Place in lower-left hand corner of board for cable connection.

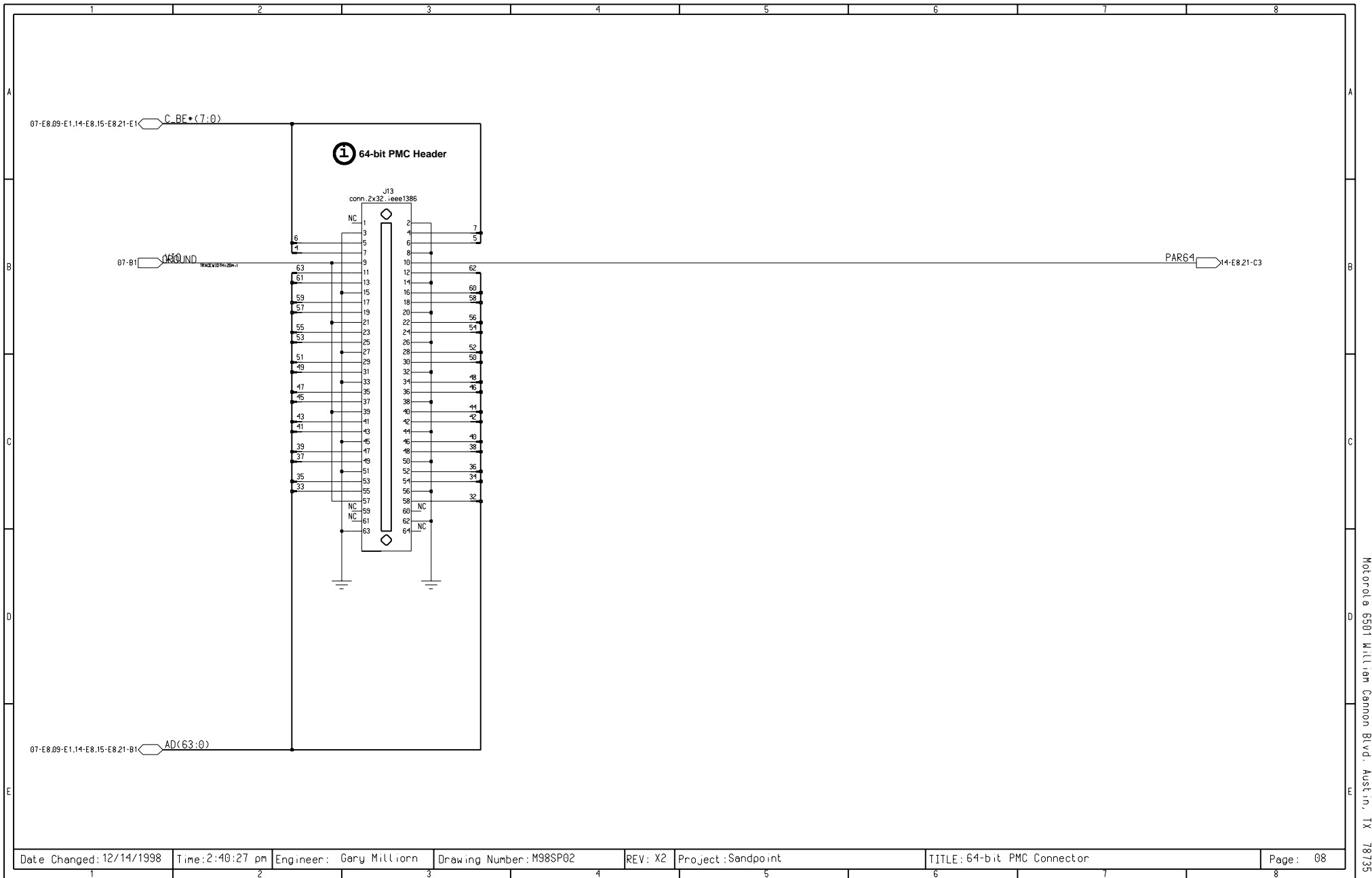
1 Local Reset Switch

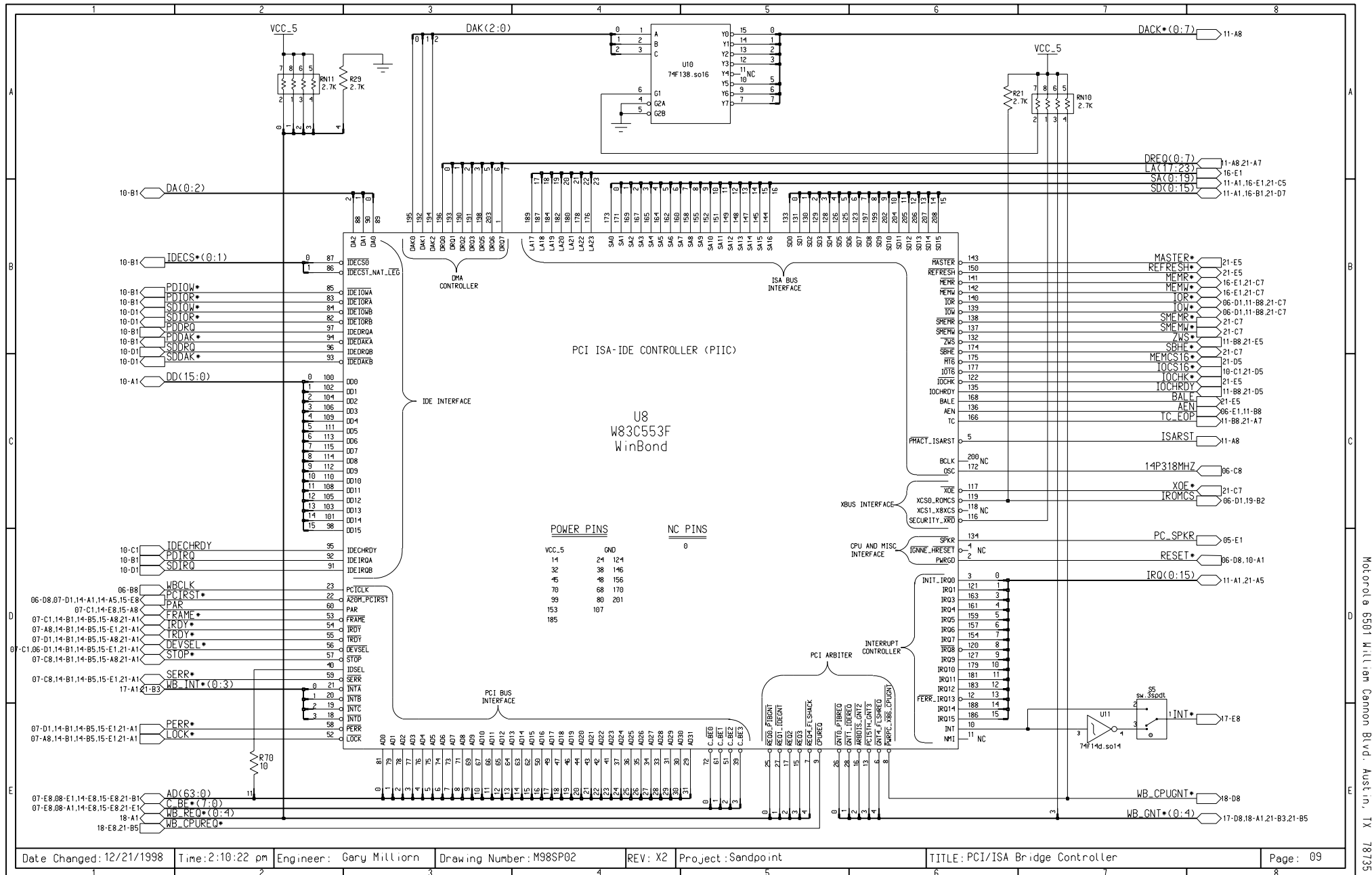
1 Local Power Switch

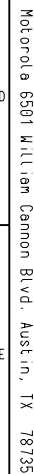


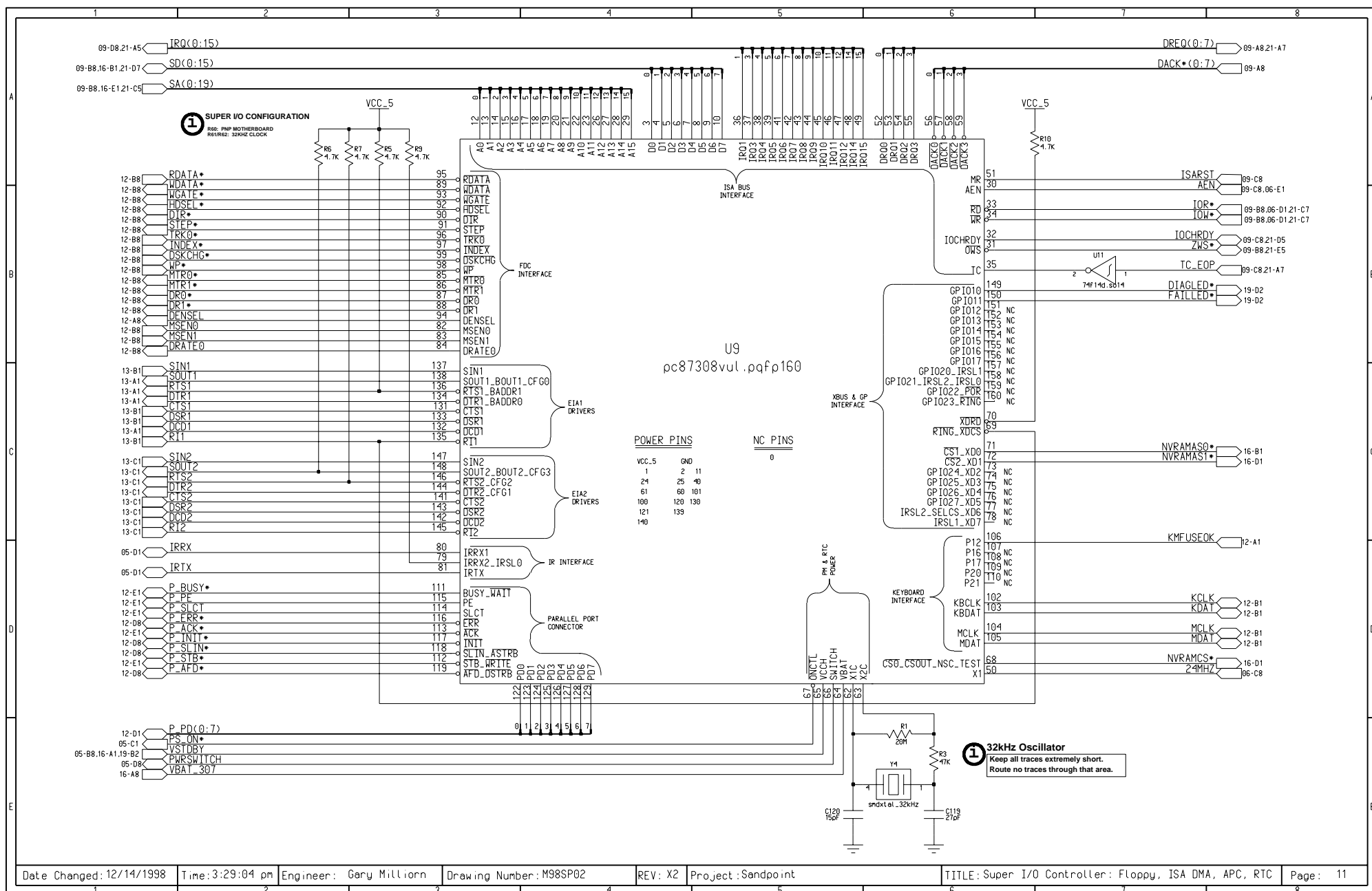
1 V(I/O) SELECTION
Select 3.3V or 5V V(I/O) option.
Arranged as a 3x4 Berg

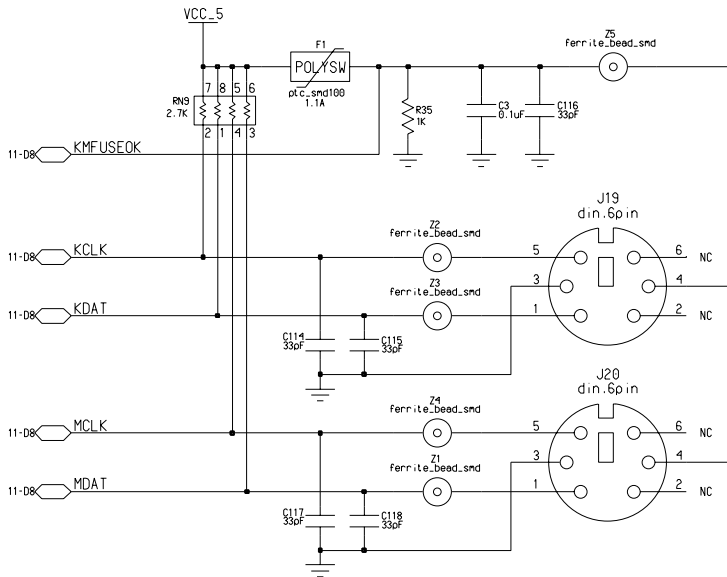








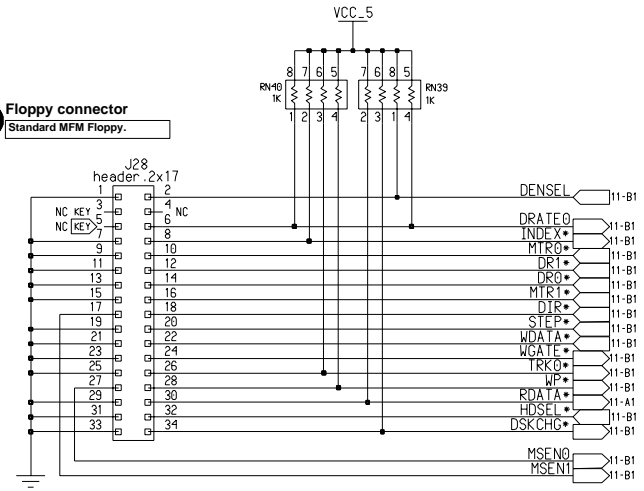




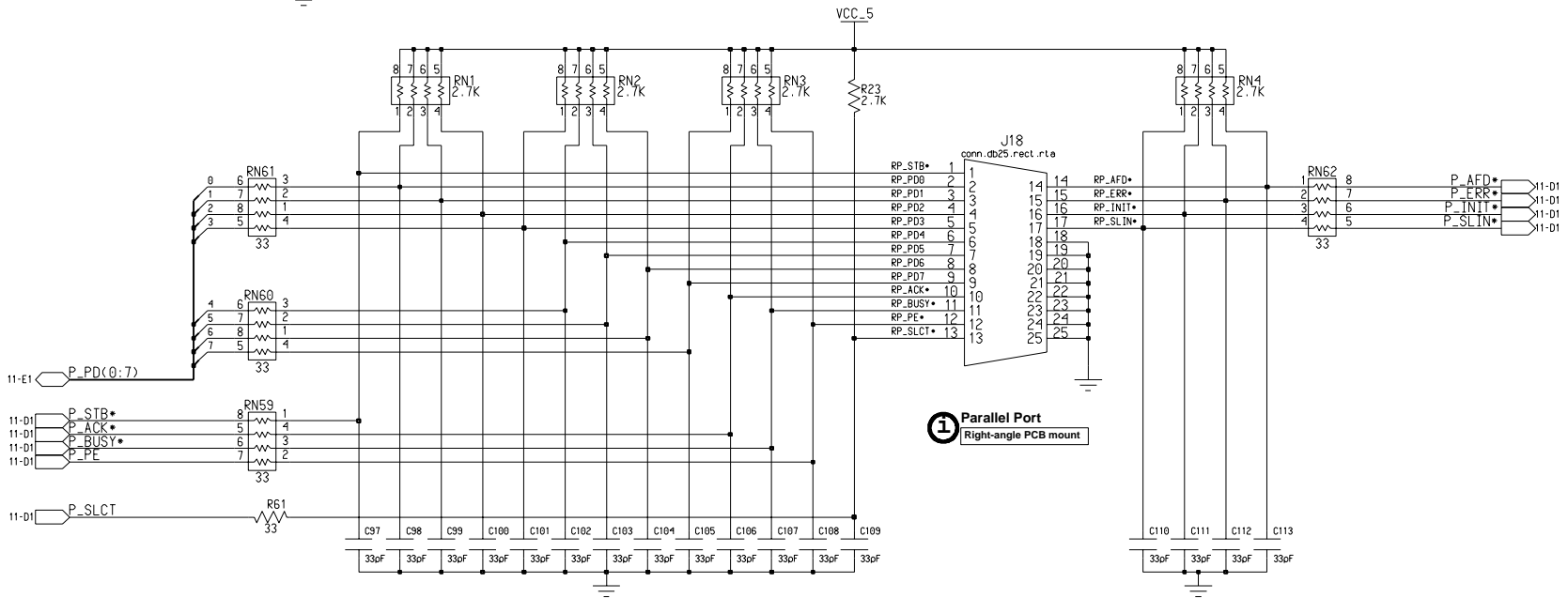
① PS/2 Keyboard

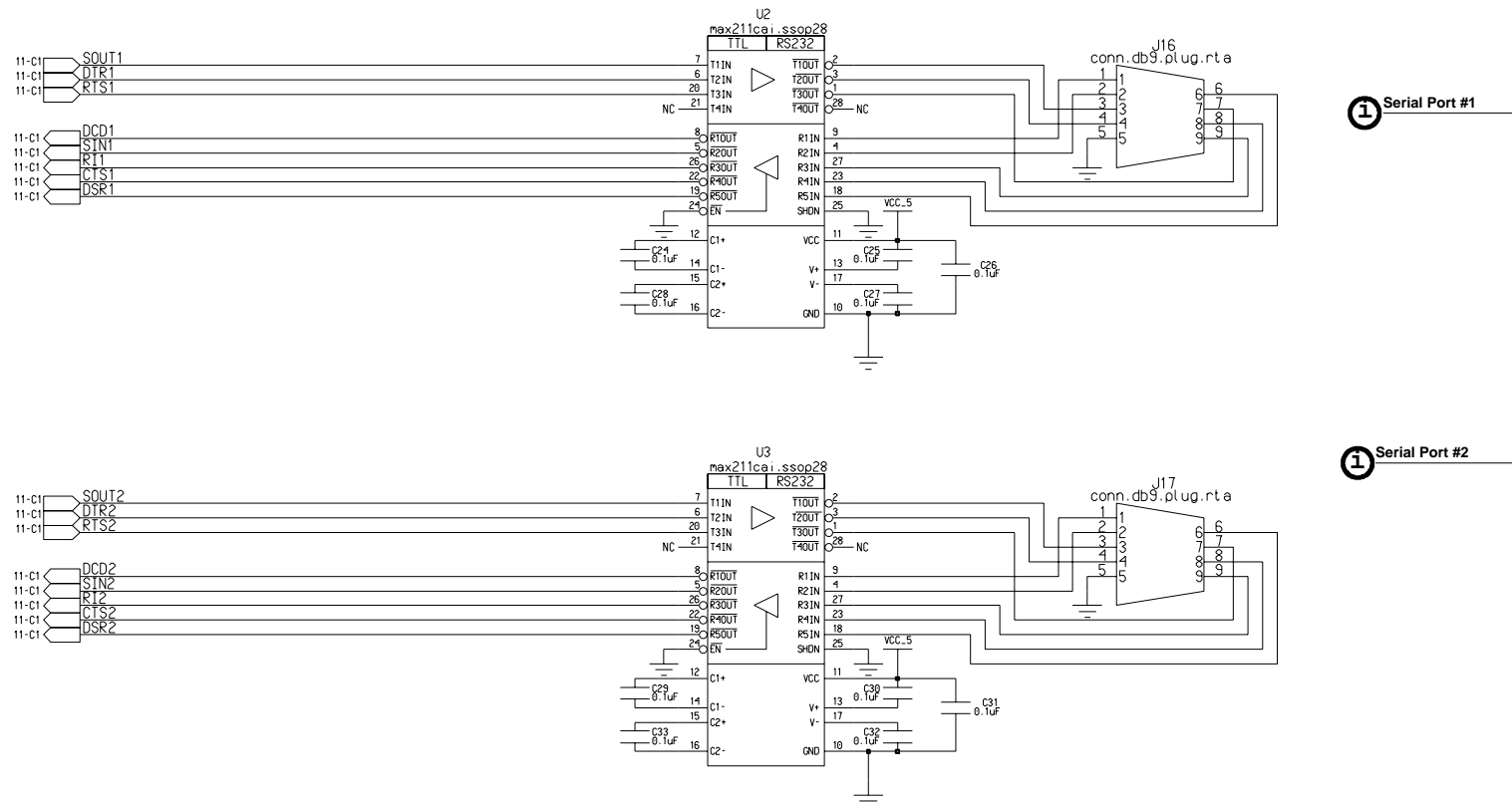
① PS/2 Mouse

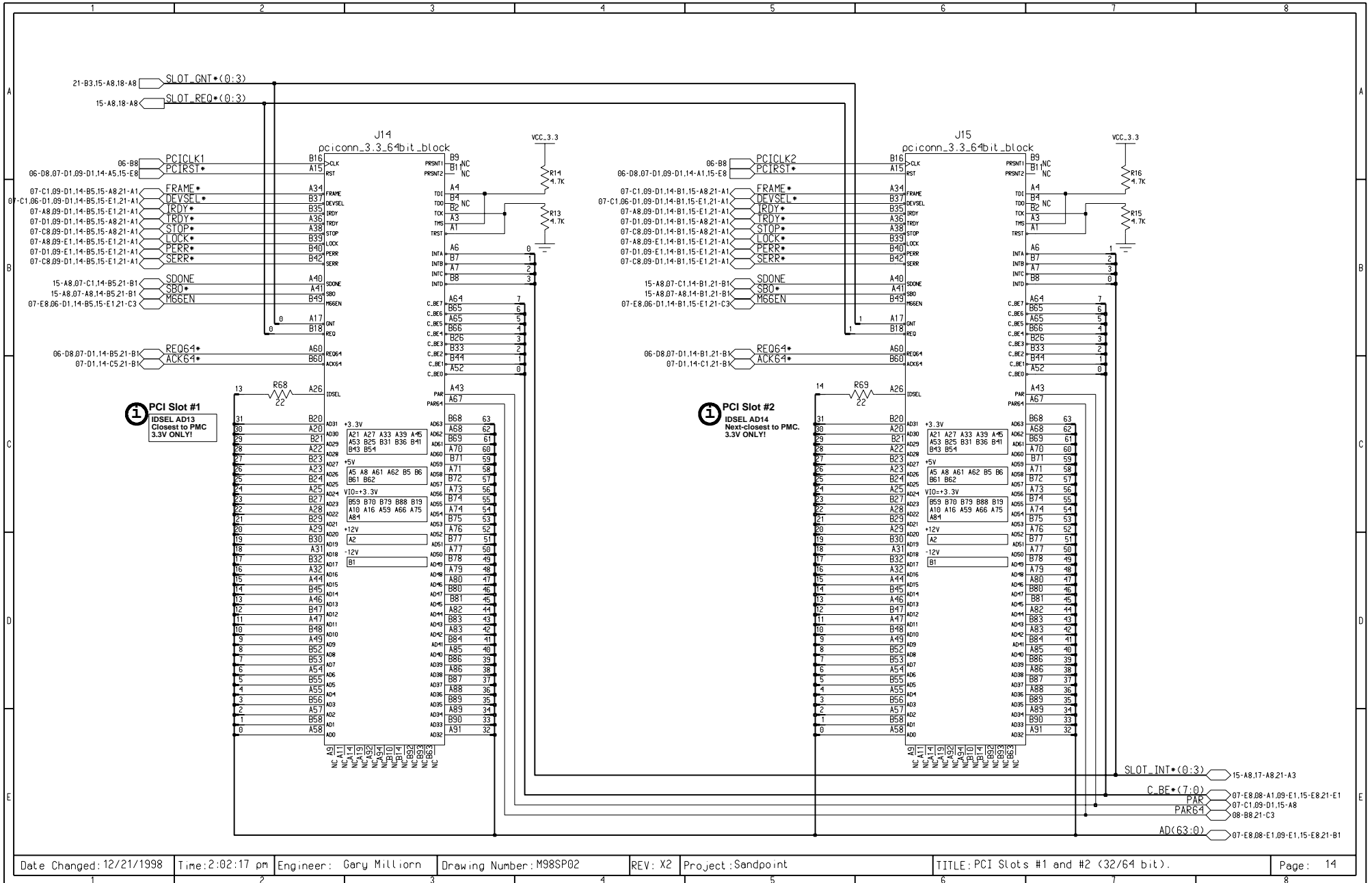
① Floppy connector
Standard MFM Floppy.

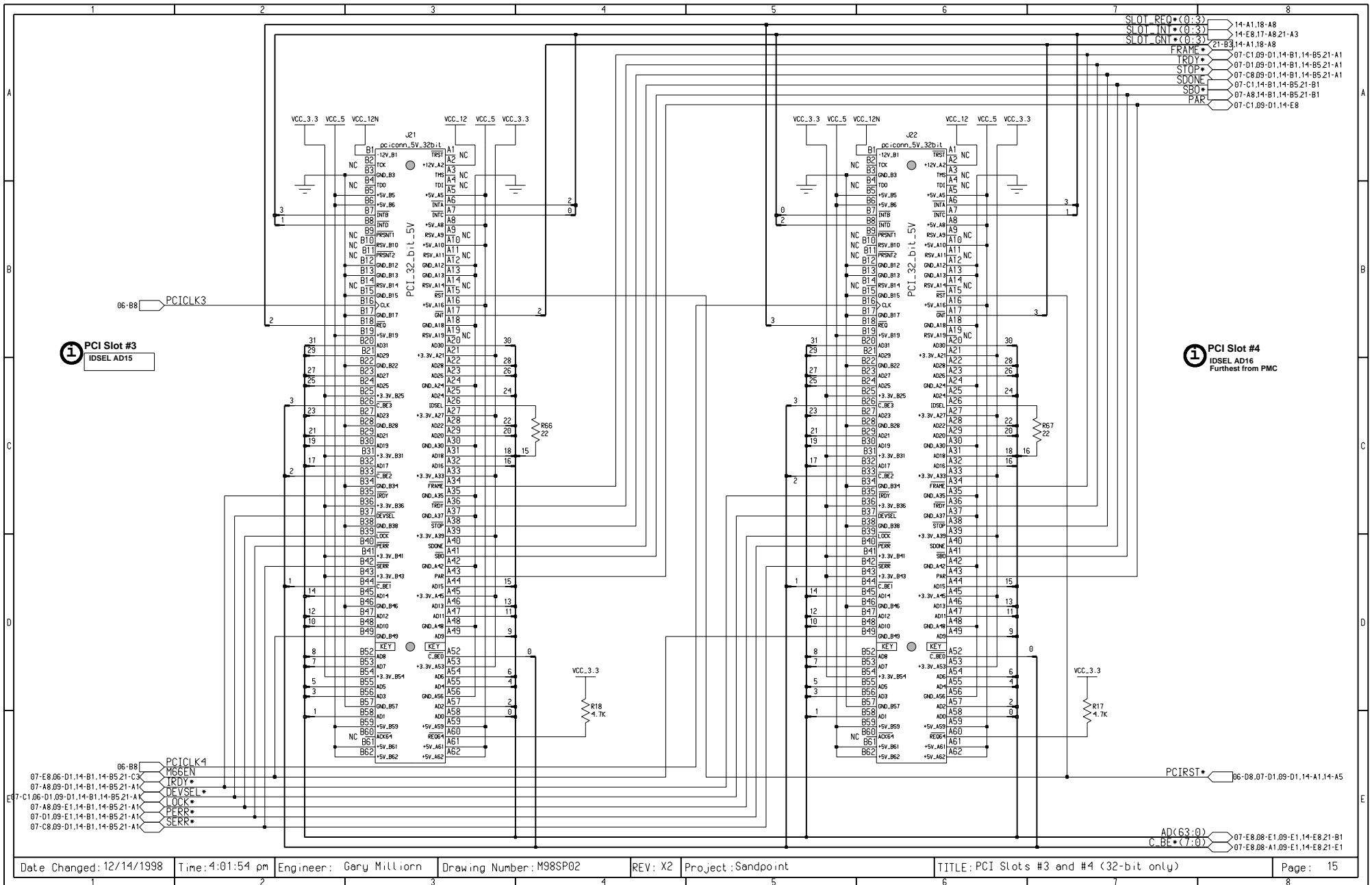


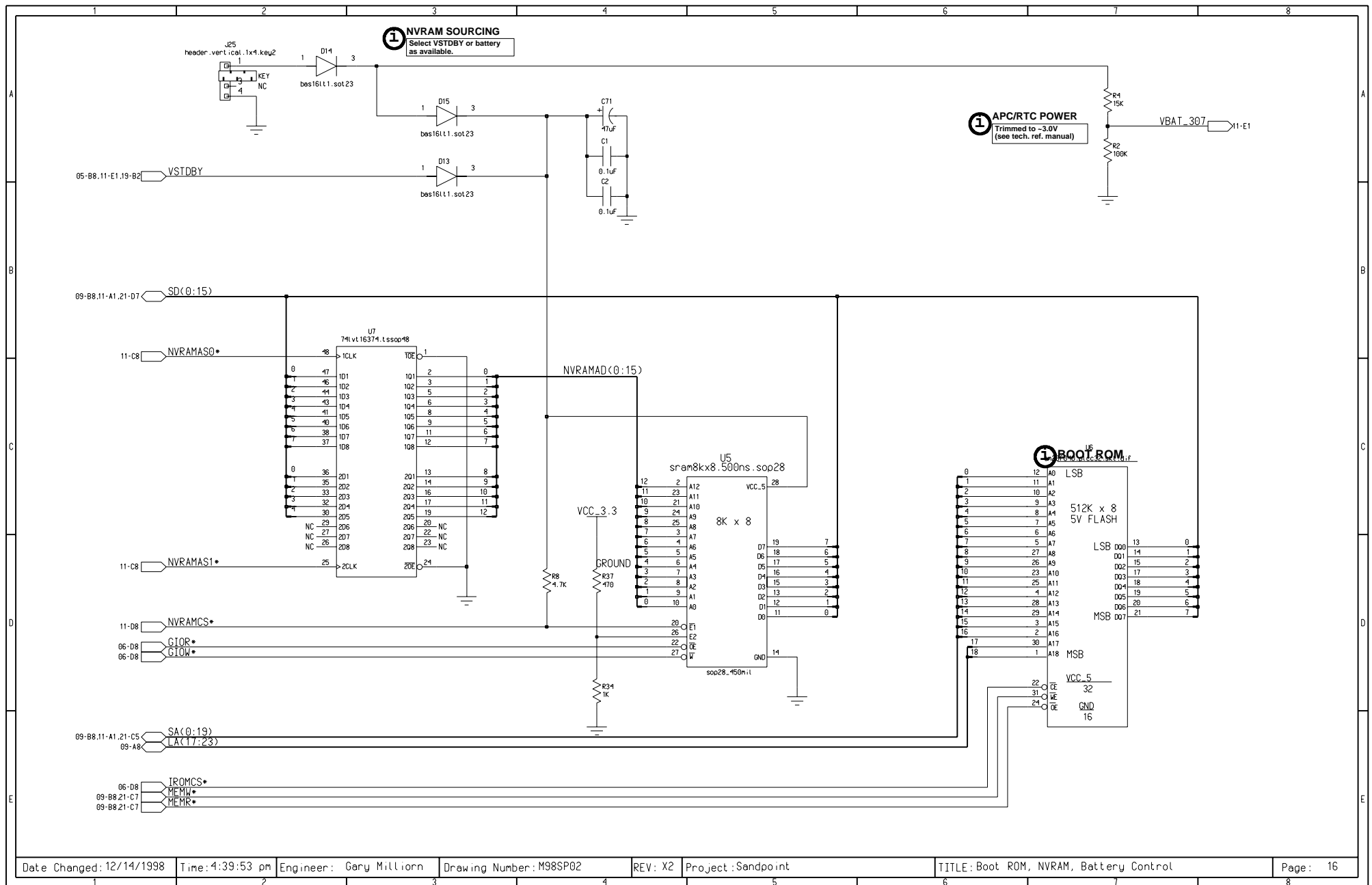
① Parallel Port
Right-angle PCB mount

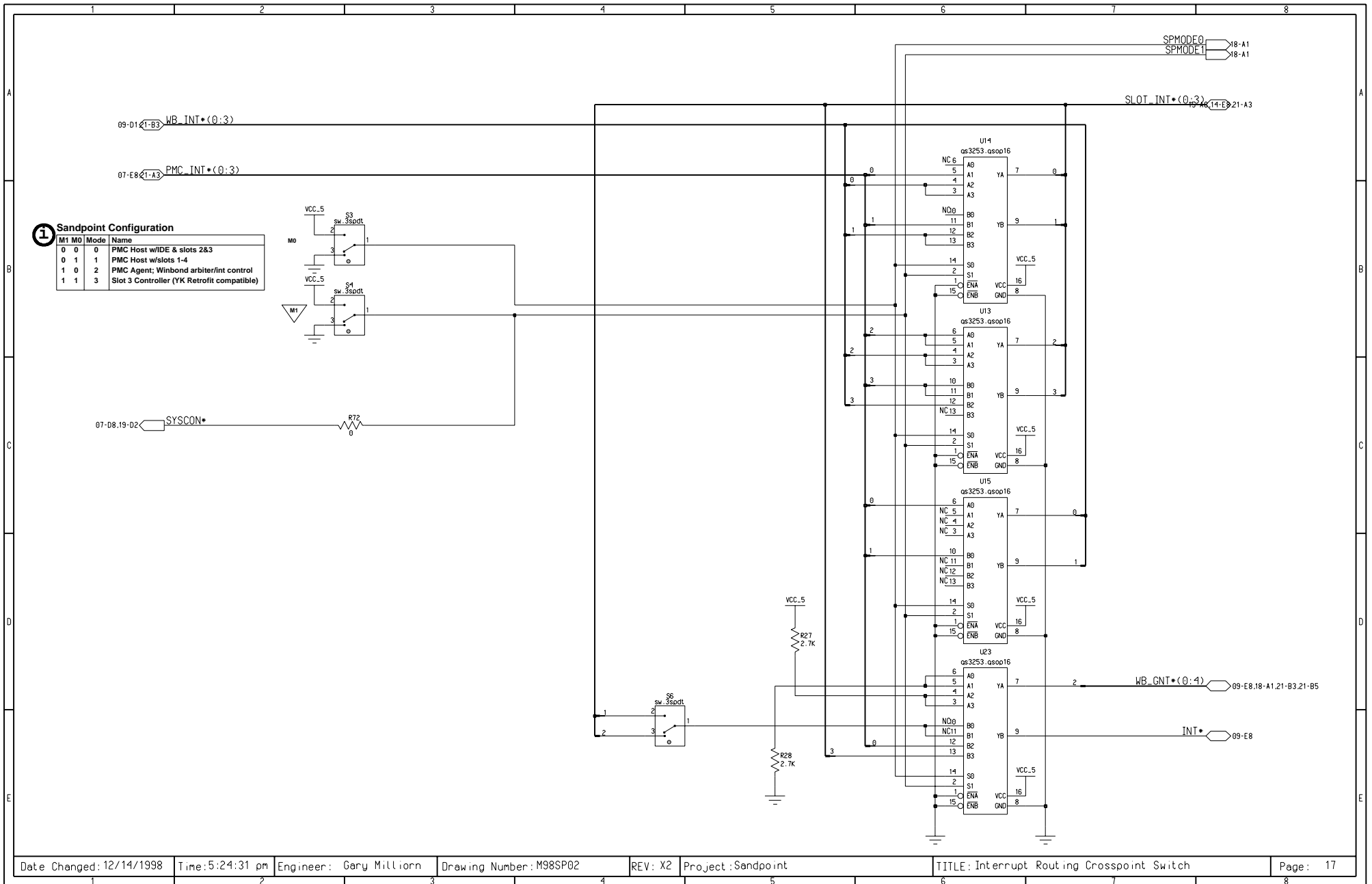


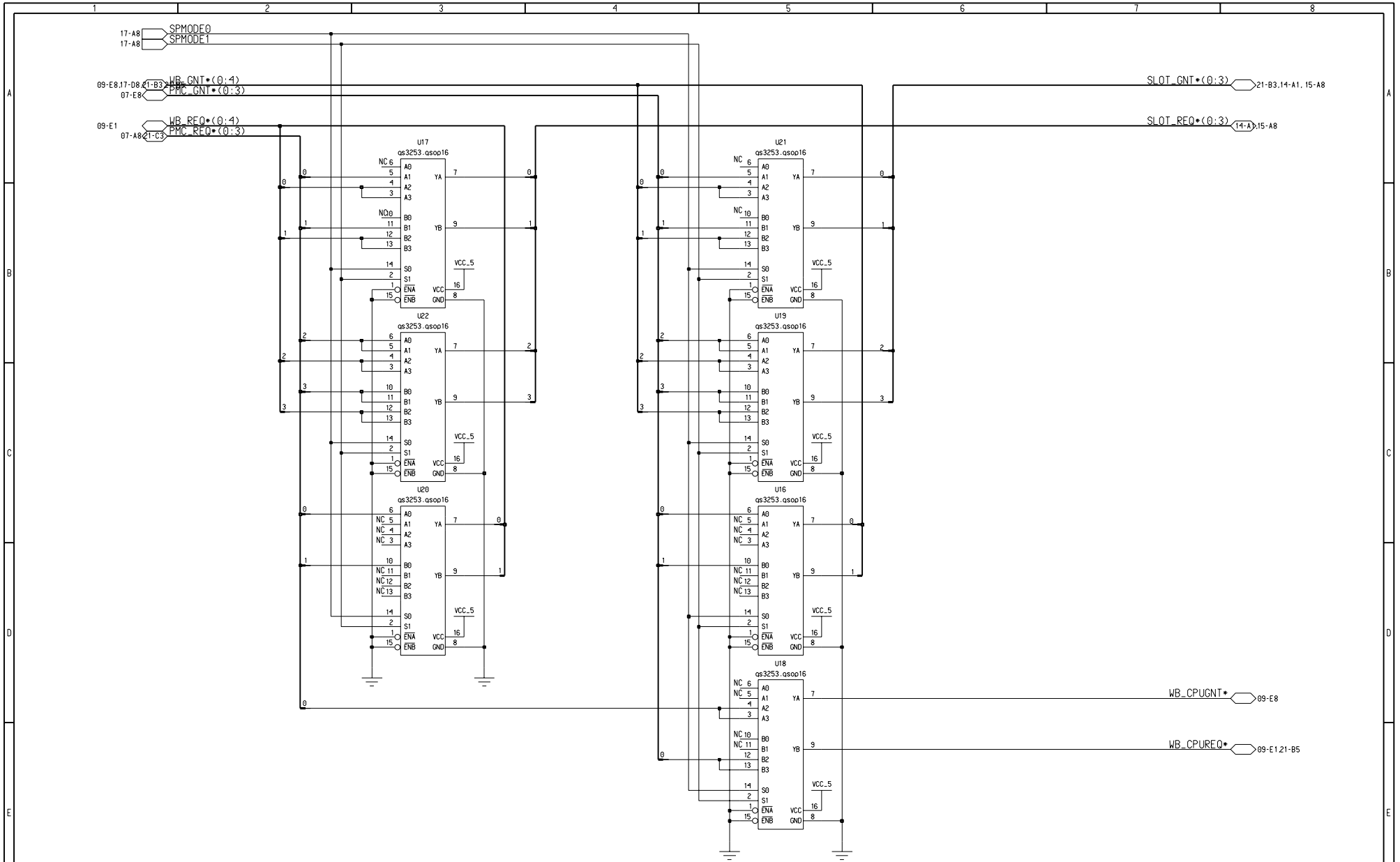




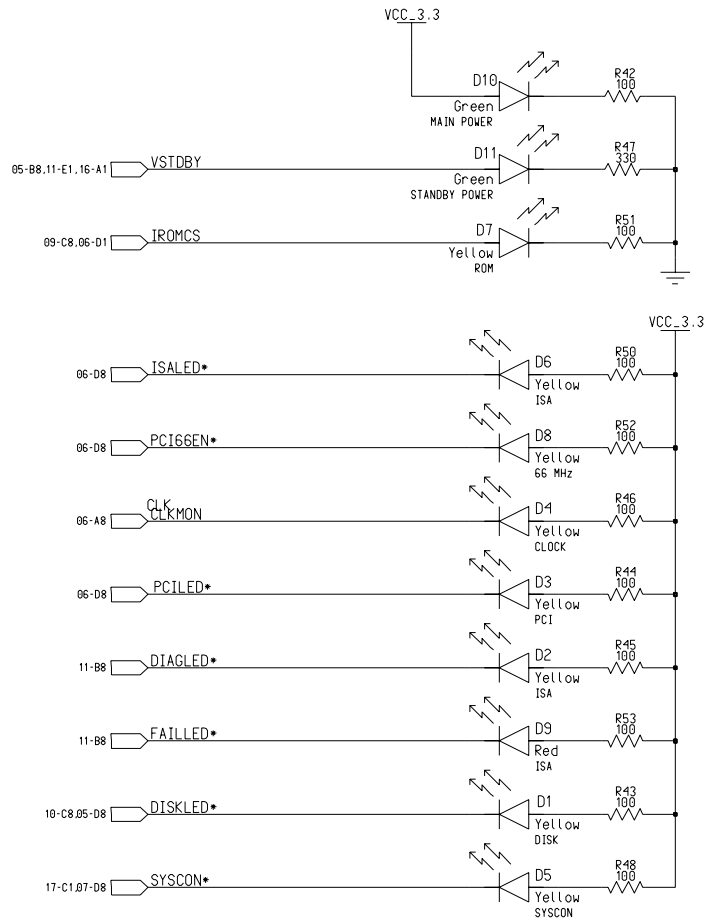




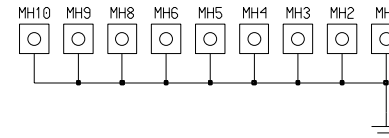




1 Diagnostic LEDs Place in visible area.



1 Chassis Mounting Holes ATX Chassis: Nine (9) holes.



1 PMC Mounting Holes PMC Standard mounting holes (4). 2.70mm non-plated

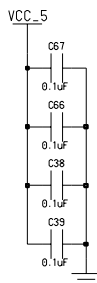


1 PMC Voltage Keying Holes PMC Standard keying holes (2). 2.70mm non-plated

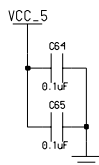


1 IC Bypassing

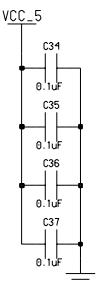
Super I/O



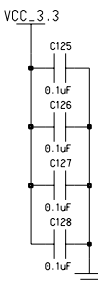
Flash ROM



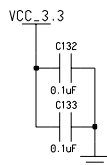
South Bridge



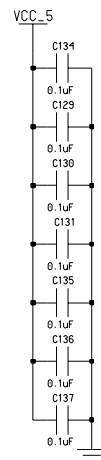
MPC972 Clock



GAL22V10

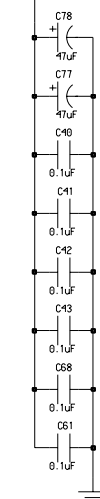


QuickSwitch

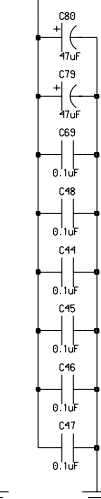


1 PMC Connectors

VCC_5

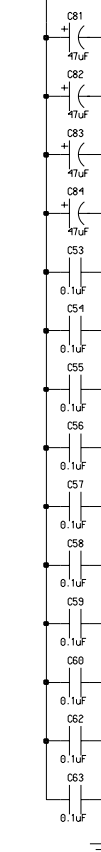


VCC_3.3

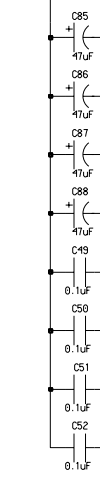


1 Bulk Capacitors

VCC_5



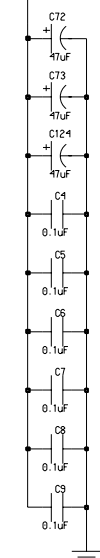
VCC_3.3



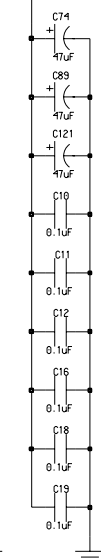
1 PCI Slots

Electrolytics between slots.
HF distributed along slot length.

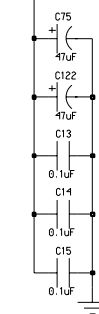
VCC_5



VCC_3.3



VCC_12



VCC_12N

