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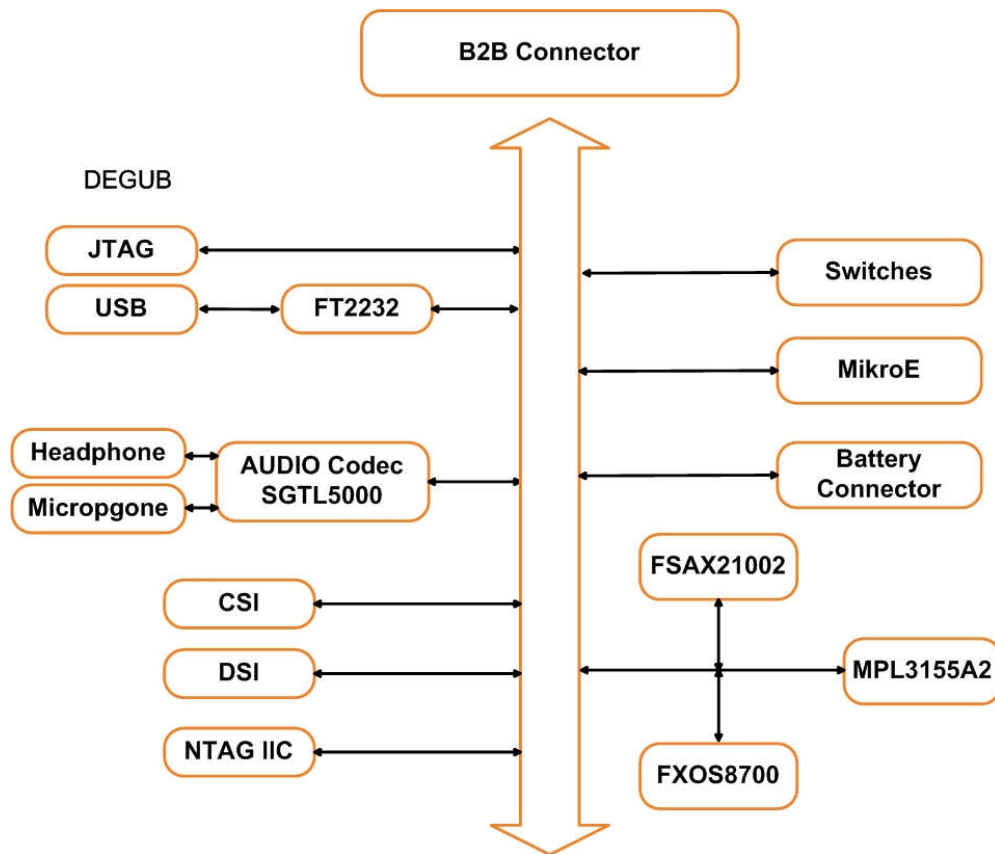
Table of Revesions

Revisions & Change Log			
Rev	Description	Date	Approved
X1	Initial Draft	06/25/15	
X2			
X3			
X4			
X5			
X6			
X7			
A			
A1			
B			
C			
D			
D1			

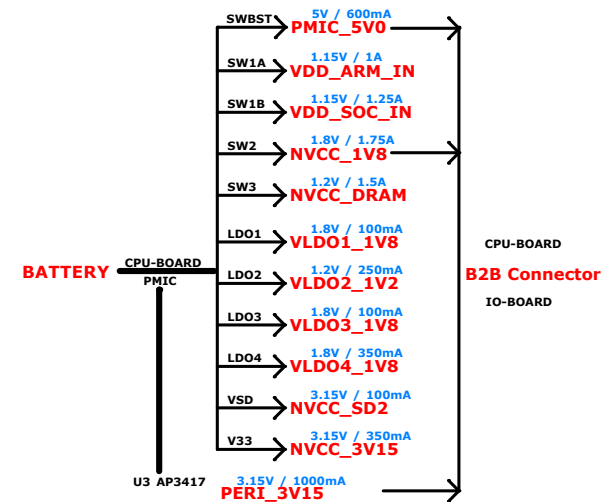
WaRP7 IO Board

			
ICAP Classification: FCP: FINO: PUB: X			
Drawing Title: WaRP7-IO Board			
Page Title: 01-Title Sheet			
Size C	Document Number <Doc>	Rev A	
Date: Friday, November 06, 2015		Sheet	1 of 7

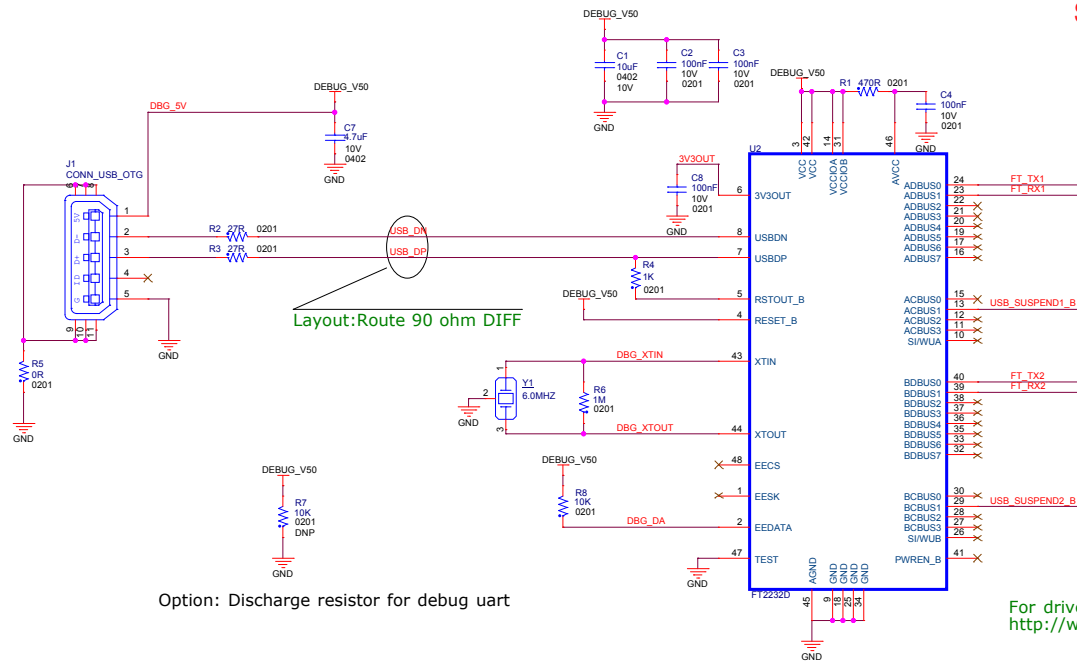
Warp7 IO Board Block Diagram



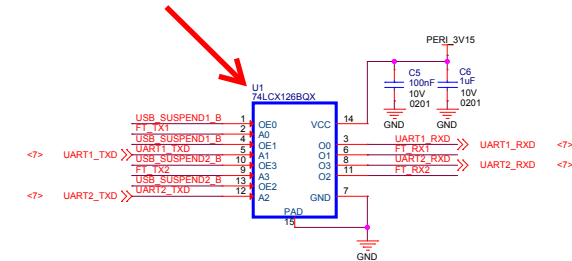
Power Distribution Diagram



Debug UART



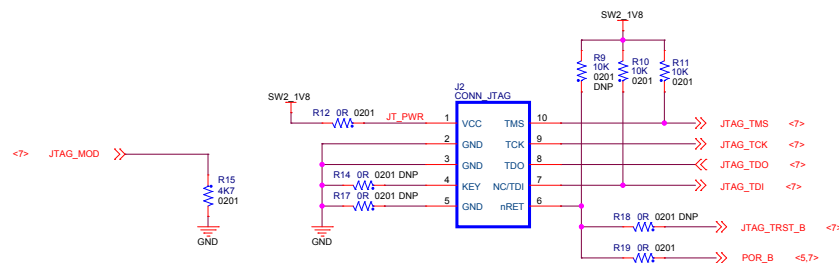
In the original design,U1(74LVC125ABQ-Q100) is active low, but the OE output of U2 is high level.
So we changed U1 from 74LVC125ABQ-Q100 to 74LCX126BQX which is active high.



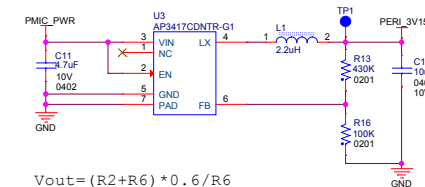
Note:
Debug UART1 for Cortex-A7
Debug UART2 for Cortex-M4

For driver installation, please refer to
<http://www.ftdichip.com/Documents/InstallGuides.htm>

JTAG

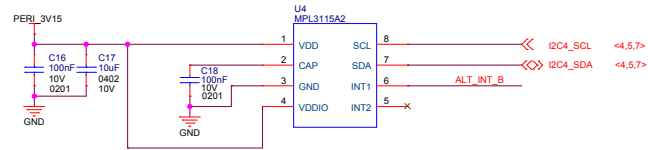


PERI 3V15 (1A)

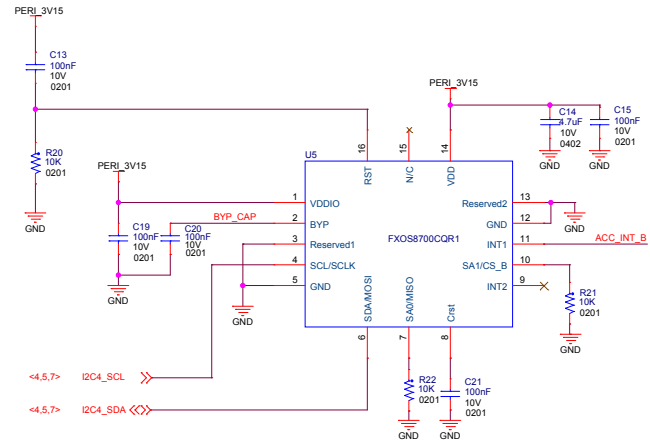


$$V_{out} = (R_2 + R_6) * 0.6 / R_6$$

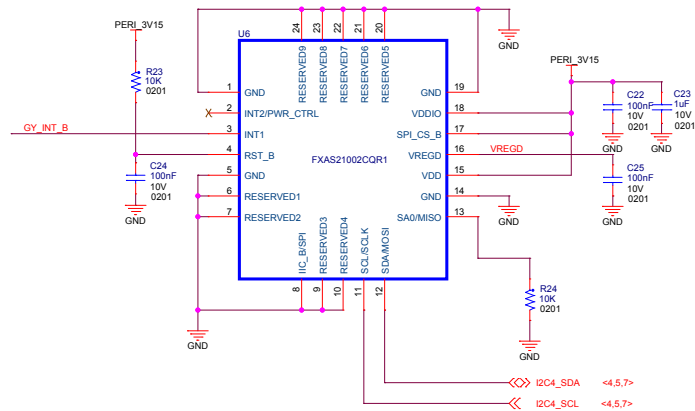
Barometer/Altimeter



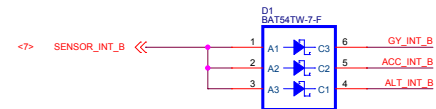
Accelerometer & Magnetometer



Gyroscope



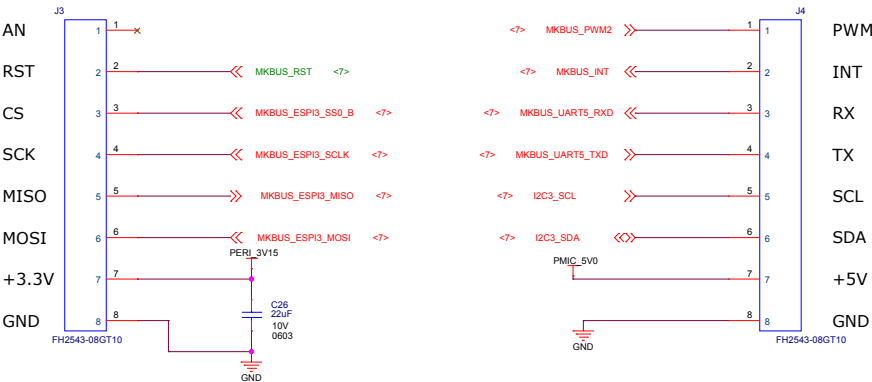
SENSOR_INT



Note: The sensors' interrupts is wired to processor by a OR circuit , the software will determine which device asserted the interrupt.

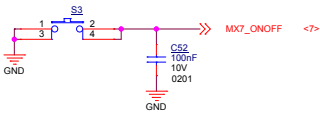
MikroBUS

<http://www.mikroe.com/>

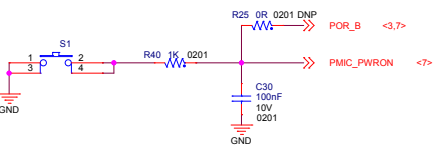


Switch

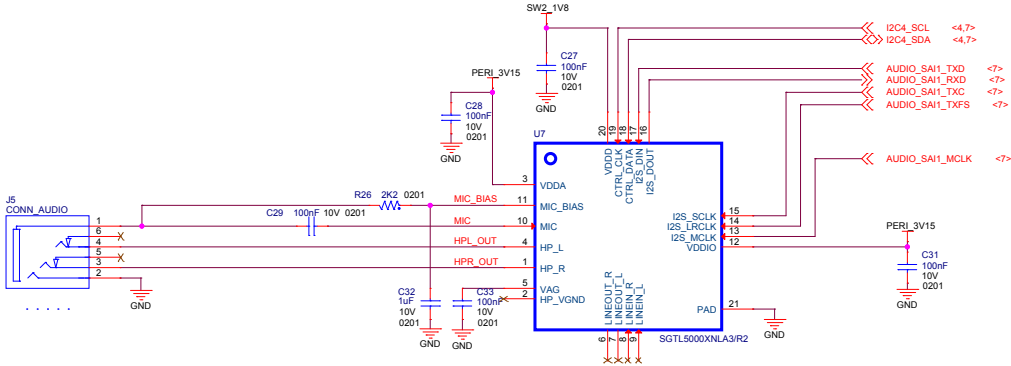
Power Button



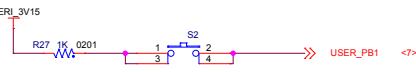
POR Button



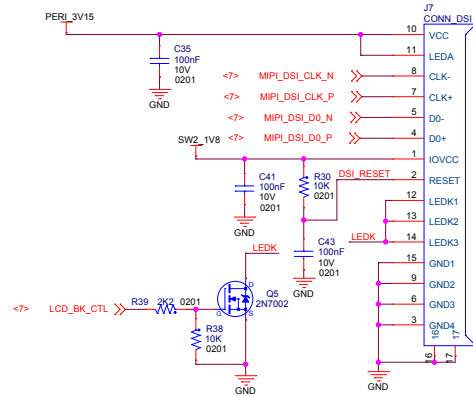
Audio



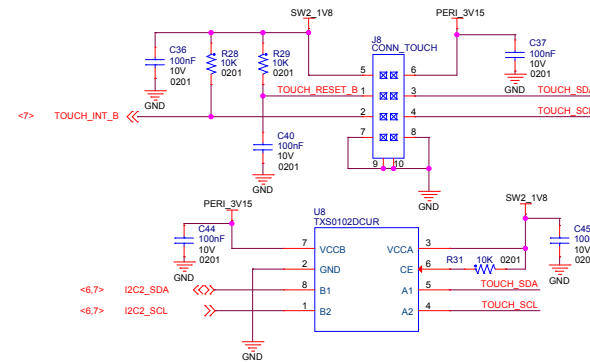
User Button



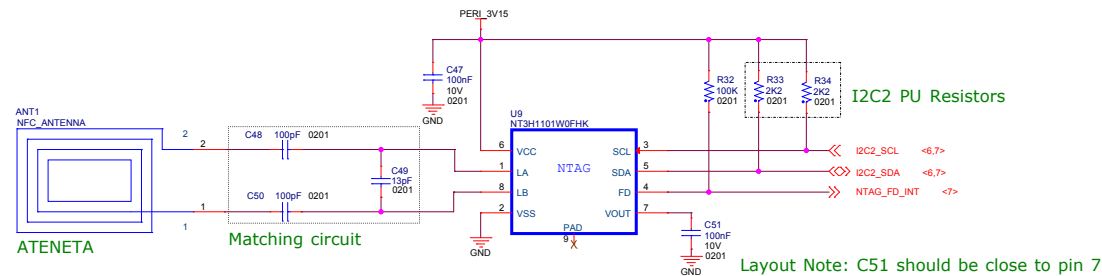
DSI



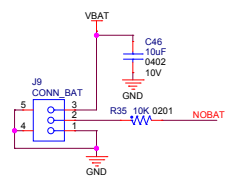
TOUCH



NTAG_I2C

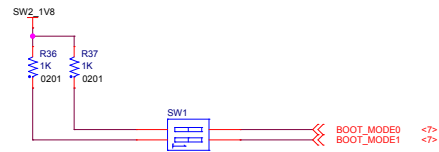


BATTERY IN



BOOT MODE

BOOT_MODE	1	0
FUSES	0	0
MANUFACTURE	0	1
INTERNAL BOOT	1	0
TEST MODE	1	1



B2B Connector

Note:All clock signals are isolated to other signals with GND.

