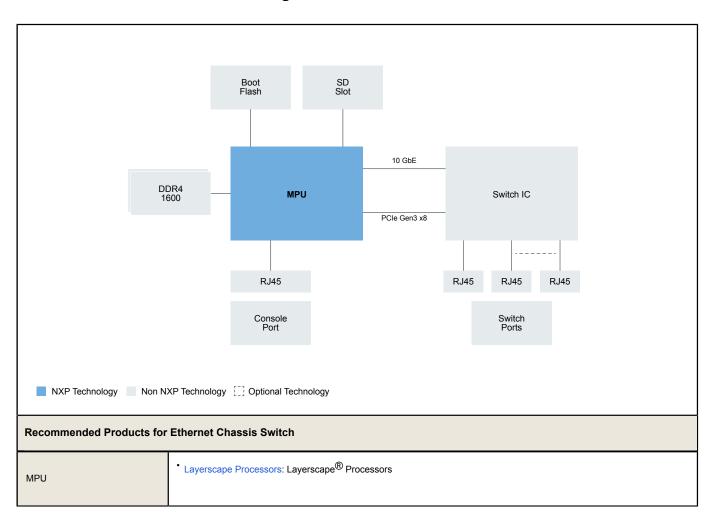


Ethernet Switch

Last Updated: Nov 22, 2023

The backbone of networks, switches need a processor for control and management. It must have high-speed PCle and Ethernet links to switch ICs to swap packets requiring special handling and issue commands and must also integrate efficient, standard CPUs to host network operating systems or traditional control-plane code. Layerscape addresses the control requirements of switches, be they for SMB, enterprise/campus, data center or carrier use.

Ethernet Chassis Switch Block Diagram



Note: The information on this document is subject to change without notice.
WWW.nxp.com NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.