



# High-Performance, Power-Efficient and Cost Sensitive Arm<sup>®</sup> Cortex<sup>®</sup>-M0+ MCUs

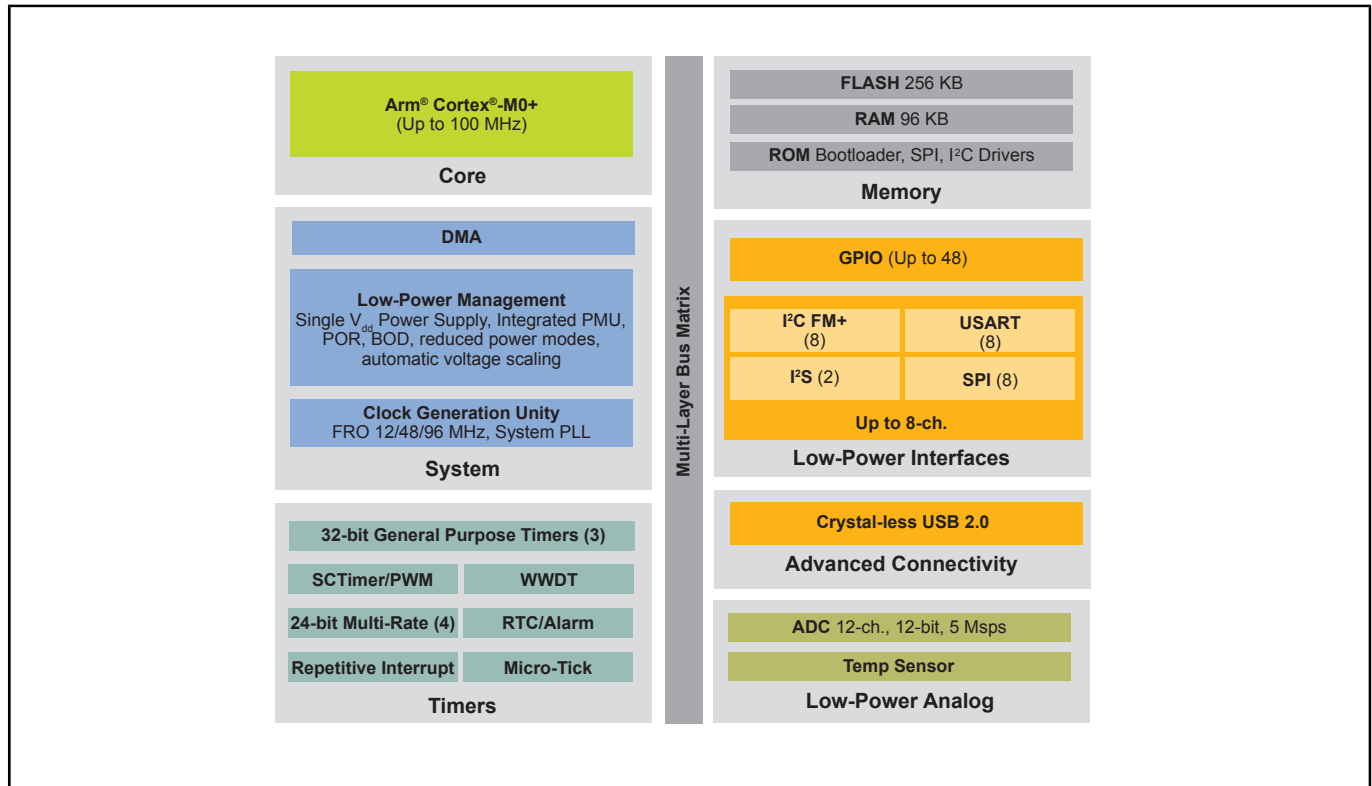
## LPC51U68

Last Updated: Nov 21, 2023

Based on the highly energy-efficient Arm<sup>®</sup> Cortex<sup>®</sup>-M0+ core and operating at CPU frequencies of up to 150 MHz, NXP's 32-bit LPC51U68 microcontrollers for embedded applications feature larger memory resources including 96 KB of on-chip SRAM and 256 KB of on-chip flash programming memory with flash accelerator.

NXP's LPC51U68 MCU features added performance, expanded memory resources and flexible serial port configuration, including USB connectivity, bringing design flexibility, computing performance and integration into today's demanding IoT and industrial applications. In addition, this MCU is pin compatible with the LPC5411x and LPC5410x MCU families providing scalability options to Arm Cortex-M4 cores.

# LPC51U68 MCU Block Diagram



View additional information for [High-Performance, Power-Efficient and Cost Sensitive Arm® Cortex®-M0+ MCUs](#).

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.