



i.MX RT500 Crossover MCU with Arm[®] Cortex[®]-M33, DSP and GPU Cores

i.MX-RT500

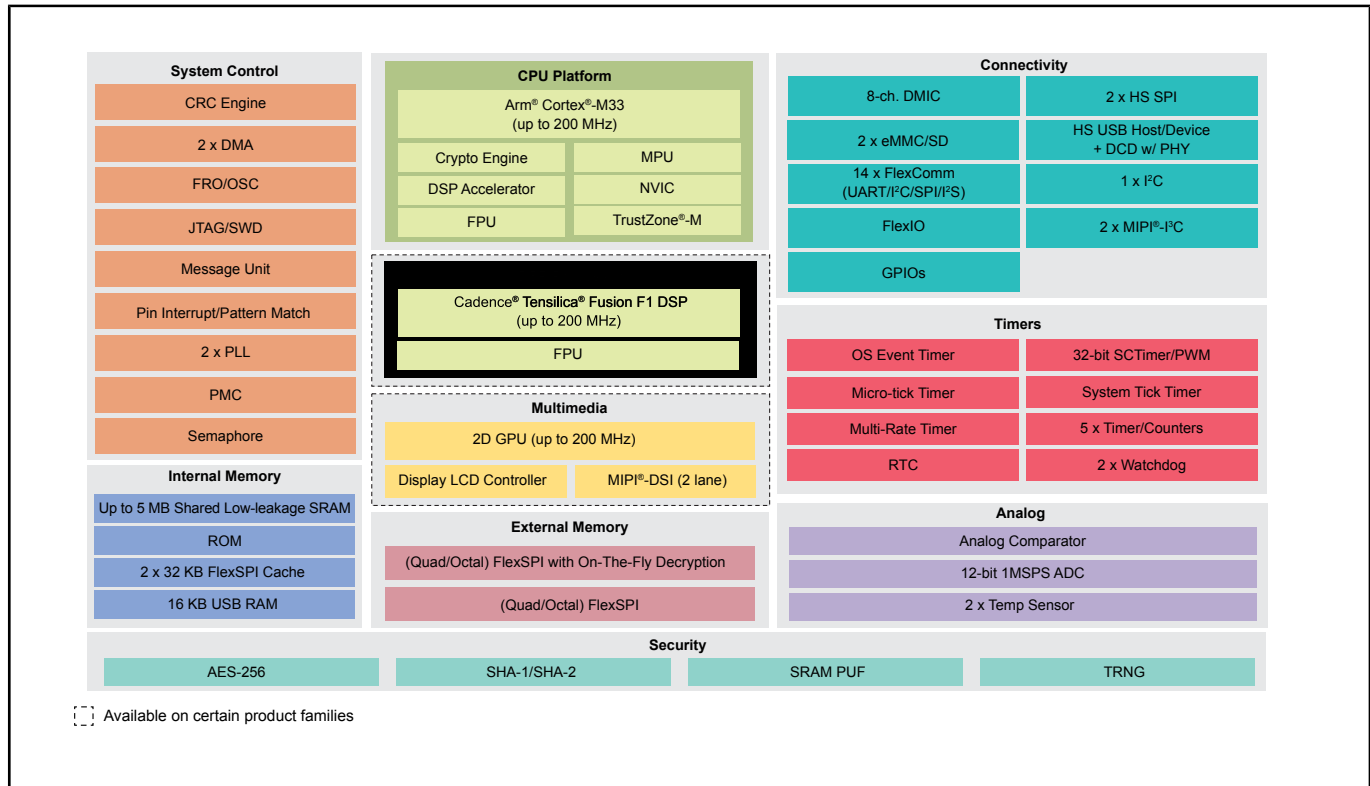
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i.MX RT500 Crossover MCUs are dual-core devices featuring an Arm[®] Cortex[®]-M33 and Cadence[®] Xtensa[®] Fusion F1 DSP, designed for low power wearable and consumer IoT applications.

The i.MX RT500 CM33 operates at up to 275 MHz and includes two coprocessors providing enhanced performance. The Fusion DSP operates at up to 275 MHz. The family offers a rich set of peripherals, embedded security and very low power consumption. The device has up to 5 MB SRAM and two FlexSPIs each with 32 KB cache.

The i.MX RT500 family is supported by the [MCUXpresso ecosystem](#), which includes an SDK, a choice of IDEs and secure provisioning and configuration tools to enable rapid development.

i.MX RT500 Crossover MCU Block Diagram



View additional information for [i.MX RT500 Crossover MCU with Arm® Cortex®-M33, DSP and GPU Cores.](#)

Note: The information on this document is subject to change without notice.

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