



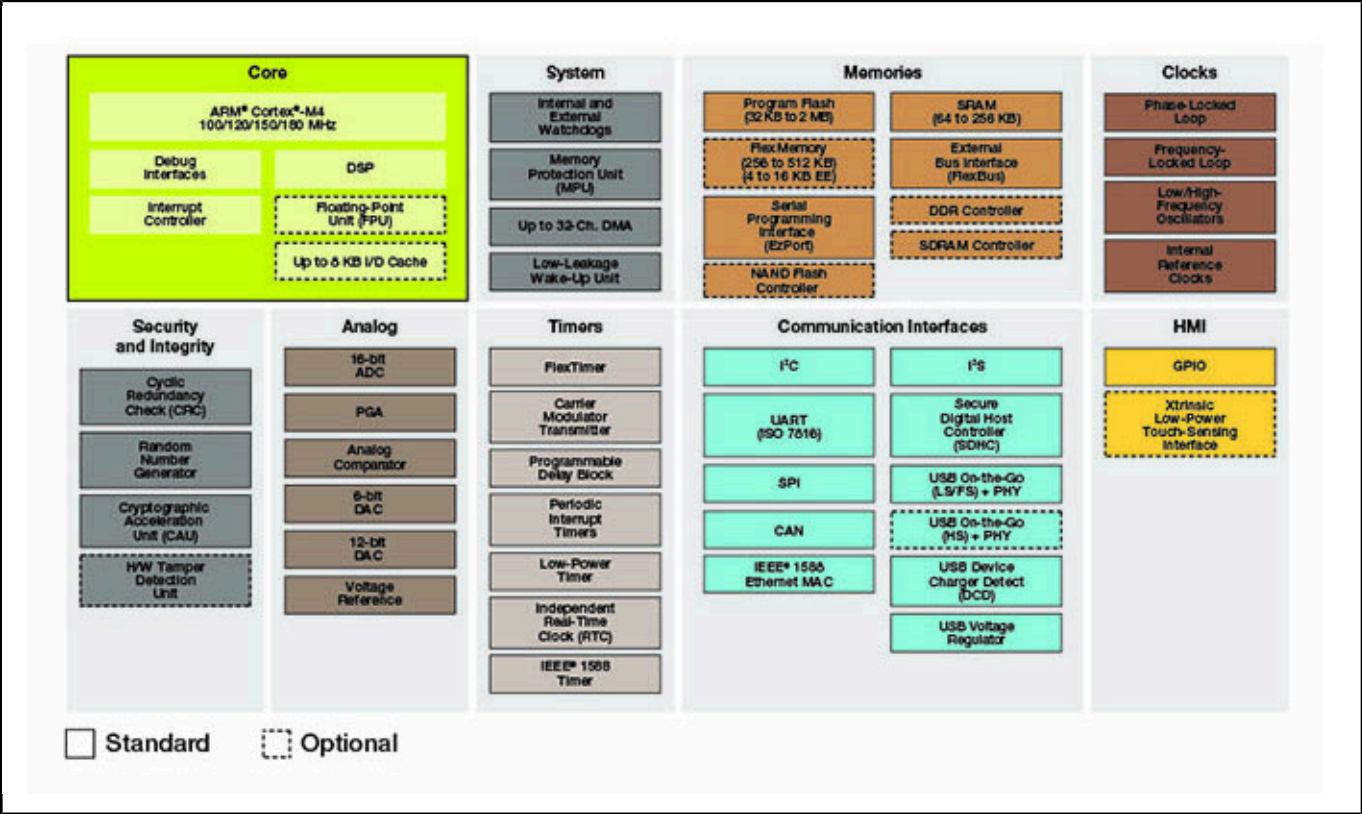
# Kinetis® K60-100 MHz, 混合信号集成 微控制器, 基于Arm® Cortex®-M4内核

## K60\_100

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Kinetis® K60 100 MHz IEEE® 1588以太网MCU基于Arm® Cortex® -M4内核构建, 配备先进的模拟集成和串行通信。这些器件采用低至5 mm x 5 mm的晶圆级芯片封装(WLCSP), 通过极短的互连, 在最大限度上节约电路板空间、增强性能, 支持现有应用的微型化。该系列具备Kinetis产品组合固有的全面支持体系和可扩展性。

Kinetis K6x MCU Family Block Diagram Block Diagram



View additional information for [Kinetis® K60-100 MHz](#), 混合信号集成微控制器，基于Arm® Cortex®-M4内核.

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