



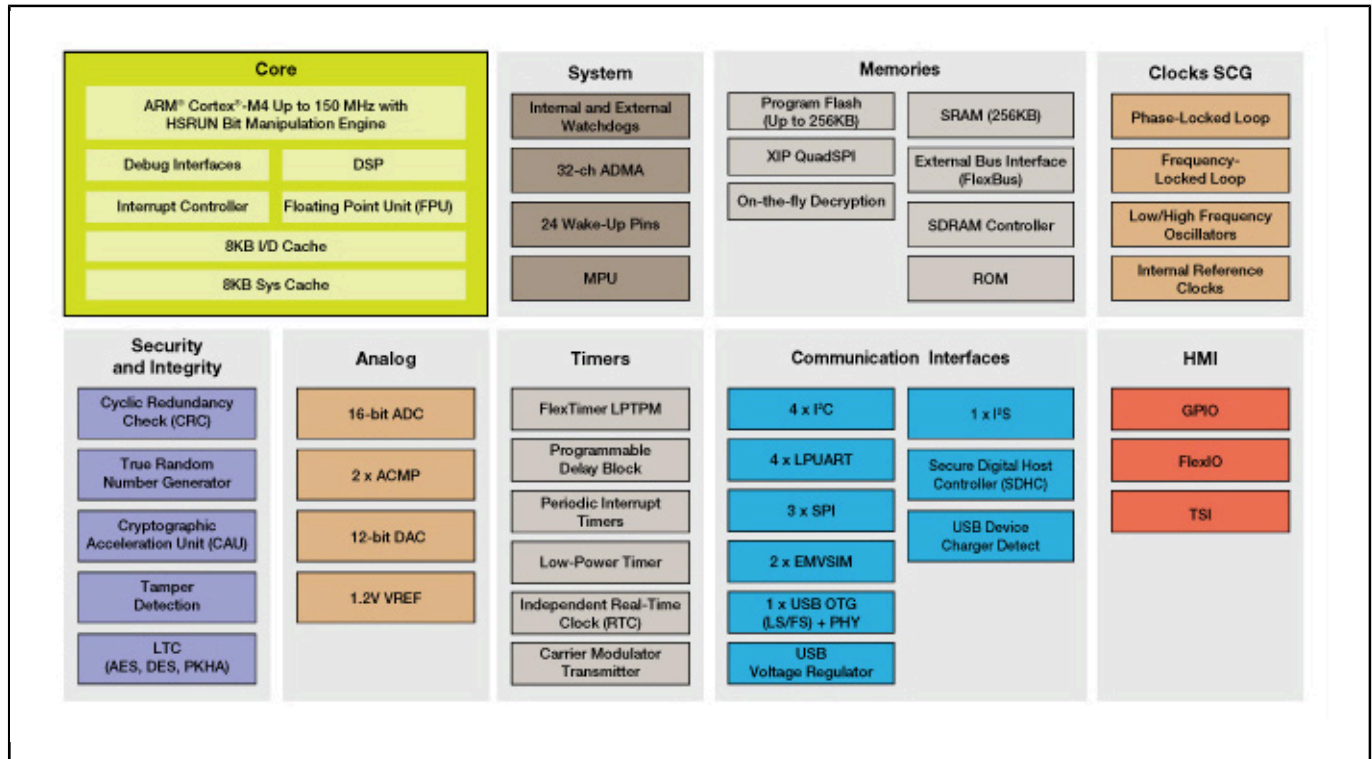
Kinetis K81-150 MHz 硬件加密协处理器，反篡改和QuadSPI微控制器 (MCU)，基于Arm® Cortex®-M4内核

K81_150

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Kinetis® K81 MCU扩展了Kinetis MCU产品组合，具有一些先进的安全功能，包括防篡改外设、引导ROM支持加密的固件更新、从外部串行闪存中自动解密、具有边带攻击保护的AES加速以及公钥加密的硬件支持。K81 MCU可用于满足POS应用的安全标准。

Kinetis K81_150 MCU Block Diagram Block Diagram



View additional information for [Kinetis K81-150 MHz硬件加密协处理器，反篡改和QuadSPI微控制器 \(MCU\)](#)，基于Arm® Cortex®-M4内核.

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