



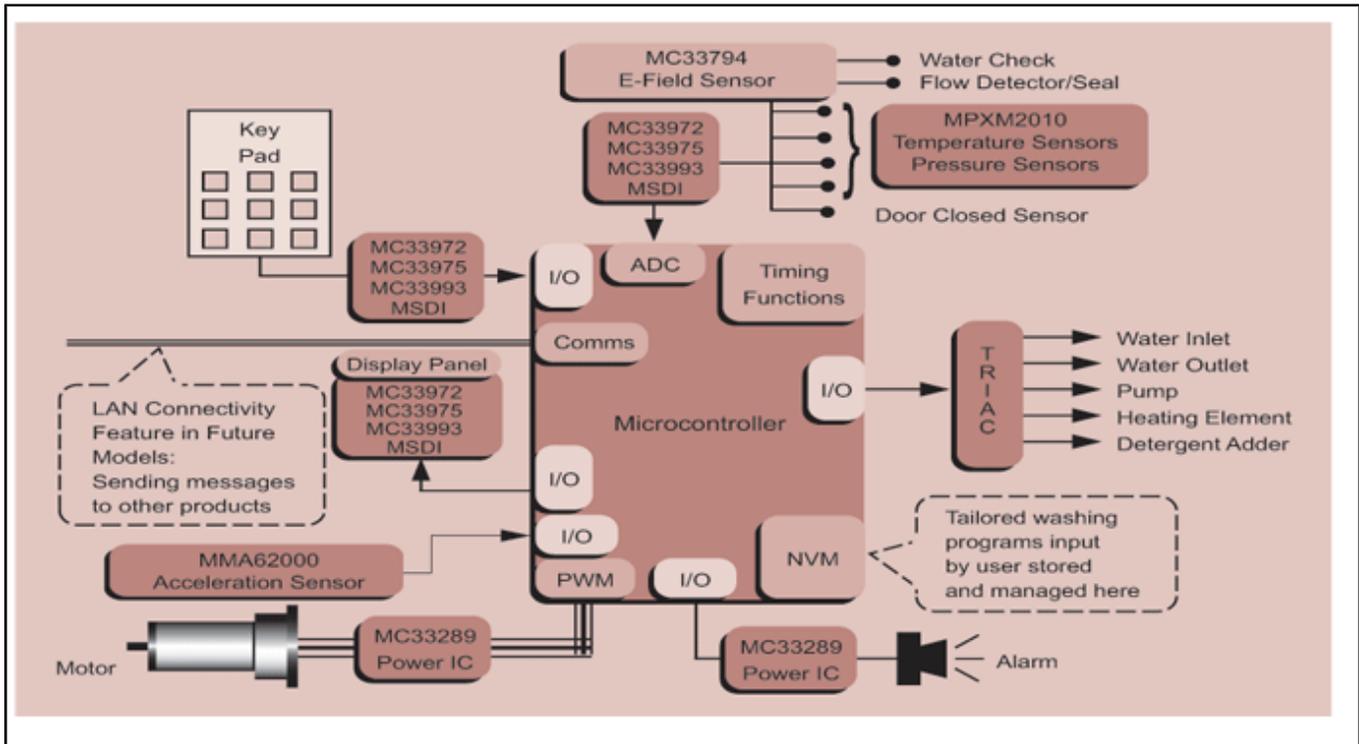
Microcontroller

68HC705P6A

Last Updated: Jan 14, 2026

The NXP® MC68HC705P6A is an EPROM version of the MC68HC05P6 microcontroller. It is a low-cost combination of an M68HC05 Family microprocessor with a 4-channel, 8-bit analog-to-digital (A/D) converter, a 16-bit timer with output compare and input capture, a serial communications port (SIOP), and a computer operating properly (COP) watchdog timer. The HC05 CPU core contains 176 bytes of RAM, 4672 bytes of user EPROM, 239 bytes of bootloader ROM, and 21 input/output (I/O) pins (20 bidirectional, 1 input-only). This device is available in a 28-pin SOIC, PDIP, or windowed DIP package.

Washer Machines Microcontrollers for M68HC05 Family Block Diagram Block Diagram



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