



# Ara240 Discrete Neural Processing Unit (DNPU)

## ARA240

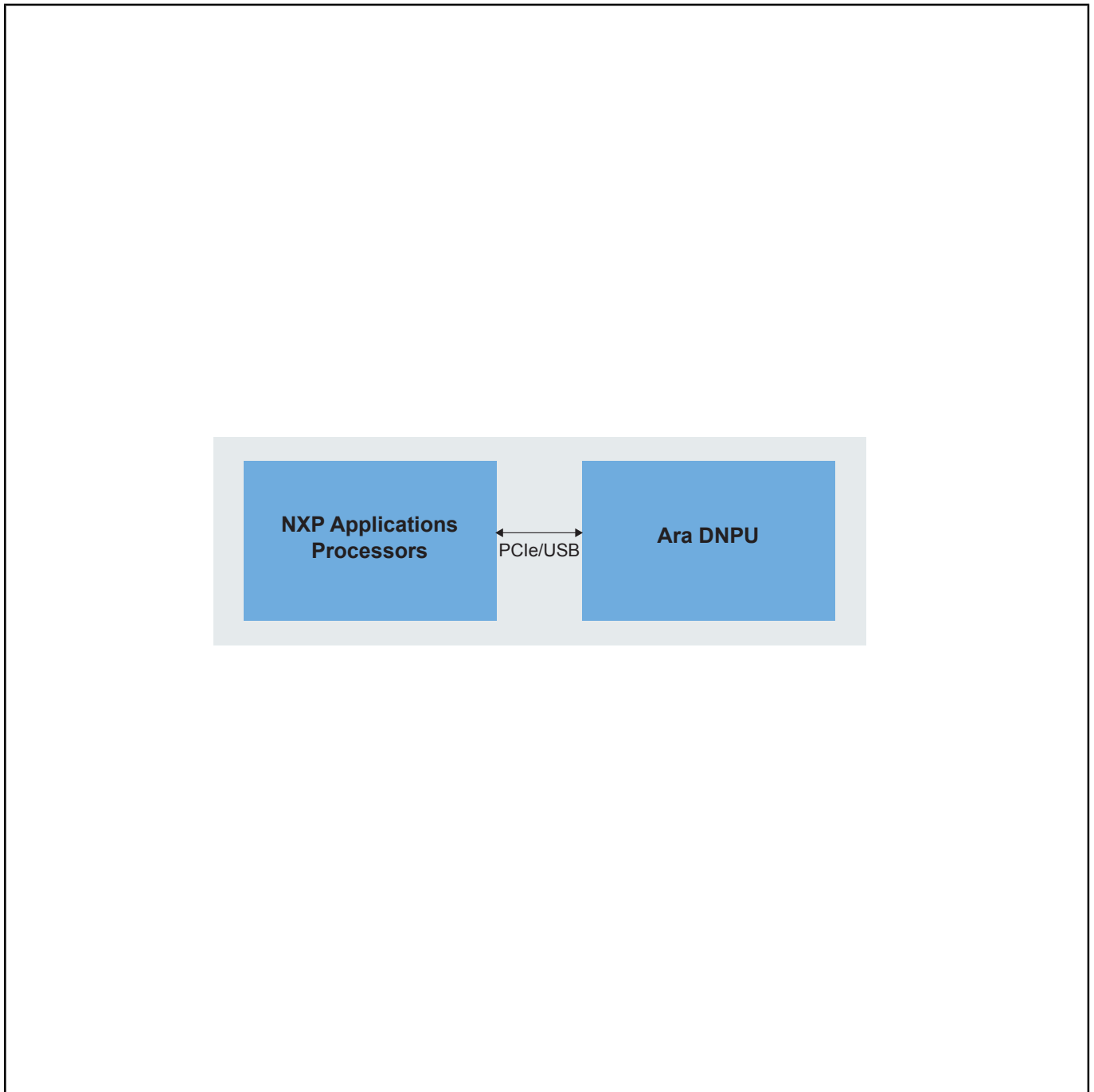
**Active**

Last Updated: Apr 16, 2026

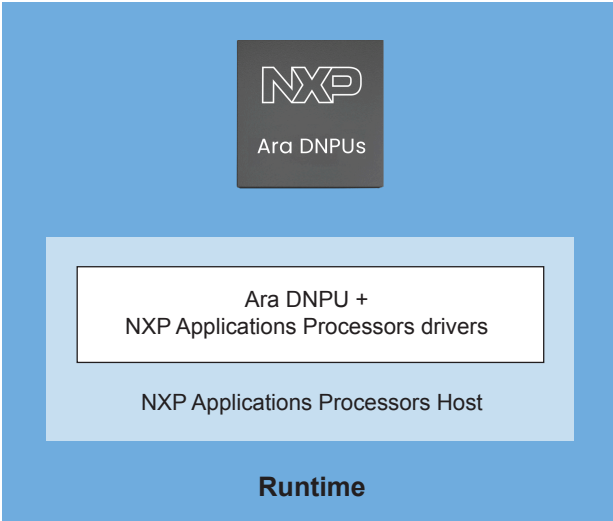
The Ara240 Discrete Neural Processing Unit (DNPU) enables real-time generative AI, large language models (LLMs) and vision language models (VLMs) execution on AI-enabled compute and embedded systems, delivering low-latency, lower operational costs and enhanced data privacy. Its innovative architecture combines balanced compute, large on-chip memory and high off-chip bandwidth to efficiently execute large models.

Explore the Ara240 DNPU with the [Ara240 16 GB M.2 Module](#).

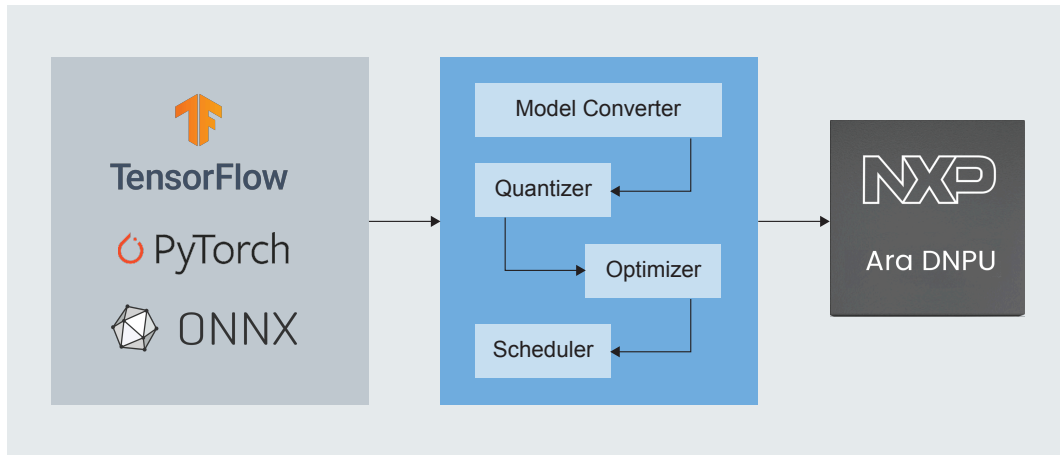
## NXP Applications Processor and Ara DNPV Connection Block Diagram



## Integration of Ara DNPV and Applications Processors Host with Drivers Block Diagram



**Machine learning deployment flow Block Diagram**



View additional information for [Ara240 Discrete Neural Processing Unit \(DNPUs\)](#).

**Note:** The information on this document is subject to change without notice.

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