



3.3 V, Two Differential Channel, 2-1 Multiplexer/Demultiplexer Switch for PCI Express® Gen2

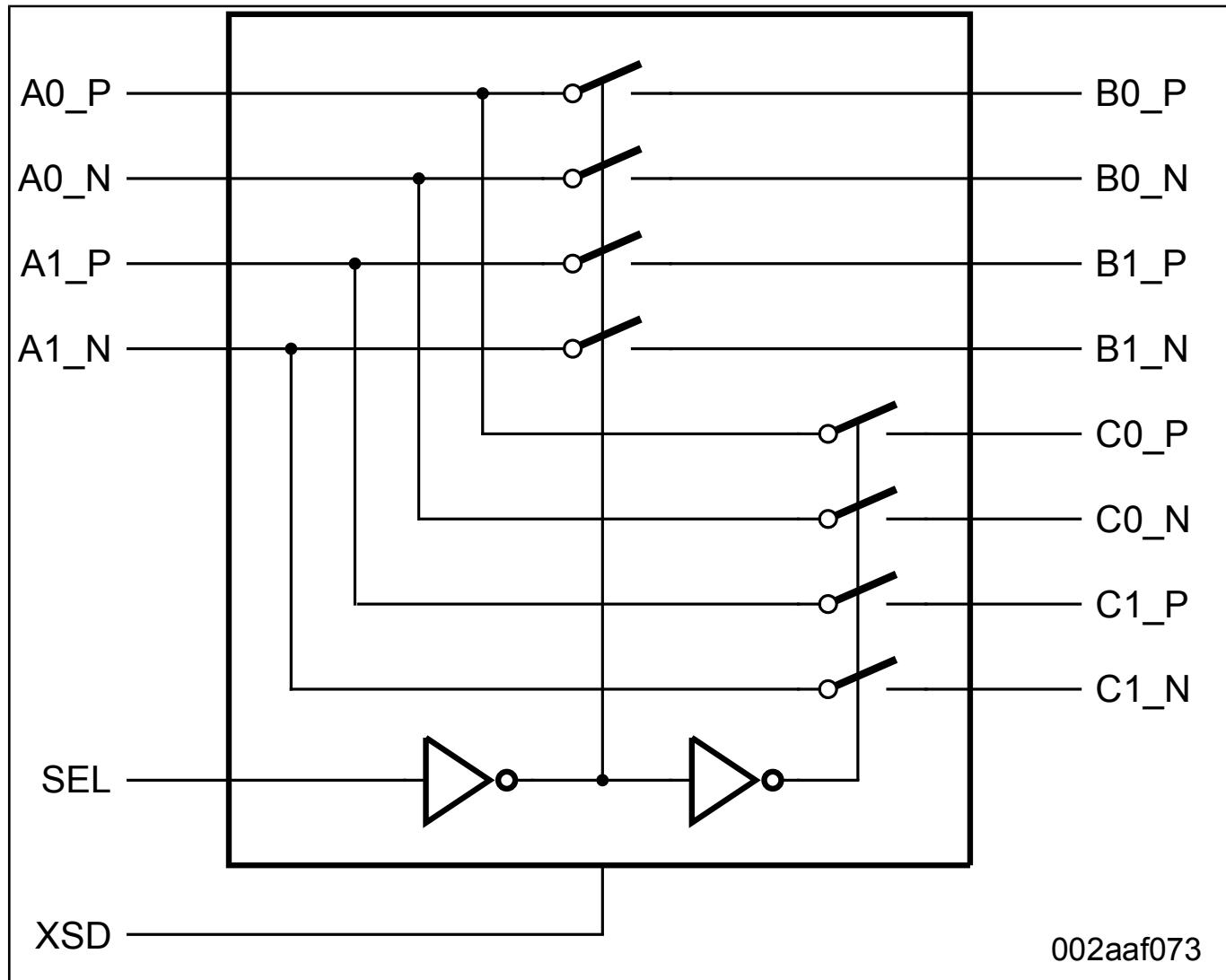
CBTL02042A_CBTLS02042B

Last Updated: Dec 23, 2025

CBTL02042A/B is a 2 differential channel, 2-to-1 multiplexer/demultiplexer switch for PCI Express Generation 2 (Gen2), and other high-speed serial interface applications. The CBTLS02042A/B can switch two differential signals to one of two locations. Using a design technique, NXP has minimized the impedance of the switch such that the attenuation observed through the switch is negligible, and also minimized the channel-to-channel skew as well as channel-to-channel crosstalk, as required by the high-speed serial interface. CBTLS02042A/B allows expansion of existing high speed ports for extremely low power.

The device's pinouts are optimized to match different application layouts. CBTLS02042A has input and output pins on the opposite of the package, and is suitable for edge connector(s) with different signal sources on the motherboard. CBTLS02042B has outputs on both sides of the package, and the device can be placed between two connectors to multiplex differential signals from a controller. Please refer to Section 8 for layout examples.

CBTL02042A, CBTL02042B Block Diagram Block Diagram



[View additional information for 3.3 V, Two Differential Channel, 2-1 Multiplexer/Demultiplexer Switch for PCI Express® Gen2.](#)

Note: The information on this document is subject to change without notice.