



FS25 System Basis Chip (SBC) for Automotive Zonal Architectures

FS25

Preproduction

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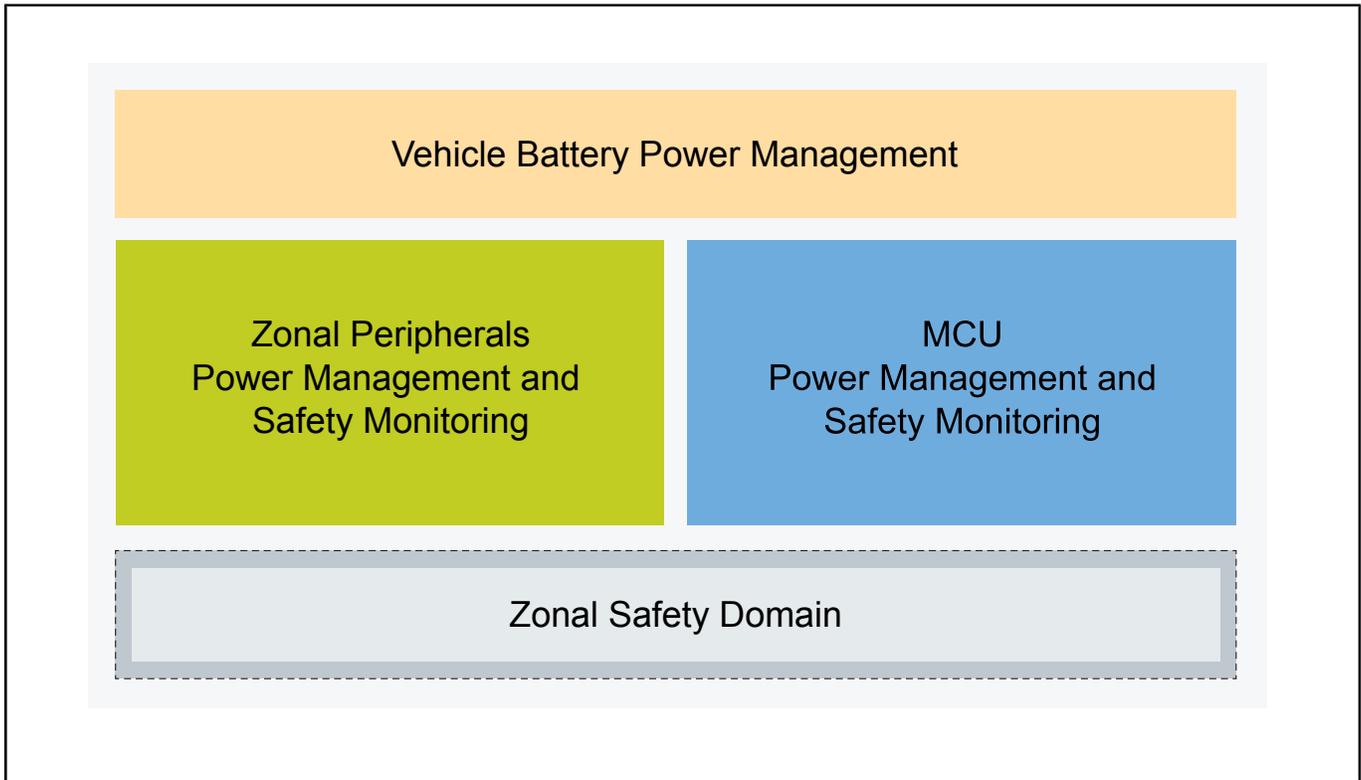
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The FS25 system basis chip (SBC) is designed to support NXP's [S32K5](#) and [S32J](#) devices by managing power distribution and system control. It addresses current energy management requirements and includes features for protection, fault handling and predictable operation.

Startup parameters are stored in one-time programmable (OTP) memory, reducing the need for external configuration components. The chip also includes an analog-to-digital converter (ADC) and general-purpose inputs/outputs (GPIOs) supporting wake-up sources like input/output (I/O) events, long-duration timers and inter-integrated circuit (I²C) communication.

Developed in line with the ISO 26262 compliance, the FS25 supports ASIL D safety levels through an independent safety monitoring unit (iSMU) with built-in self-test and fail-safe outputs to meet the highest automotive safety integrity levels.

SBC for Automotive Zonal Architectures Block Diagram



View additional information for [FS25 System Basis Chip \(SBC\) for Automotive Zonal Architectures](#).

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