



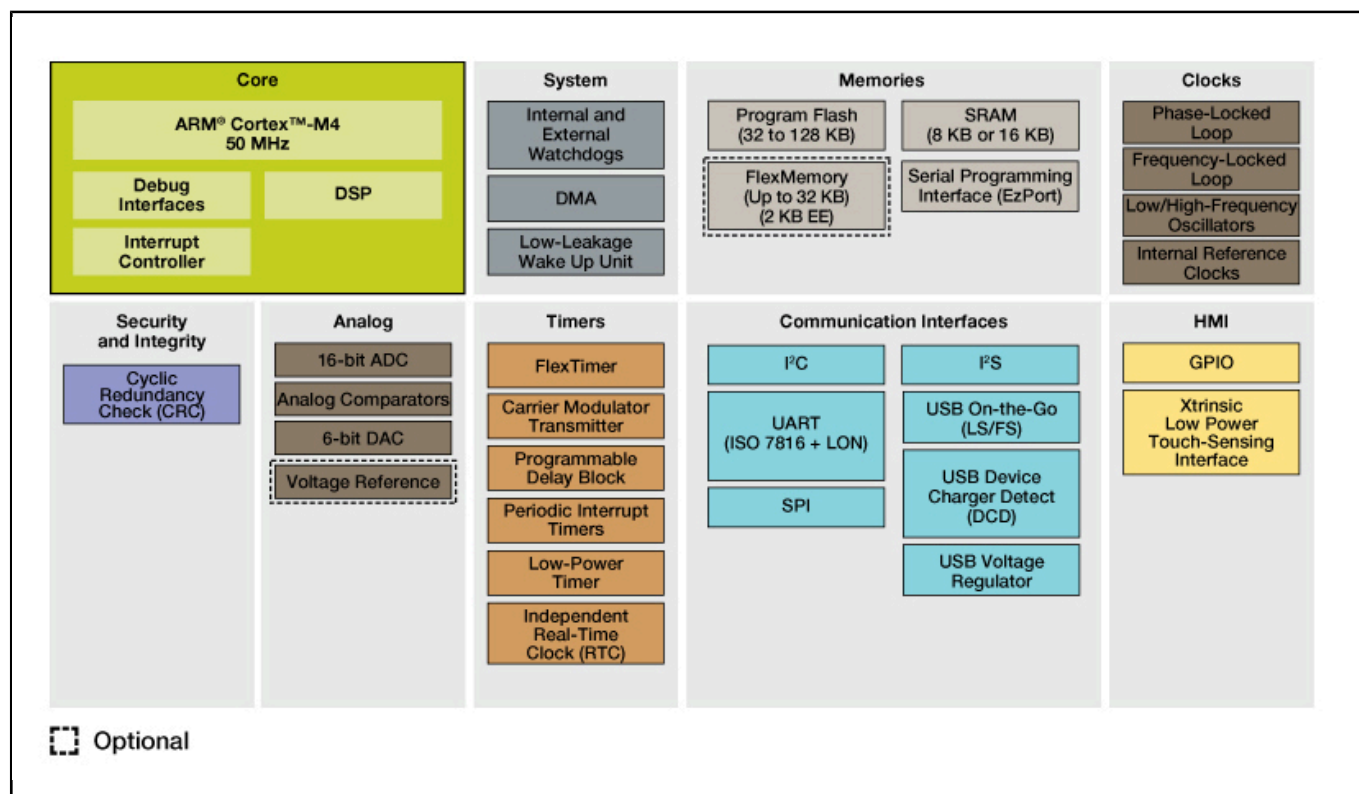
# **Kinetis® K20–50 MHz, Full-Speed USB, Mixed-Signal Integration Microcontrollers based on Arm® Cortex®–M4 Core**

## **K20\_50**

Last Updated: Mar 5, 2025

- Includes a rich suite of analog, communication, timing and control peripherals to accommodate a wide range of requirements
- Maximizes board space and enhances performance with minimum-length interconnections
- Provides performance efficiency with low power
- Delivers significant BOM savings through smart on-chip integration
- Shares the comprehensive enablement and scalability of the Kinetis® portfolio

## Kinetis K20 Entry Level USB MCUs Block Diagram Block Diagram



View additional information for [Kinetis® K20-50 MHz, Full-Speed USB, Mixed-Signal Integration Microcontrollers based on Arm® Cortex®-M4 Core](#).

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.