



# Kinetis<sup>®</sup> K21-50 MHz, Full-Speed USB, Anti-Tamper Microcontrollers based on Arm<sup>®</sup> Cortex<sup>®</sup>-M4 Core

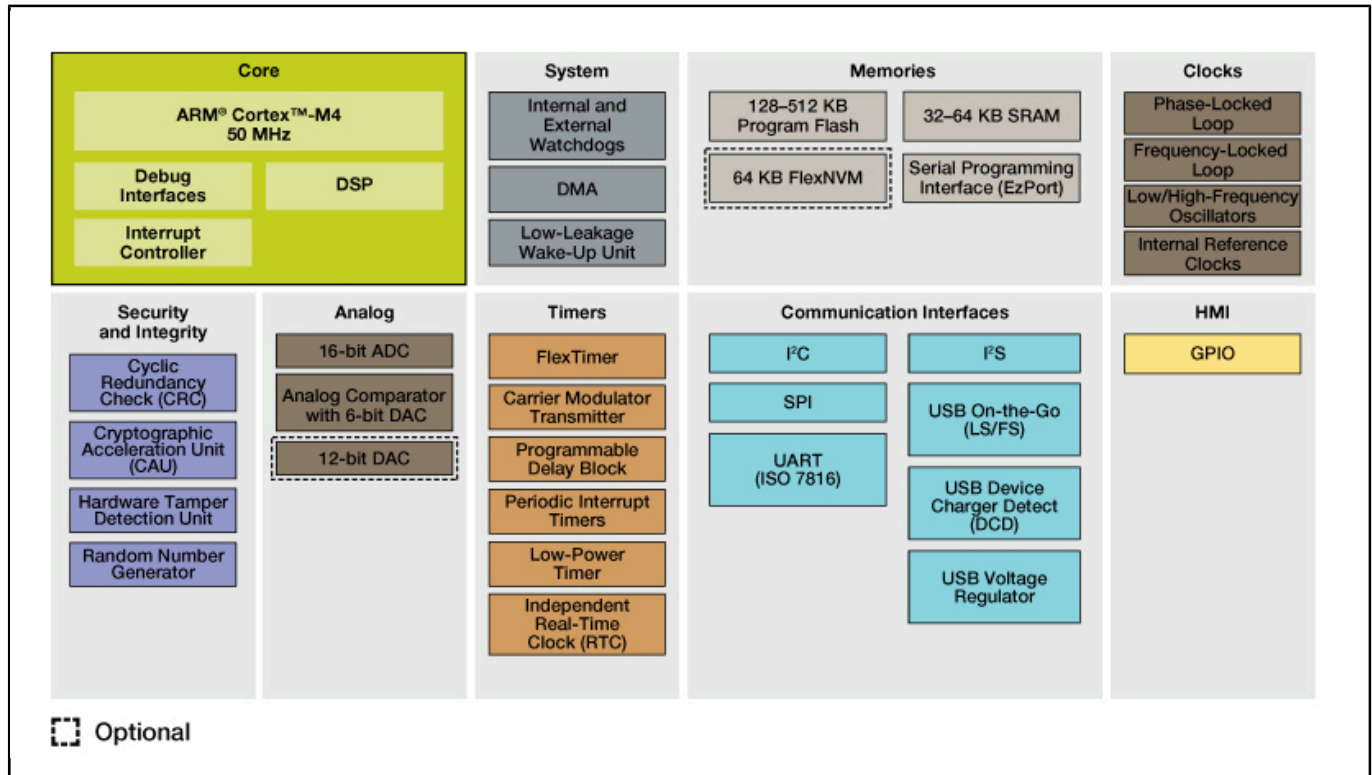
## K21\_50

Last Updated: Jan 16, 2023

- Features hardware encryption and tamper detection
- Includes a rich suite of analog, communication, timing and control peripherals to accommodate a wide range of requirements
- Maximizes board space and enhances performance with minimum-length interconnections
- Provides performance efficiency with low power
- Delivers significant BOM savings through smart on-chip integration
- Shares the comprehensive enablement and scalability of the Kinetis<sup>®</sup> portfolio

Please contact your local NXP representative to download the K21 Security Data sheet and Reference Manual documents (under NDA).

# Kinetis K21\_50 MCUs Block Diagram Block Diagram



View additional information for [Kinetis® K21-50 MHz, Full-Speed USB, Anti-Tamper Microcontrollers based on Arm® Cortex®-M4 Core](#).

Note: The information on this document is subject to change without notice.

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