



# Kinetis® K27-150 MHz, 2x USB, 2MB Flash, 1MB SRAM Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core

## K27\_150

Last Updated: Jan 16, 2026

The Kinetis K27 USB Arm® Cortex®-M4 MCUs target applications requiring processing efficiency and extra-large embedded memory with 2 MB Flash and 1 MB SRAM. This sub-family is:

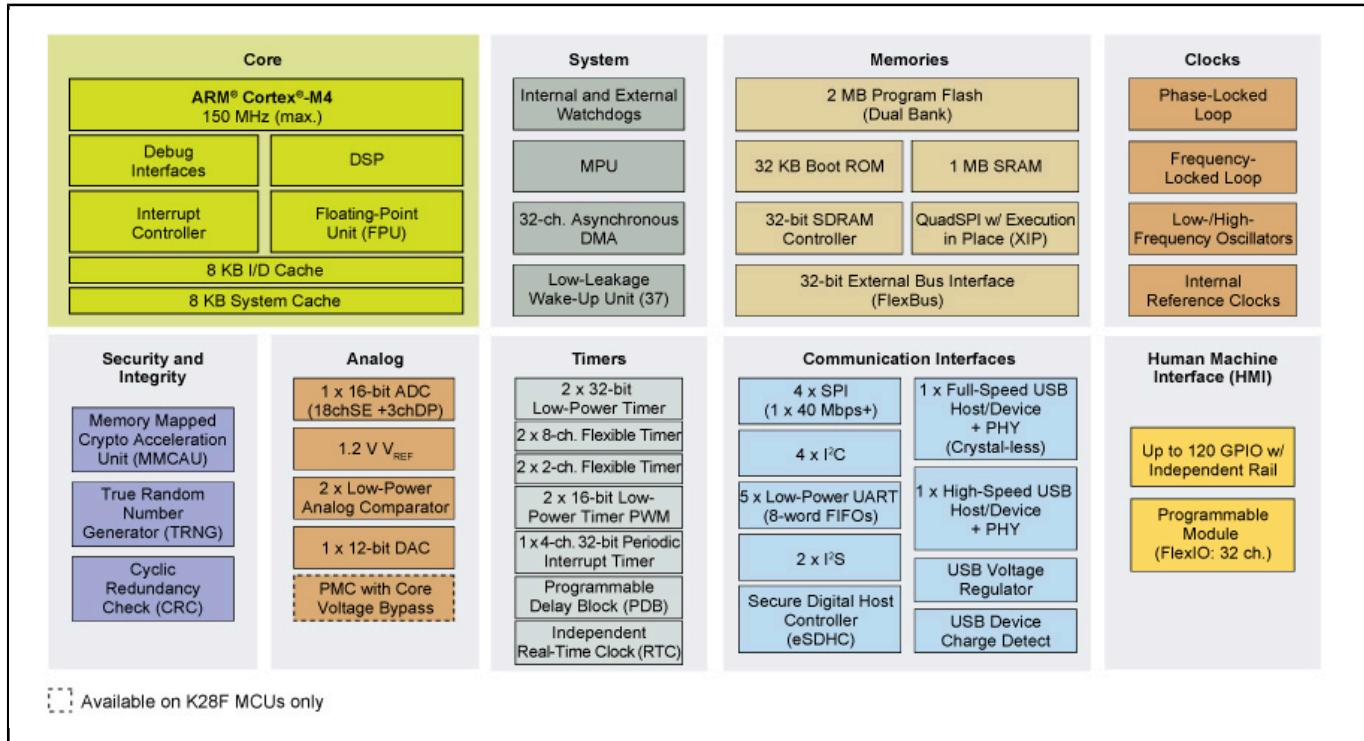
- Highly integrated with two I2S interfaces, two USB Controllers (High-Speed with integrated High-Speed PHY and Full-Speed) and mainstream analog peripherals
- Expandable through a 32-bit SDRAM memory controller and QuadSPI interface supporting eXecution-In-Place (XiP)
- Enabling secure content using a True Random Number Generator, Cyclic Redundancy Check, Memory Mapped Cryptographic Acceleration Unit

Input supply voltage: 1.71V to 3.6V + separate VBAT domain

Packages: 169 MAPBGA (9x9mm, 0.65mm pitch)

Evaluation / Development platform: [FRDM-K28F](#)

## Kinetis K27/K28 USB MCUs Block Diagram Block Diagram



View additional information for Kinetis® K27-150 MHz, 2x USB, 2MB Flash, 1MB SRAM Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core.

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.