



Kinetis® K64-120 MHz, 256 KB SRAM Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core

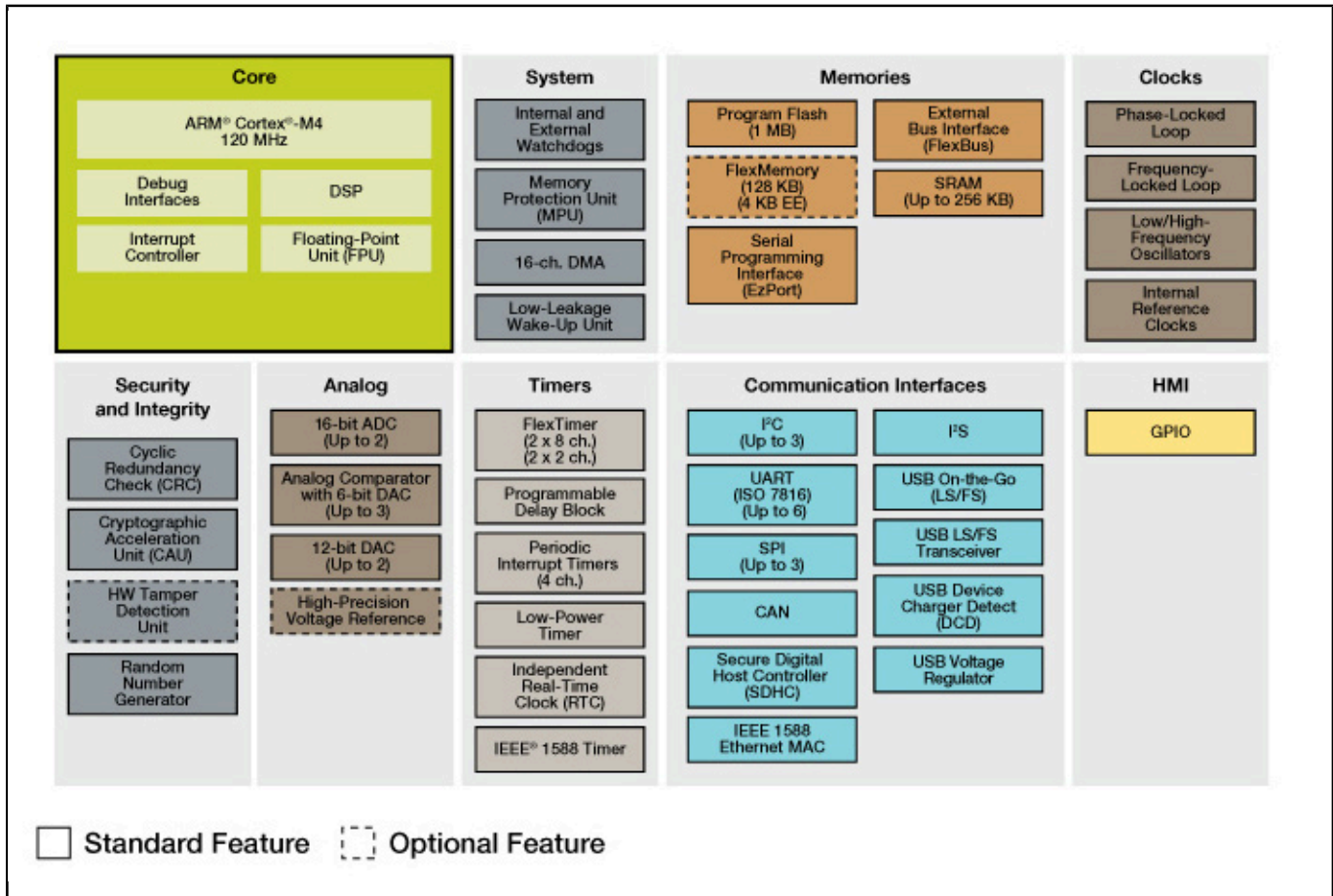
K64_120

Last Updated: Jul 4, 2022

The Kinetis® K series MCU portfolio offers the broadest selection of pin, peripheral- and software-compatible MCU families based on the Arm® Cortex®-M4 core. The Kinetis K64 120 MHz MCUs feature:

- IEEE® 1588 Ethernet MCUs
- Optimization for applications requiring large memory densities and low-power processing efficiency
- Crystal-less USB design for reduced system cost and board space
- The comprehensive enablement and scalability of the Kinetis portfolio

Kinetis K63/K64 MCU Family Block Diagram Block Diagram



View additional information for [Kinetis® K64-120 MHz, 256 KB SRAM Microcontrollers \(MCUs\) Based on Arm® Cortex®-M4 Core](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2022 NXP B.V.