Kinetis® K80-150 MHz Advanced Security and QuadSPI Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core

K80_150

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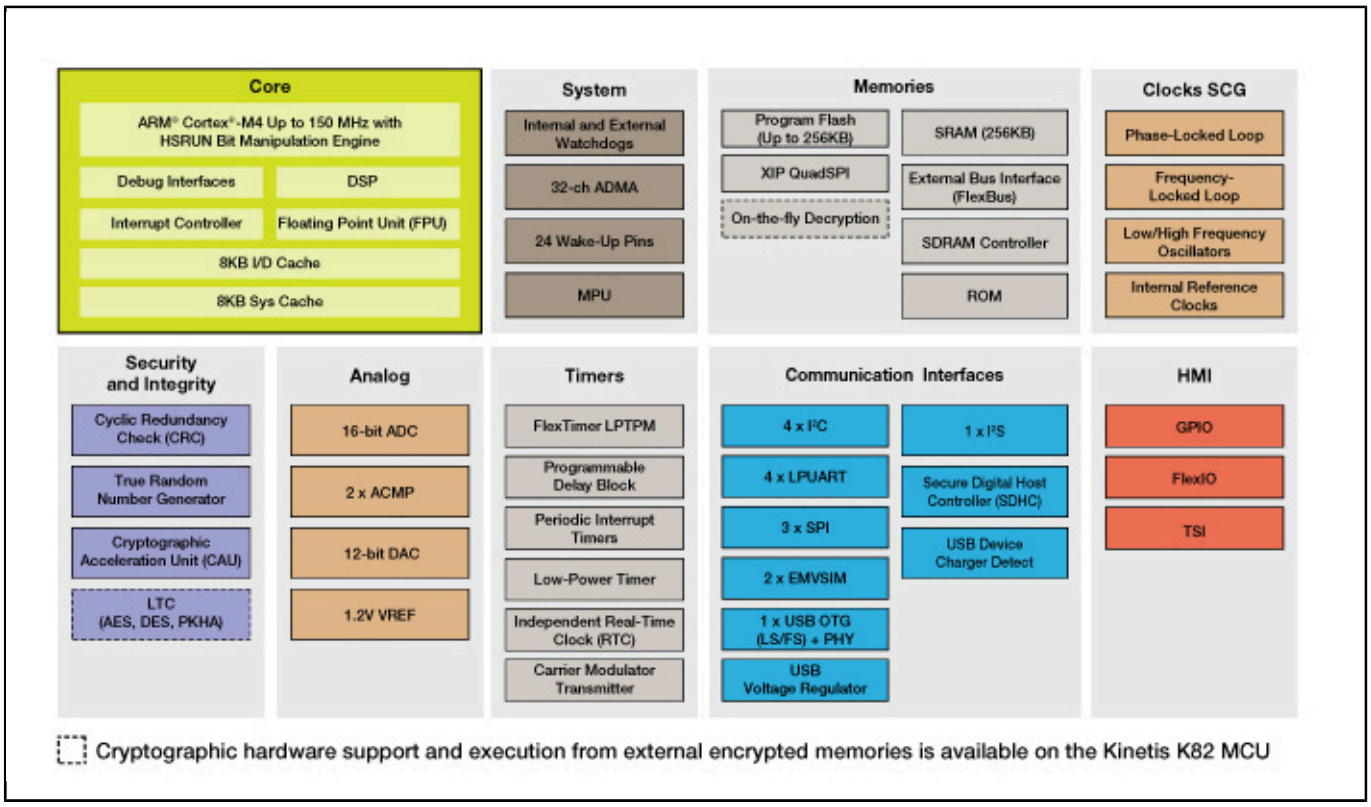
The Kinetis® K80 MCU sub-family builds upon the Kinetis MCU portfolio with advanced security capabilities including Boot ROM to support encrypted firmware updates. The K80 MCU sub-family sub-family includes QuadSPI serial flash interface and separate voltage domain for a subset of IO pins. The QuadSPI interface supports connections to non-volatile memory for data or code. The extended memory resources allow developers to enhance their embedded applications with greater capability and features. These advancements are done while maintaining a high level of compatibility with the rest of the Kinetis MCU portfolio.

Kinetis K80/K82 MCU Block Diagram Block Diagram



View additional information for Kinetis® K80-150 MHz Advanced Security and QuadSPI Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core.

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