



# **Kinetis K81-150 MHz HW Cryptographic Co-Processor, Anti-Tamper and QuadSPI Microcontrollers (MCUs) Based on Arm<sup>®</sup> Cortex<sup>®</sup>-M4 Core**

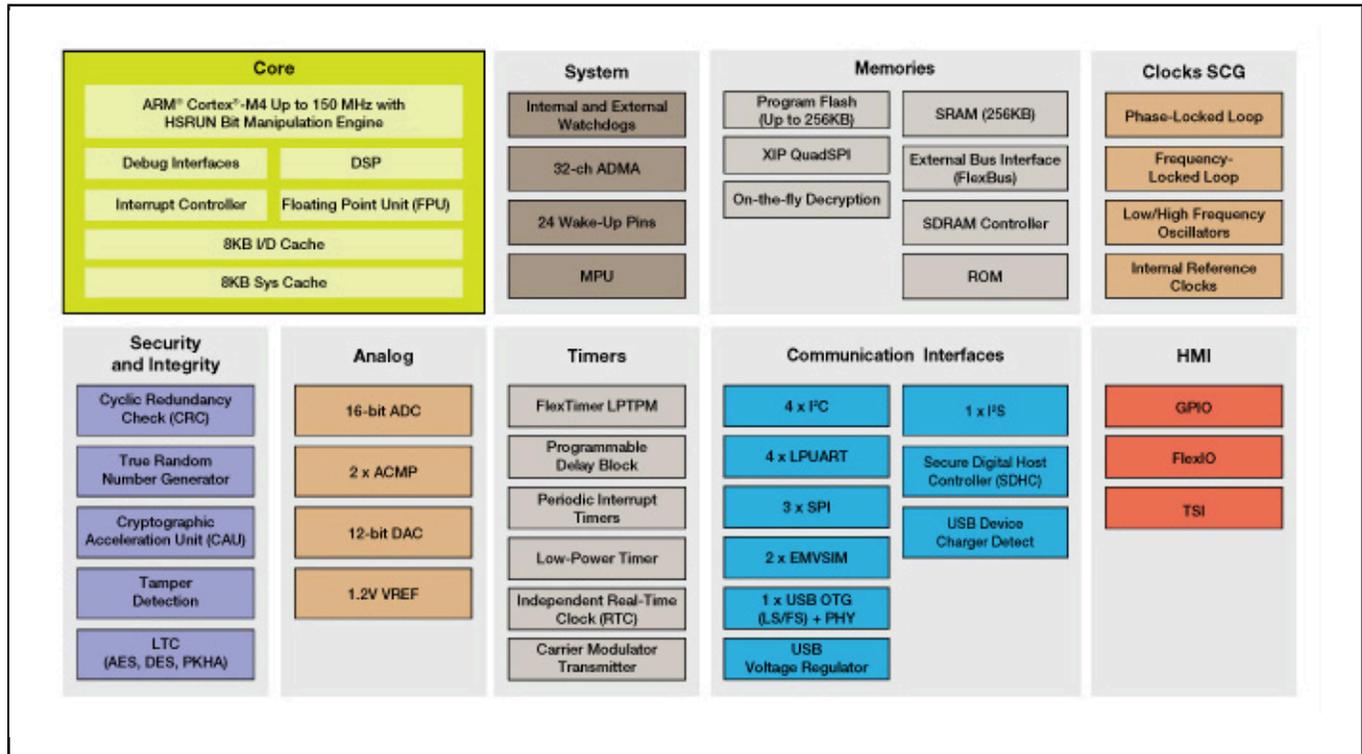
## **K81\_150**

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The Kinetis<sup>®</sup> K81 MCU extends the Kinetis MCU portfolio with advanced security capabilities including anti-tamper peripheral, boot ROM to support encrypted firmware updates, automatic decryption from external serial flash memory, AES acceleration, and hardware support for public key cryptography. The K81 MCU can be used to meet the security standards for point-of-sale (PoS) applications.

Please contact your local NXP representative to download the K81 Security Data sheet and Reference Manual documents (under NDA).

# Kinetis K81\_150 MCU Block Diagram Block Diagram



View additional information for [Kinetis K81-150 MHz HW Cryptographic Co-Processor, Anti-Tamper and QuadSPI Microcontrollers \(MCUs\) Based on Arm® Cortex®-M4 Core.](#)

Note: The information on this document is subject to change without notice.

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