



KE1xZ: Arm® Cortex®-M0+ 5V Main Stream MCU with NXP Touch and CAN Control

KE1xZ

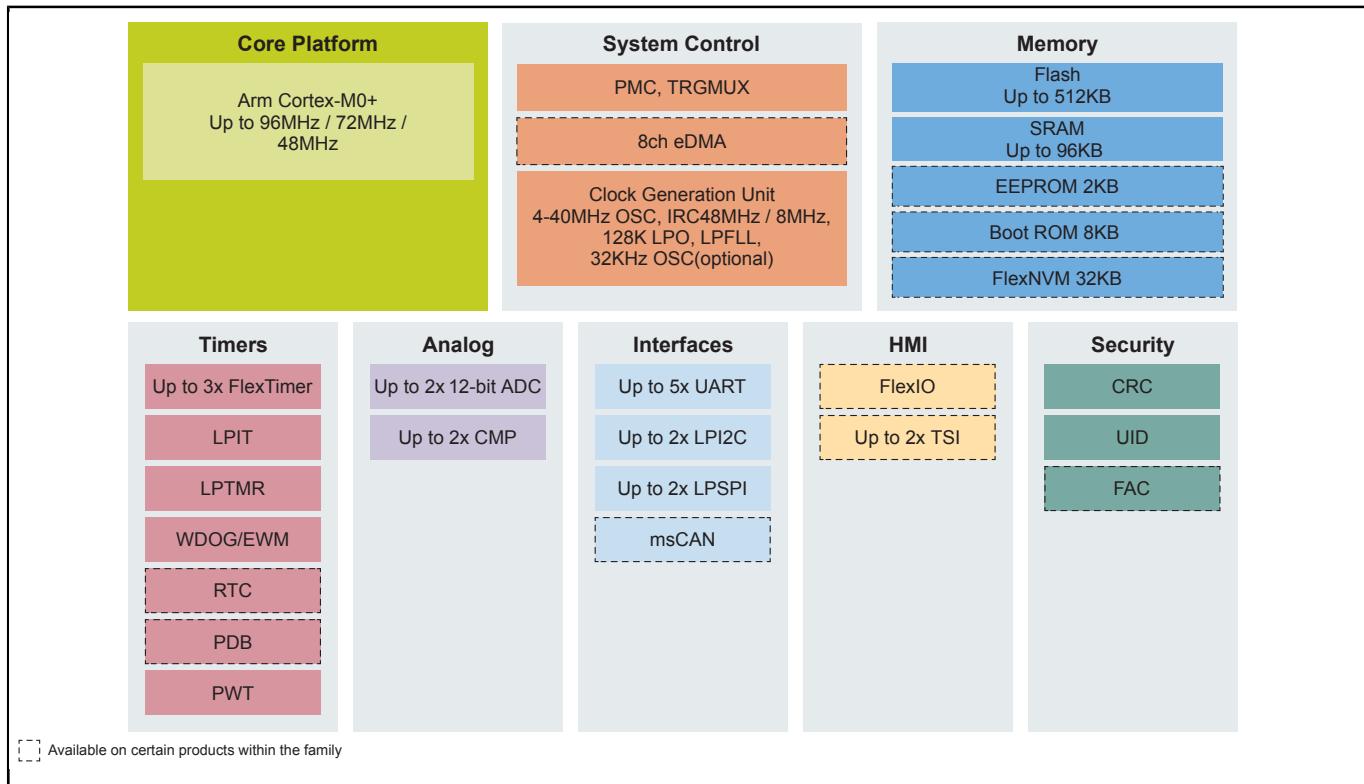
Last Updated: Jan 7, 2026

KE1xZ MCUs are based on the Arm®Cortex®-M0+ core, running up to 96 MHz. These MCUs support up to 512 KB flash, 96 KB RAM, and a complete set of analog/digital features. The robust TSI module with up to 50 channels provides a high level of stability and accuracy to any HMI system. The 1-Msps ADC and FlexTimer modules provide an ideal solution for BLDC motor-control systems.

The MSCAN IP is ideal for the CAN node control and auto after car market applications.

This device is fully supported by NXP's [MCUXpresso Software and Tools](#), a comprehensive and cohesive set of free software development tools for Kinetis, LPC and i.MX RT microcontrollers. MCUXpresso SDK also includes project files for Keil MDK and IAR Embedded Workbench for Arm.

Kinetis KE1xZ MCU Family Block Diagram



View additional information for [KE1xZ: Arm® Cortex®-M0+ 5V Main Stream MCU with NXP Touch and CAN Control](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.