



# PF5030 Safety PMIC Evaluation Board

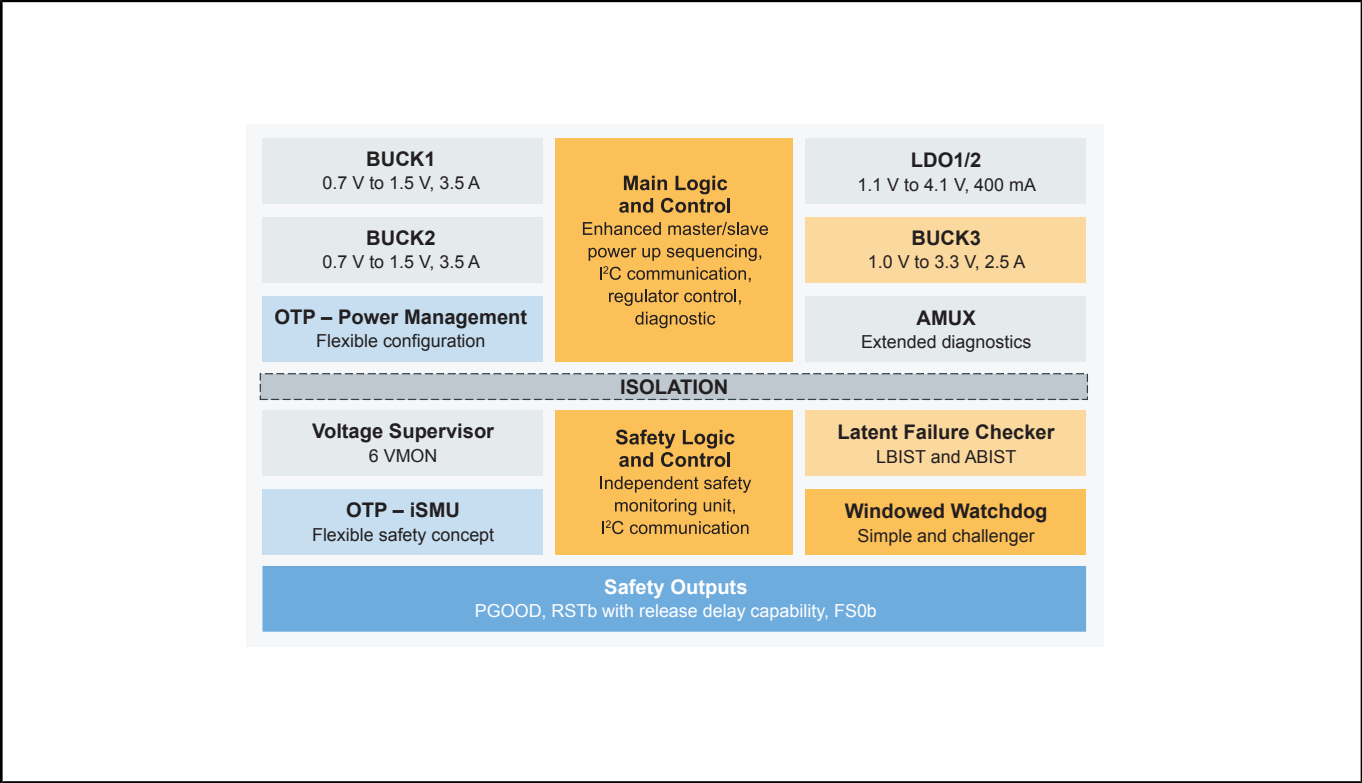
## KITPF5030FRDMEVM

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The KITPF5030FRDMEVM provides easy customer development of the PF5030 device family with flexibility to play with all the features of the device and make measurements on the main part of the application. All regulators are accessible through connectors. Nonuser signals, like DC/DC switcher node, are mapped on test points. The PF5030 is a power management integrated circuit (PMIC) designed for S32Z2/E2 processors, offering scalability in power and safety, ideally attached to NXP front system supply families (FS86, FS6x, other).

The KL25Z FRDM connected to the board, combined with the PF5030 NXP GUI software, allows to fully configure and control PF5030 PMIC. The board can be used in a standalone mode and controlled with an USB interface. This board is populated with a PF5030 family superset part for device evaluation. The PF5030 part soldered on the board can be programmed (OTP) two times and it is possible to test as many configurations as needed in emulation mode before and after programming. No extra tools or board are needed for device programming.

# PF5030 Block Diagram



View additional information for [PF5030 Safety PMIC Evaluation Board](#).

**Note:** The information on this document is subject to change without notice.