



# **Kinetis® KL8x-72/96 MHz Secure Ultra-Low Power Microcontrollers (MCUs) based on Arm® Cortex®-M0+ Core**

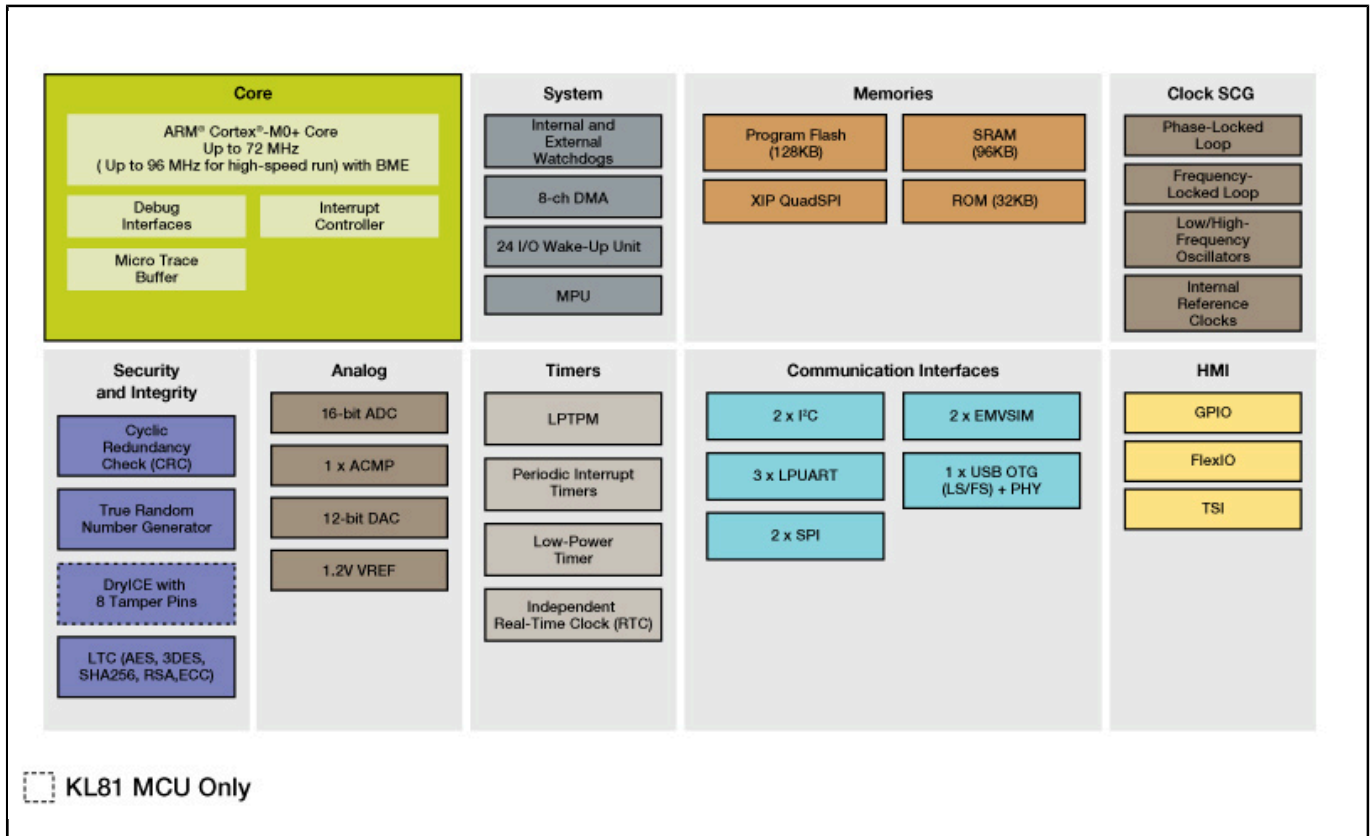
## **KL8x**

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The Kinetis® KL8x MCU expands on the Kinetis low-power MCU portfolio with rich security features including tamper detection, true random number generator and low-power trusted crypto engine supporting AES, DES, 3DES, SHA, RSA and ECC.

Please contact your local NXP representative to download the KL81 Data sheet and Reference Manual document (under NDA).

# Kinetis KL8x MCU Family Block Diagram Block Diagram



View additional information for [Kinetis® KL8x-72/96 MHz Secure Ultra-Low Power Microcontrollers \(MCUs\) based on Arm® Cortex®-M0+ Core](#).

**Note:** The information on this document is subject to change without notice.

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