Kinetis® KW21Z-2.4 GHz 802.15.4 Wireless Radio Microcontroller (MCU) based on Arm® Cortex®-M0+ Core
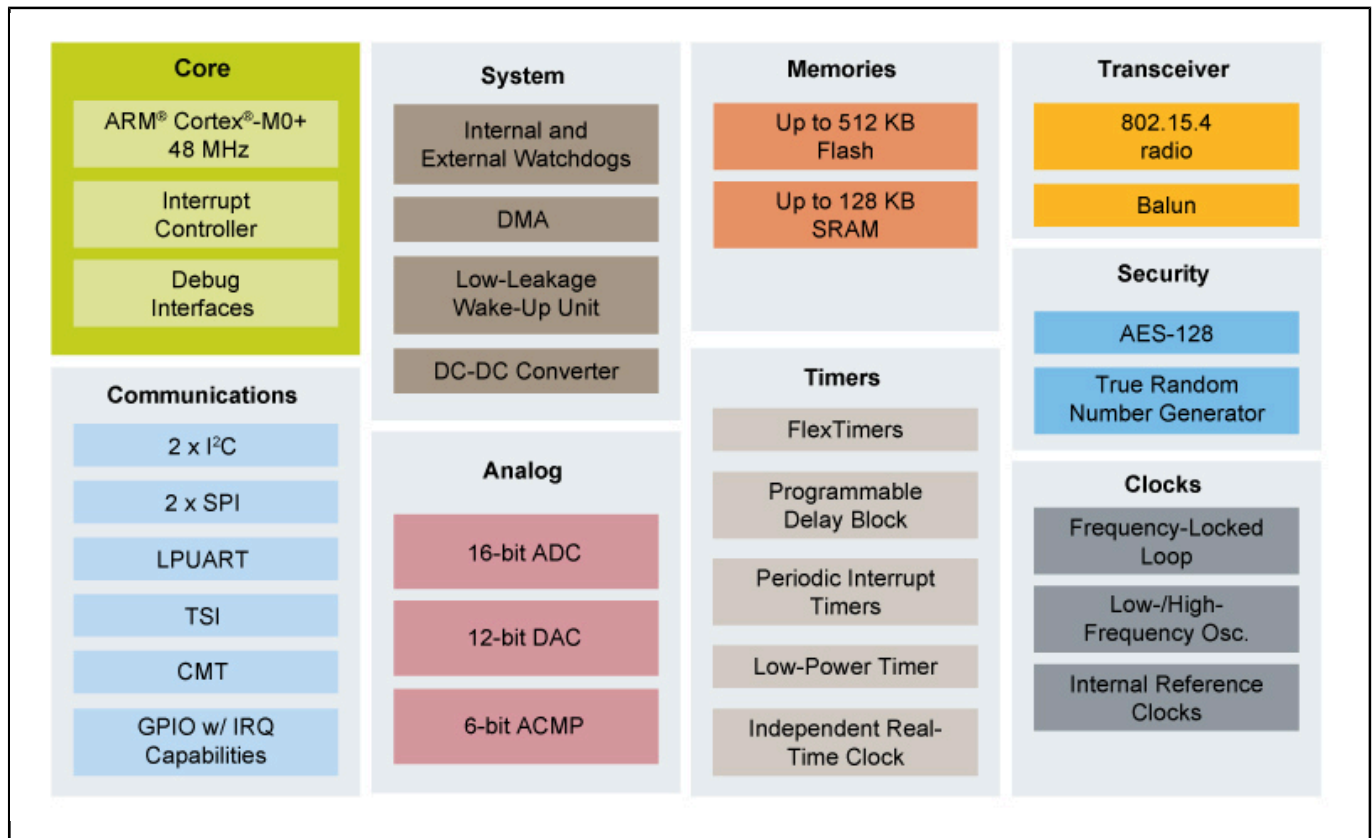
KW21Z

Last Updated: Dec 23, 2025

Note: JN5189/88T is preferred for any new Zigbee®, Thread design. No new software releases planned

The KW21Z is an ultra-low-power, highly-integrated single-chip device that enables IEEE® 802.15.4 RF connectivity for portable, extremely low-power embedded systems. Applications include portable healthcare devices, access control, security systems, smart energy and home area networks.

The KW21Z Wireless MCU integrates a 2.4 GHz transceiver supporting O-QPSK modulations, an Arm® Cortex®-M0+ CPU, up to 512 KB Flash and up to 128 KB SRAM, 802.15.4 packet processor, hardware security and peripherals optimized to meet the requirements of the target applications. The KW21Z is an ideal solution for single-chip Thread and Zigbee devices including routers and end devices as well as border routers and Coordinators when adding connectivity for communicating with the cloud.

Kinetis® W Series KW21Z MCUs Block Diagram Block Diagram

Core
- ARM® Cortex®-M0+ 48 MHz
- Interrupt Controller
- Debug Interfaces

Communications
- 2 x I²C
- 2 x SPI
- LPUART
- TSI
- CMT
- GPIO w/ IRQ Capabilities

System
- Internal and External Watchdogs
- DMA
- Low-Leakage Wake-Up Unit
- DC-DC Converter

Analog
- 16-bit ADC
- 12-bit DAC
- 6-bit ACMP

Memories
- Up to 512 KB Flash
- Up to 128 KB SRAM

Timers
- FlexTimers
- Programmable Delay Block
- Periodic Interrupt Timers
- Low-Power Timer
- Independent Real-Time Clock

Transceiver
- 802.15.4 radio
- Balun

Security
- AES-128
- True Random Number Generator

Clocks
- Frequency-Locked Loop
- Low-/High-Frequency Osc.
- Internal Reference Clocks

View additional information for Kinetis® KW21Z-2.4 GHz 802.15.4 Wireless Radio Microcontroller (MCU) based on Arm® Cortex®-M0+ Core.

Note: The information on this document is subject to change without notice.