Scalable Mainstream 32-bit Microcontroller (MCU) based on Arm® Cortex®-M3 Core

LPC1752FBD80

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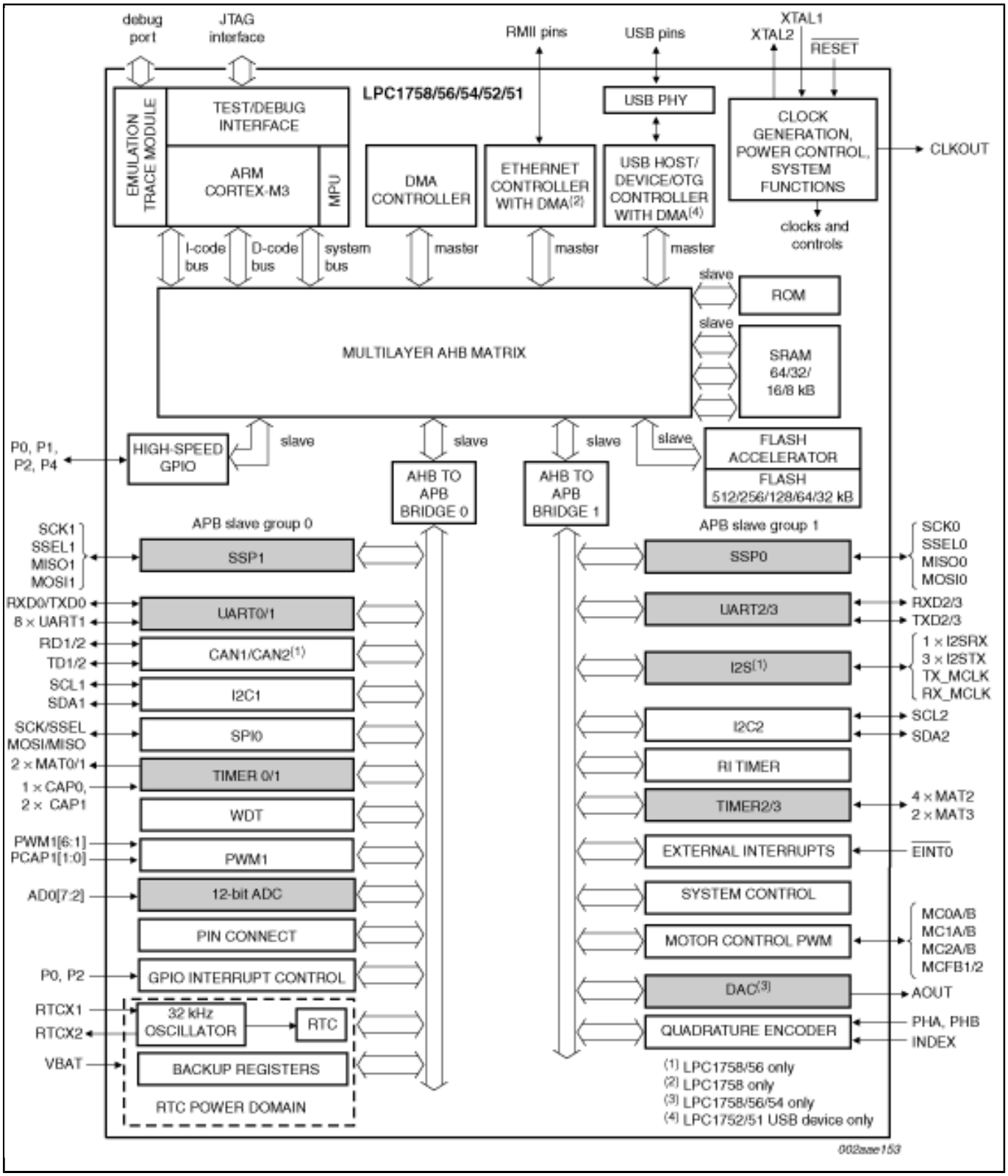
The LPC1752 is a Cortex-M3 microcontroller for embedded applications featuring a high level of integration and low power consumption at frequencies of 100 MHz. Features include 64 kB of flash memory, 16 kB of data memory, USB Device, 8-channel DMA controller, 4 UARTs, 1 CAN channels, 2 SSP, 1 SPI, 2 I2C, 8-channel 12-bit ADC, motor control PWM, Quadrature Encoder interface, 4 general purpose timers, 6-output general purpose PWM, ultra-low power Real-Time Clock with separate battery supply, and up to 52 general purpose I/O pins.

Block diagram: LPC1751FBD80, LPC1752FBD80, LPC1754FBD80, LPC1756FBD80, LPC1758FBD80 Block Diagram



View additional information for Scalable Mainstream 32-bit Microcontroller (MCU) based on Arm® Cortex®-M3 Core.

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