



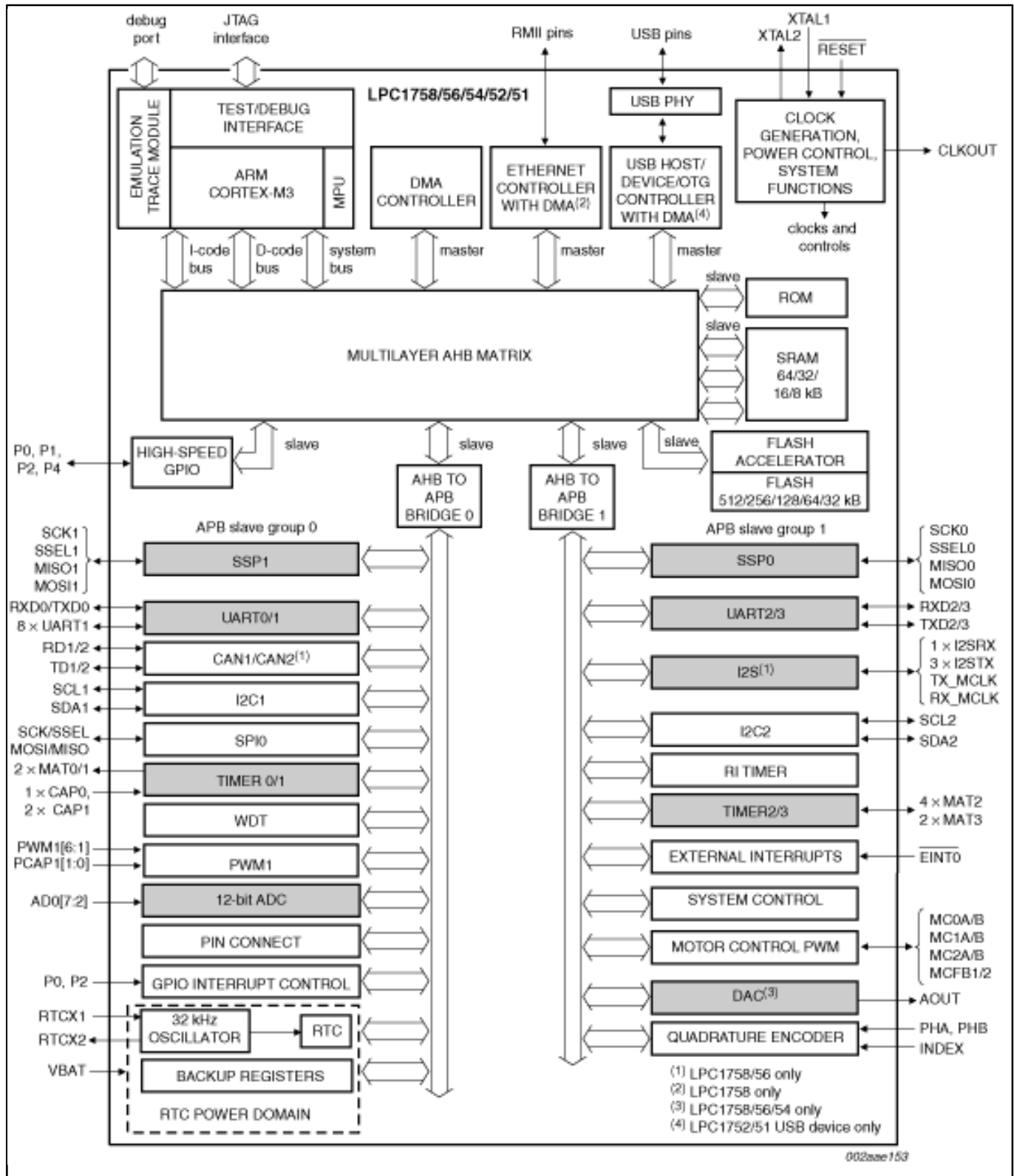
Scalable Mainstream 32-bit Microcontroller (MCU) based on Arm® Cortex®-M3 Core

LPC1756FBD80

Last Updated: Apr 8, 2022

The LPC1756 is a Cortex-M3 microcontroller for embedded applications featuring a high level of integration and low power consumption at frequencies of 100 MHz. Features include 256 kB of flash memory, 32 kB of data memory, USB Device/Host/OTG, 8-channel DMA controller, 4 UARTs, 2 CAN channels, 2 SSP, 1 SPI, 2 I²C, I²S, 8-channel 12-bit ADC, DAC, motor control PWM, Quadrature Encoder interface, 4 general purpose timers, 6-output general purpose PWM, ultra-low power Real-Time Clock with separate battery supply, and up to 52 general purpose I/O pins.

Block diagram: LPC1751FBD80, LPC1752FBD80, LPC1754FBD80, LPC1756FBD80, LPC1758FBD80 Block Diagram



Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2022 NXP B.V.