



# Single-chip 16/32-bit microcontrollers; 128/256 kB ISP/IAP flash with 10-bit ADC

## LPC2124FBD64

### Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

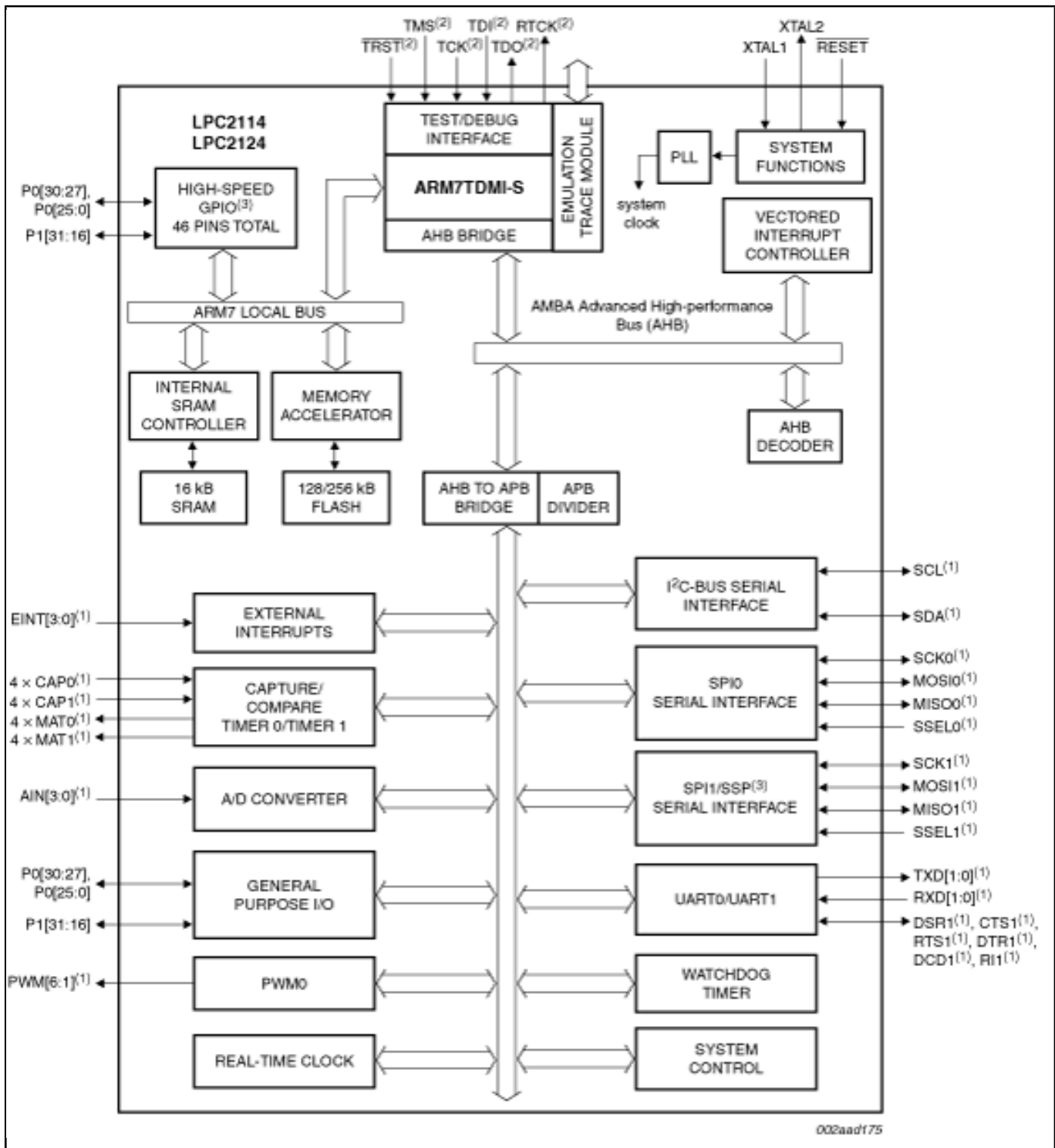
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The LPC2114/2124 are based on a 16/32-bit Arm7TDMI-S™ CPU with real-time emulation and embedded trace support, together with 128/256 kB of embedded high-speed flash memory. A 128-bit wide memory interface and a unique accelerator architecture enable 32-bit code execution at maximum clock rate. For critical code size applications, the alternative 16-bit Thumb mode reduces code by more than 30 % with minimal performance penalty.

With their compact 64-pin package, low power consumption, various 32-bit timers, 4-channel 10-bit ADC, PWM channels and 46 fast GPIO lines with up to nine external interrupt pins these microcontrollers are particularly suitable for industrial control, medical systems, access control and point-of-sale. With a wide range of serial communications interfaces, they are also very well suited for communication gateways, protocol converters and embedded soft modems as well as many other general-purpose applications.

Remark: Throughout the data sheet, the term LPC2114/2124 will apply to devices with and without the /00 or /01 suffixes. The /00 or the /01 suffix will be used to differentiate from other devices only when necessary.

# Block diagram: LPC2114FBD64, LPC2124FBD64 Block Diagram



View additional information for [Single-chip 16/32-bit microcontrollers; 128/256 kB ISP/IAP flash with 10-bit ADC.](#)

Note: The information on this document is subject to change without notice.

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